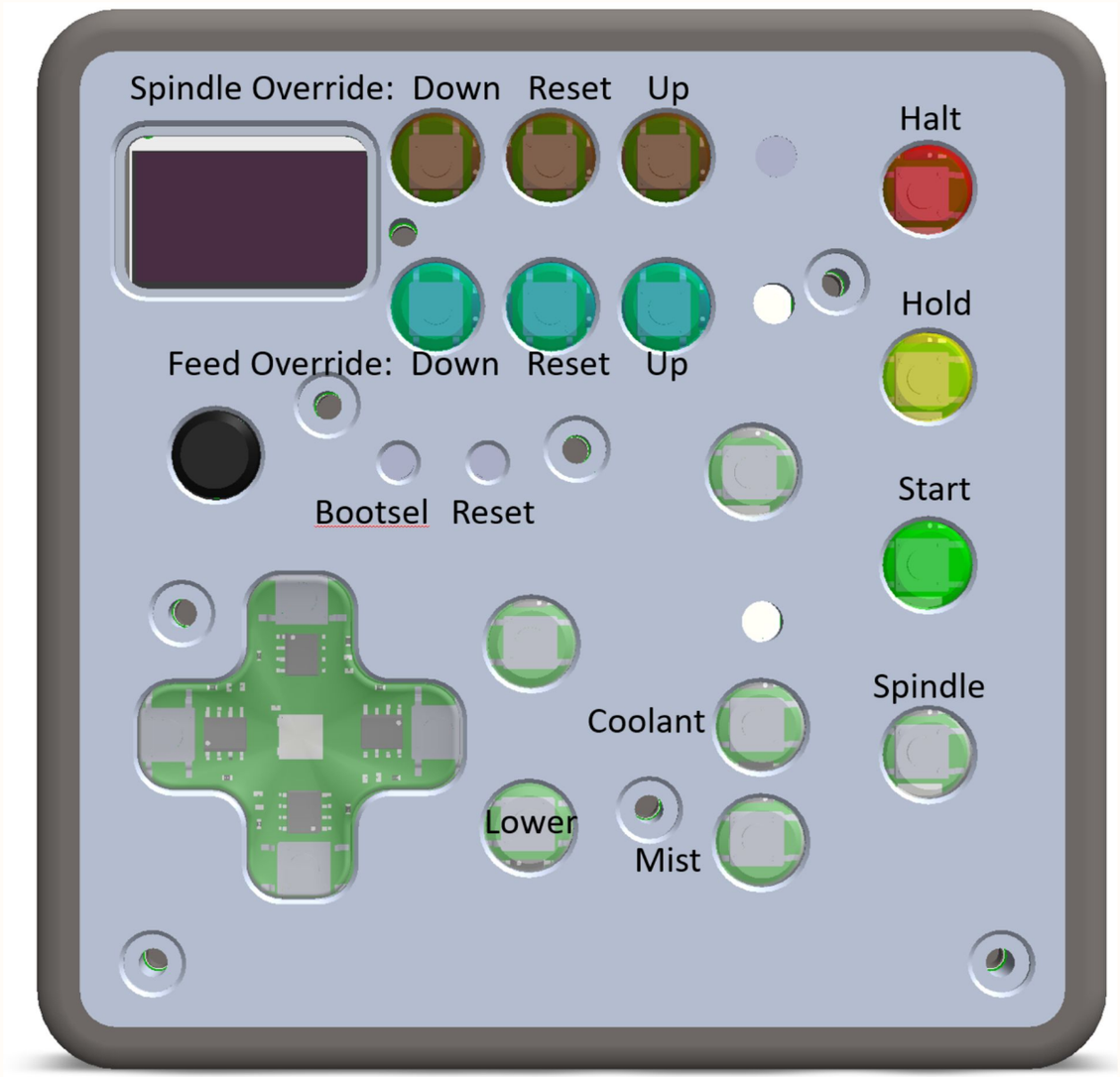
I2C Jog Controller



Mechanical

PCB
LOGO
Expatria

FID1 FID2 FID3

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

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SHEET	Title Sheet		
PROJECT	I2C_Jog_Controller.PrjPCB	REV	A4
ENGR	DM	DOCUMENT	Title Sheet.SchDoc
SIZE	B	MODIFIED	2022-04-20 11:44:19 PM
		SHEET	1 / 6

Top Level Design

No nets on these pages

U_Title Sheet
Title Sheet.SchDoc

U_Change Log
Change Log.SchDoc

U_PrintNC_BREAKOUT
I2C_Jog_Controller.SchDoc

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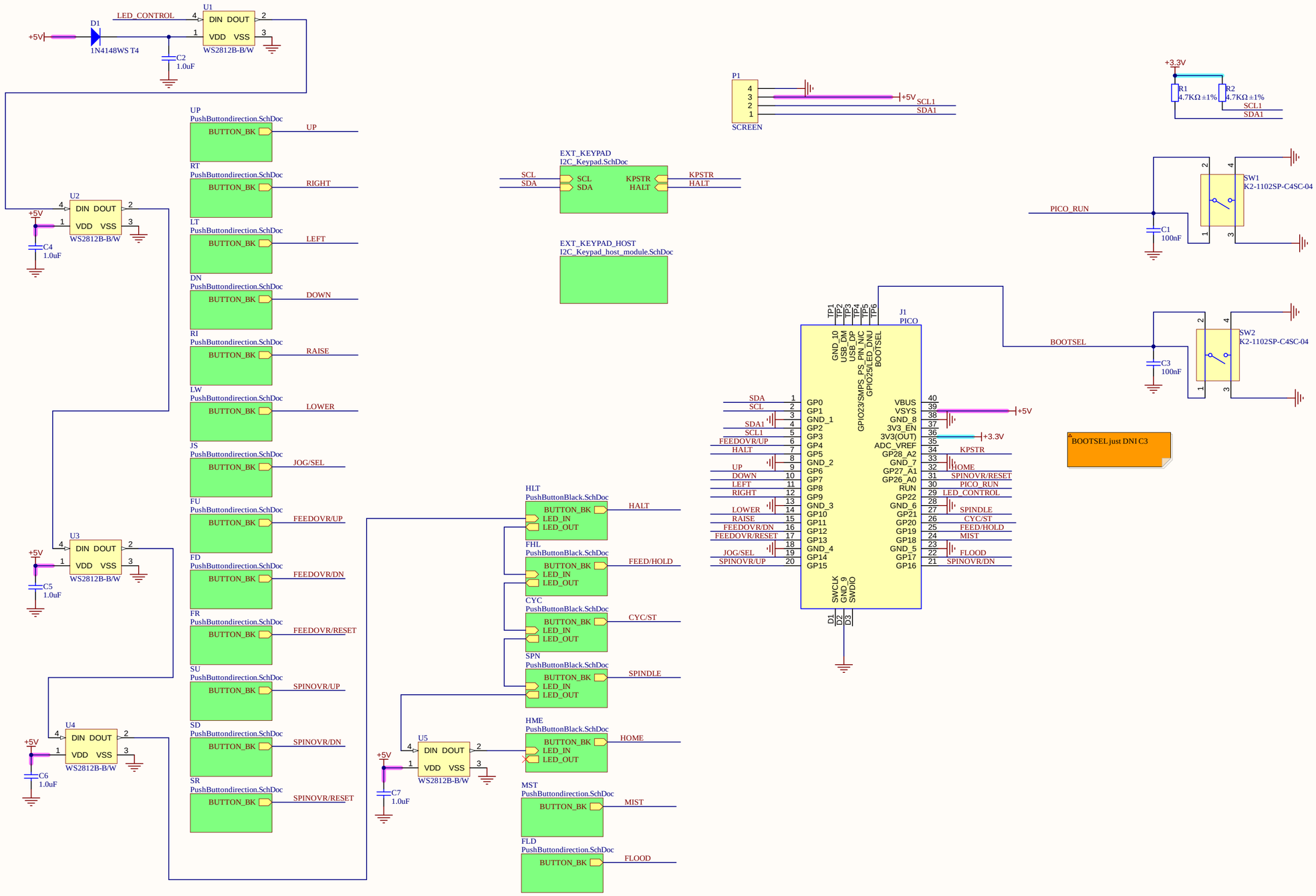
SHEET	Top Level Design		
PROJECT	I2C_Jog_Controller.PrjPCB	REV	A4
ENGR	DM	DOCUMENT	Top Level.SchDoc
SIZE	B	MODIFIED	2022-04-20 11:44:19 PM
		SHEET	2 / 6

GRBLHAL2000 JOG Controller



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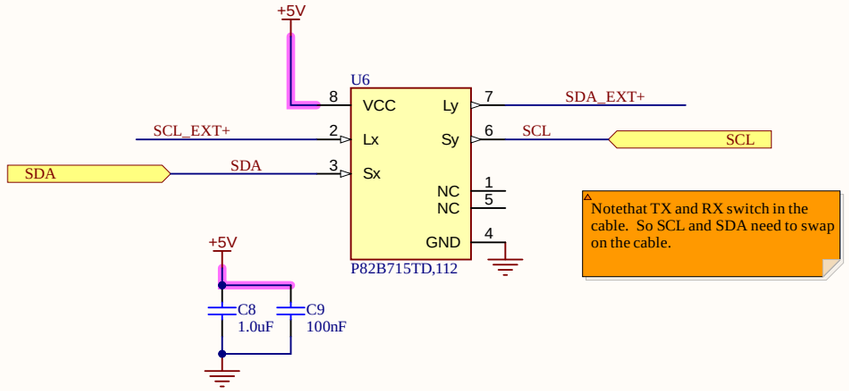
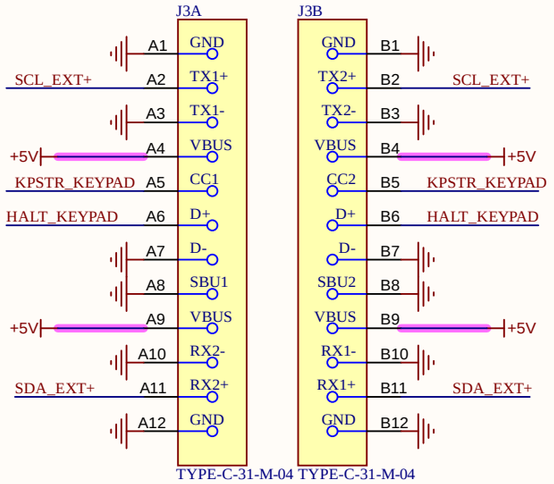
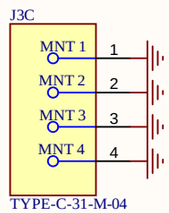
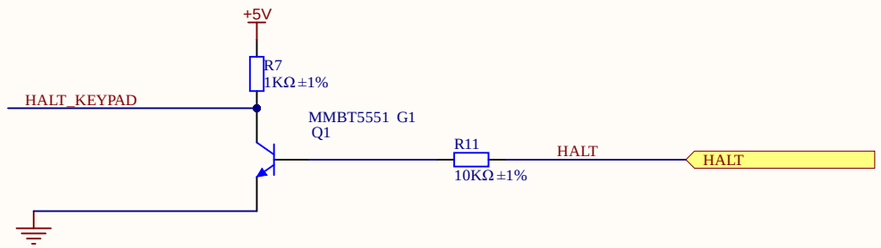
SHEET	I2C Jogger Main
PROJECT	I2C_Jog_Controller.PrjPCB
REV	A4
ENGR	DM
DOCUMENT	I2C_Jog_Controller.SchDoc
SIZE	C
MODIFIED	2022-04-20 11:44:19 PM
SHEET	3 / 6

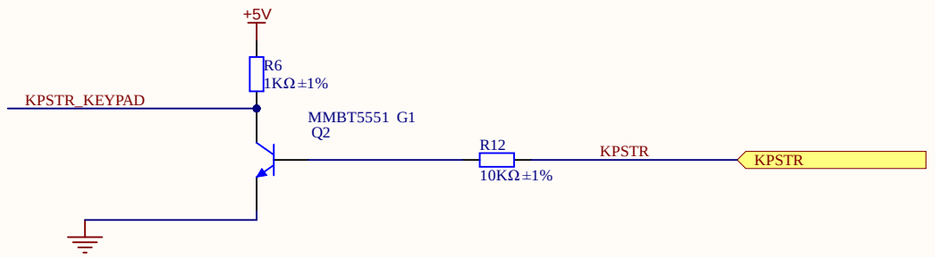


Notethat TX and RX switch in the cable. So SCL and SDA need to swap on the cable.









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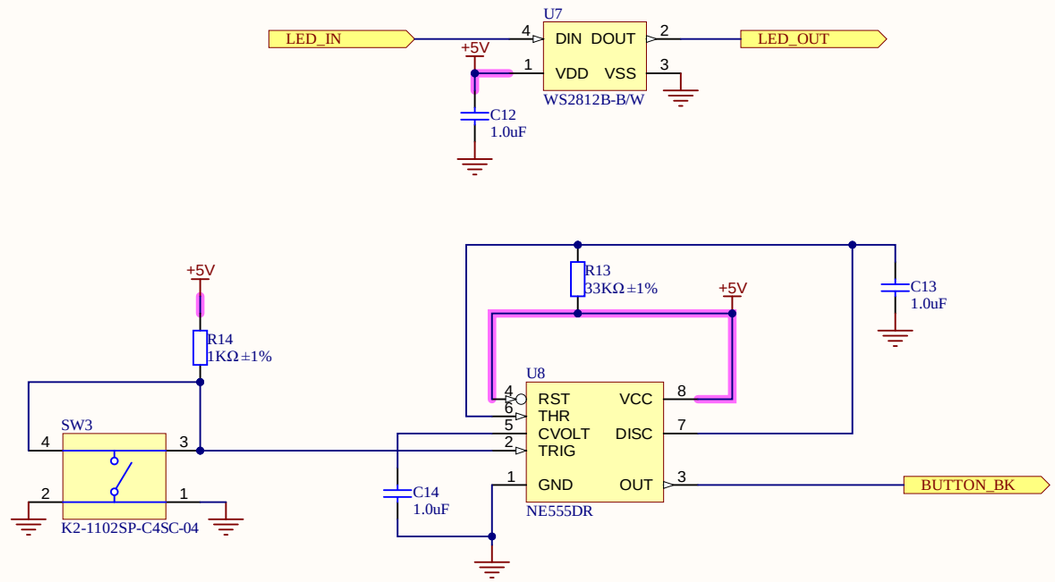
SHEET	I2C Interface
PROJECT	I2C_Jog_Controller.PrjPCB
REV	*
ENGR	*
DOCUMENT	I2C_Keypad.SchDoc
SIZE	B
MODIFIED	2022-04-20 11:44:19 PM
SHEET	4 / 6



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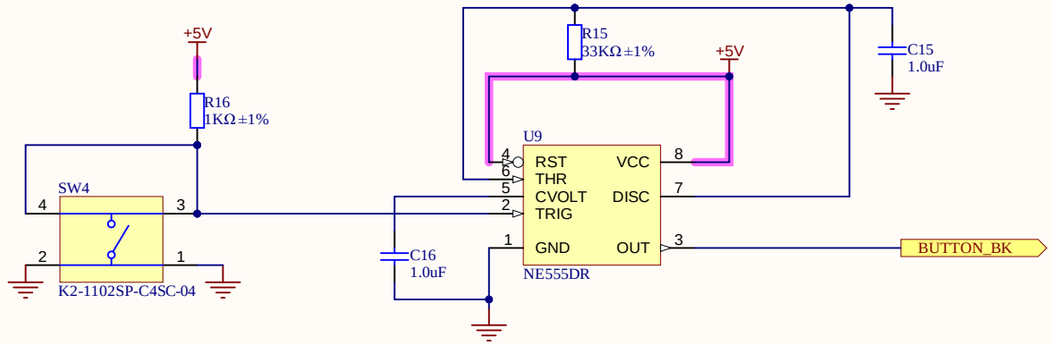
SHEET	PushButton
PROJECT	I2C_Jog_Controller.PrjPCB
REV	*
ENGR	*
DOCUMENT	PushButtonBlack.SchDoc
SIZE	B
MODIFIED	2022-04-20 11:44:19 PM
SHEET	5 / 6



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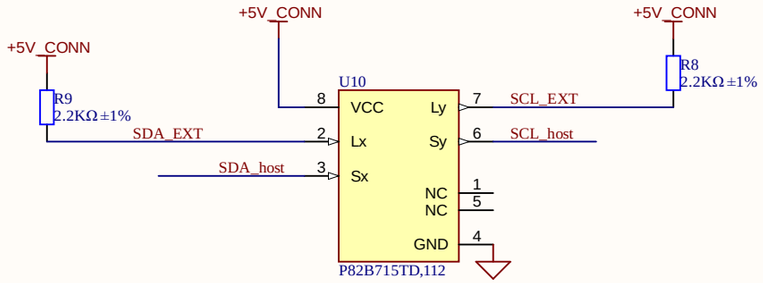
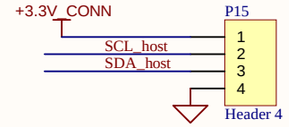
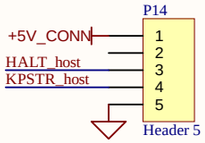
SHEET	PushButton
PROJECT	I2C_Jog_Controller.PrjPCB
REV	*
ENGR	*
DOCUMENT	PushButtondirection.SchDoc
SIZE	B
MODIFIED	2022-04-20 11:44:19 PM
SHEET	6 / 6

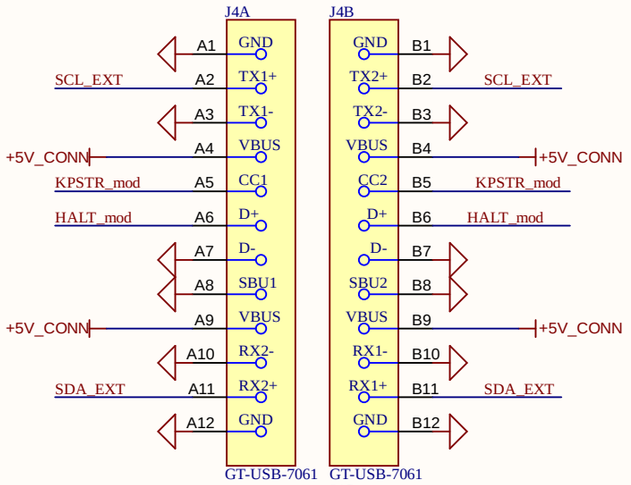
Keypad I2C Buffer

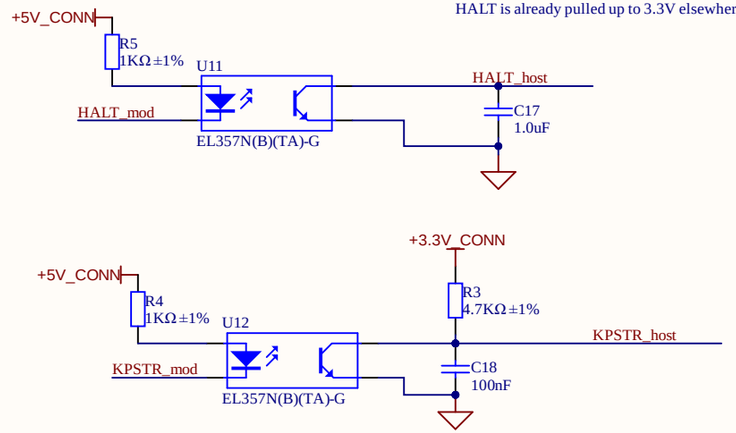
This card is for use with A5 and A6 GRBLHAL2000.











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SHEET	I2C Keypad Host
PROJECT	I2C_Jog_Controller.PrjPCB
REV	A1
ENGR	AM
DOCUMENT	I2C_Keypad_host_module.SchDoc
SIZE	B
MODIFIED	2022-04-20 11:44:20 PM
SHEET	13 of 14