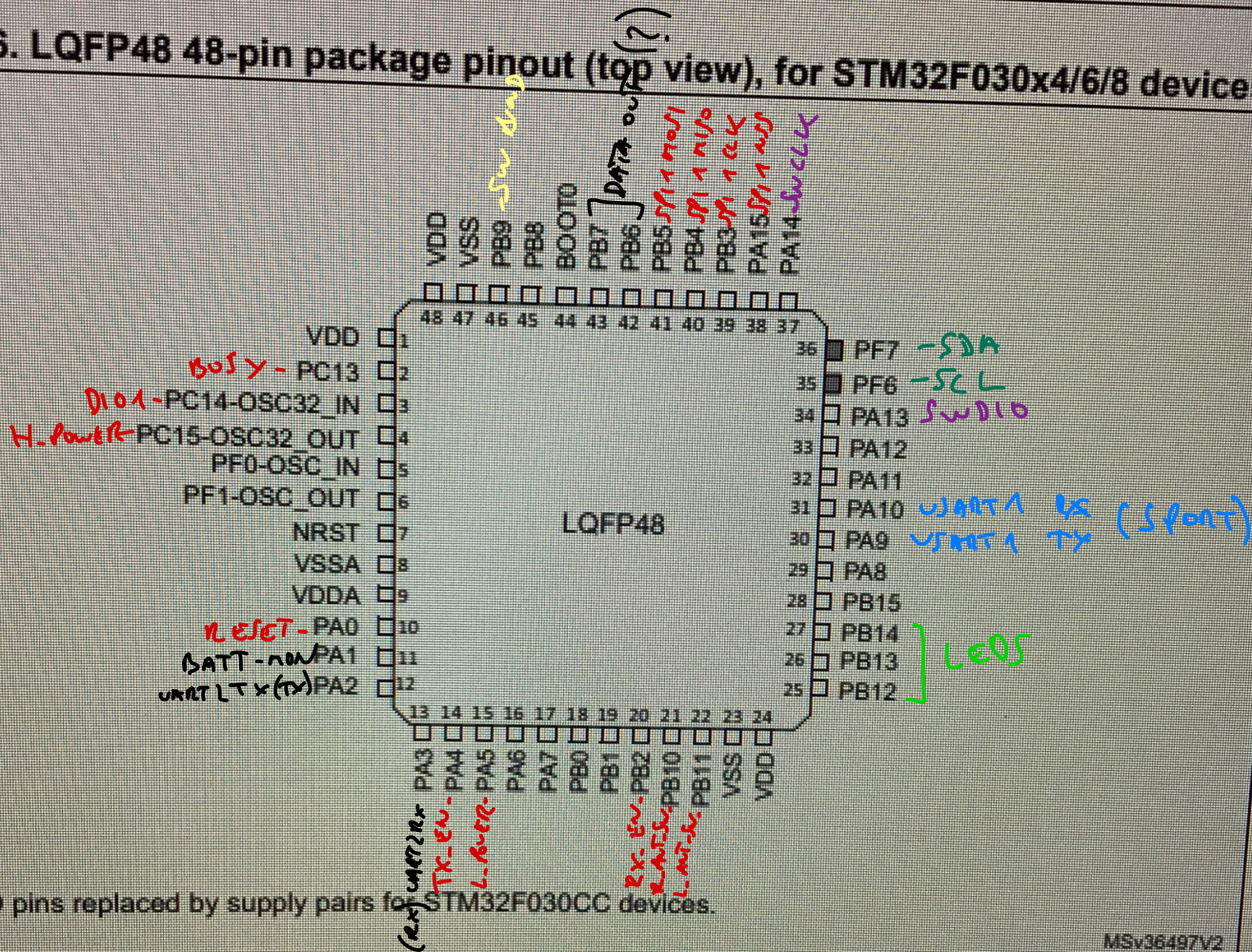
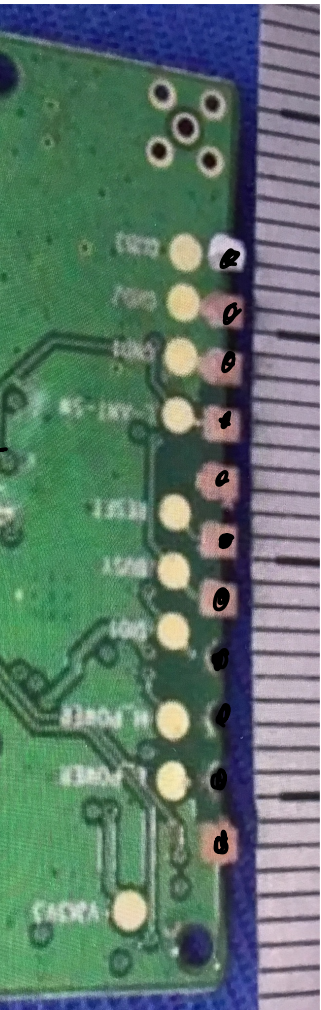
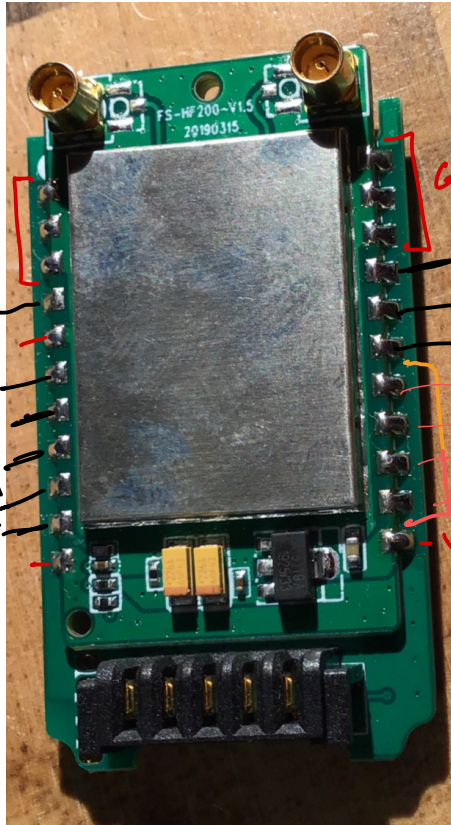


Figure 6. LQFP48 48-pin package pinout (top view), for STM32F030x4/6/8 devices





(GND)
L-ANT-SW P12M
RESET - PA0
BUSY - PL13
D01 - PL14
H-POWER PC15
L-POWER PA15
(GND)



(GND)
PB10 - V-ANT-SW
PB2 - RXEN
PA4 - TXEN
SPIA NSS - PA15
SPIA CLK - PB3
SPIA MOSI - PB5
SPIA MISO - PB4
-VCC

