Ams to a westion No-3 UPtate Gate and Reset Orate of aRU!

Poset Cate: The reset gate is responsible for the short-term memory of the network. Here is the equation of the Reset gate:

The value of Mt will rear ge from o tot.

because of the sigmoid function.

Here un and who are weight matrices

for the se reset gade.

ler me many and the equation for the gate is $u_t = \sigma(x_t \times u_u + t_{t-1} \times w_u)$

the only difference in of.

nomary is to keep around. It control.

the information that flows into the
memory.

LSTM and applu work flow,

LSTM contain a repet gate for forget and imput. Combine forget and imput gate the forget gate and imput gate to form upstate gate. BTM has to form upstate gate and coll state information update and coll state update. where GRU combine the information update and cell state information update and cell state update.

ARU is faster to in compare to ANN because aru has less activation RNN because aru has less activation