CSE460L: Assignment 4

Assignment deadline: **April 14, 2023 (11.59 PM).**

General guidelines

Draw the layout using the appropriate tool as taught in the lab, perform **DRC** and rectify all design errors (if any), submit the full screen screenshots of the design file and the simulation file **with proper discussion.**

Problem:
Draw the layout in *microwind2* for the logic function described the following equation:

$$Y = \overline{A.B + C}$$

Submission link: https://forms.gle/49U8vMPUkmRpSbsK8