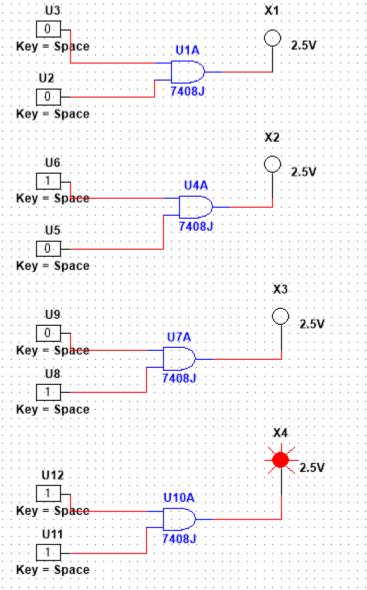
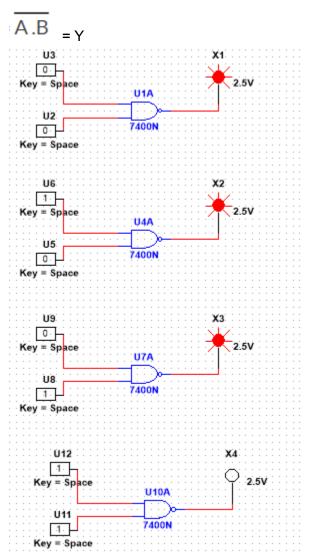
AND gate: A HIGH output of 1 only occurs when both inputs are high, if inputs arent both high then the remaining outputs are low





А	В	Υ	MyPrediction
0	0	0	0/low
1	0	0	0/low
0	1	0	0/low
1	1	1	1/high

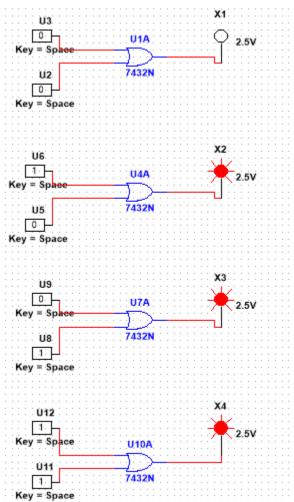
NAND gate: A low output of 0 only occurs when both inputs are high, if inputs arent both high then the remaining outputs are high



А	В	Υ	MyPrediction
0	0	1	1/high
1	0	1	1/high
0	1	1	1/high
1	1	0	0/low

OR gate: A low output of 0 only occurs when both inputs are low, if inputs arent both low then the remaining outputs are high

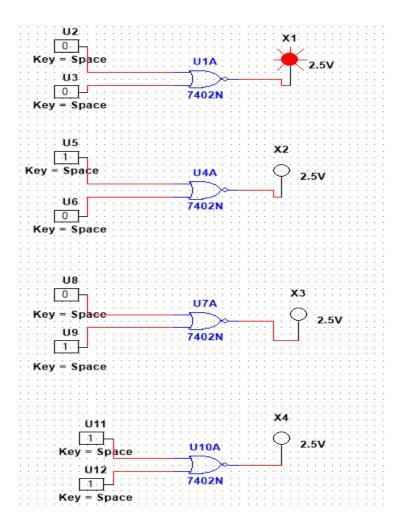
## A+B= Y



Α	В	Υ	MyPrediction
0	0	0	0/low
1	0	1	1/high
0	1	1	1/high
1	1	1	1/high

NOR gate :A high output of 1 only occurs when both inputs are low, if inputs arent both low then the remaining outputs are low





А	В	Υ	MyPrediction
0	0	1	1/high
1	0	0	0/low
0	1	0	0/low
1	1	0	0/low