

$$Y = ABCD + ABCD + ABCD$$

$$Y = ABC (D+D) + ABCD$$

$$Y = ABCC + ABCD$$

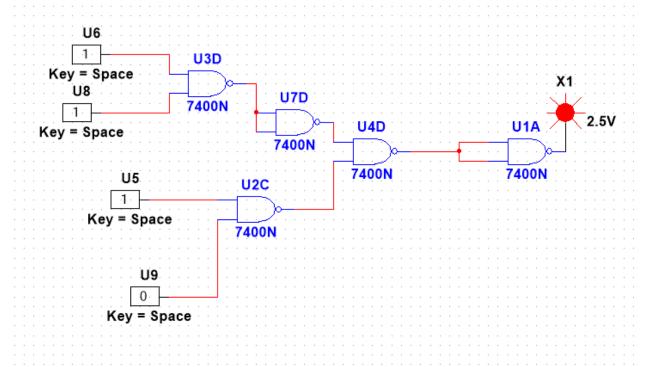
$$Y = AB(C+CD)$$

$$Y = AB(C+CD)$$

$$Y = ABC + ABD$$

$$Y = ABC + ABD$$

## REPLACED THE CIRCUIT WITH NAND GATES:



А	В	С	D	Y1
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0

1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

А	В	С	D	Y2
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0

0	1	1	0	0
0	1	1	1	0
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

А	В	С	D	Y1	Y2
0	0	0	0	0	0
0	0	0	1	0	0
0	0	1	0	0	0
0	0	1	1	0	0

0	1	0	0	0	0
0	1	0	1	0	0
0	1	1	0	0	0
0	1	1	1	0	0
1	0	0	0	0	0
1	0	0	1	0	0
1	0	1	0	0	0
1	0	1	1	0	0
1	1	0	0	1	1
1	1	0	1	1	1
1	1	1	0	1	1
1	1	1	1	0	0

Q1: In (Table 3), does Y1 equal Y2? If Y1 and Y2 are equal explain why and if they are not equal, explain why in your lab report?

## Yes, Y1 is equal to Y2,

because we compared both circuits and tested them out, the logic hasent changed which resulted in the same values for Y1 and Y2 in the truth table above which proves that Y1=Y2.