

1) Under what input conditions is the output of a 2 input OR Gate Low?

*Based on the tables shown in my EMT 1250L Exp. 1 pdf, we get a low OR gate output when both inputs are low.

2) Under what input conditions is the output of a 2 input AND Gate High?

*Based on the tables shown in my EMT 1250L Exp. 1 pdf, we get a high AND gate output when both inputs are high.

3) If one input of a 3 input OR Gate was accidentally connected to Vcc (shorted to 5V), how would the output of the OR Gate react no matter what the other two input levels might be?

*Whenever any input is high, then the output will always be high no matter what the other two input levels might be

4) If one input to a three input And gate was accidentally connected to Ground (shorted to Ground 0V), how would the Output of the And Gate react no matter what the other two Input levels might be.

The out put of the AND gate will be low (0) regardless of the other 2 input levels, because inorder for an AND gate to be high all inputs must be high/(1)