

Assignment 1

1. True or False

- a. ALU is responsible for storing programs and data
- b. The main memory is a large number of semi conductor devices (Transistors) to store data
- c. The MAR (Memory Address Register) and the MDR (Memory Data Register) are two registers located in the main memory responsible for data transfers
- d. The PC (Program Counter) is a buffer located in the Processor for data isolation
- e. The instruction register is the first register in the Processor that gets loaded with data for a single program
- f. Address bus are typically unidirectional
- g. Control bus carries data
- h. Dedicated computer buses are more complicated
- i. Bus arbitration means that more than one module is connected on the same bus
- j. Memory received and sends addresses

2. Illustrate with graphs the difference between the single bus structure, the two bus structure, and the multiple bus structure. Show advantages and disadvantages on each type.

3. Define what is meant by Computer Structure, Computer Function