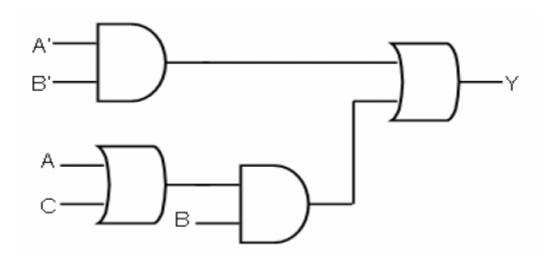
Galala University, Faculty of Computer Science and Engineering

CSE 131 Logic Design

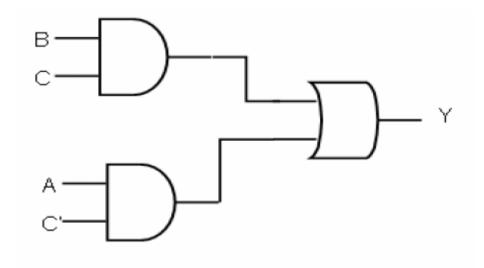
Dr Gamal Fahmy

Homework 3 sol

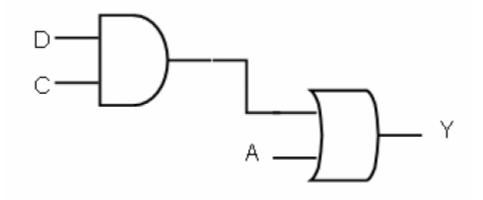
Draw the logical diagrams for the following Boolean expressions: (a)Y=A'B'+B(A+C).



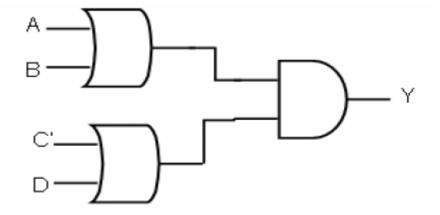
(b)Y=BC+AC'.



(c) Y=A+CD.



(d)Y = (A+B)(C'+D).



<u>Problem: 1-34</u>

Assume a 3-input AND gate with output F and a 3-input OR gate with G output. Show the signals of the outputs F and G as functions of the three inputs ABC. Use all 8 possible combinations of ABC.

