# **CSE 230**

# Assembly for 8086 Lecture 6

# 80 x 86 Assembly Programming

Some slides are taken from the Mazidi book slides, Brey book and from Dr. Ali Ziya Alkar (Ph.D University of Colorado, Boulder)

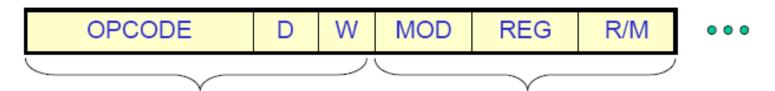
### **Assembly Language**

- There is a one-to-one relationship between assembly and machine language instructions
- What is found is that a compiled machine code implementation of a program written in a high-level language results in inefficient code
  - More machine language instructions than an assembled version of an equivalent handwritten assembly language program
- Two key benefits of assembly language programming
  - It takes up less memory
  - It executes much faster

### Languages in terms of applications

- One of the most beneficial uses of assembly language programming is real-time applications.
- Real time means the task required by the application must be completed before any other input to the program that will alter its operation can occur
- For example the device service routine which controls the operation of the floppy disk drive is a good example that is usually written in assembly language
- Assembly language not only good for controlling hardware devices but also performing pure software operations
  - searching through a large table of data for a special string of characters
  - Code translation from ASCII to EBCDIC
  - Table sort routines
  - Mathematical routines
- Assembly language: perform real-time operations
- High-level languages: Those operations mostly not critical in time.

## Converting Assembly Language Instructions to Machine Code



- An instruction can be coded with 1 to 6 bytes
- Byte 1 contains three kinds of information:
  - Opcode field (6 bits) specifies the operation such as add, subtract, or move
  - Register Direction Bit (D bit)
    - Tells the register operand in REG field in byte 2 is source or destination operand
      - 1:Data flow to the RFG field from R/M
      - 0: Data flow from the REG field to the R/M
  - Data Size Bit (W bit)
    - Specifies whether the operation will be performed on 8-bit or 16-bit data
      - 0: 8 bits
      - 1: 16 bits

#### Byte 2 has two fields:

- Mode field (MOD) 2 bits
- Register field (REG) 3 bits
- Register/memory field (R/M field) 2 bits

### **Continued**

REG field is used to identify the register for the first operand

REG	W = 0	W = 1
000	AL	AX
001	CL	CX
010	DL	DX
011	BL	BX
100	AH	SP
101	СН	BP
110	DH	SI
111	ВН	DI

### Continued

2-bit MOD field and 3-bit R/M field together specify the second operand

CODE	EXPLANATION
00	Memory Mode, no displacement follows*
01	Memory Mode, 8-bit displacement follows
10	Memory Mode, 16-bit displacement follows
11	Register Mode (no displacement)

\*Except when R/M = 110, then 16-bit displacement follows

(a)

MOD = 11		EFFECTIVE ADDRESS CALCULATION				
R/M	<b>W</b> =0	W = 1	R/M	MOD = 00	MOD=01	MOD = 10
000	AL	AX	000	(BX)+(SI)	(BX)+(SI)+D8	(BX)+(SI)+D16
001	CL	CX	001	(BX) + (DI)	(BX)+(DI)+D8	(BX)+(DI)+D16
010	DL	DX	010	(BP) + (SI)	(BP)+(SI)+D8	(BP) + (SI) + D16
011	BL	BX	011	(BP) + (DI)	(BP)+(DI)+D8	(BP) + (DI) + D16
100	AH	SP	100	(SI)	(SI) + D8	(SI) + D16
101	CH	BP	101	(DI)	(DI) + D8	(DI) + D16
110	DH	SI	110	DIRECT ADDRESS	(BP) + D8	(BP) + D16
111	BH	DI	111	(BX)	(BX)+D8	(BX) + D16

### **Examples**

- MOV BL,AL
- Opcode for MOV = 100010
- We'll encode AL so
  - D = 0 (AL source operand)
- W bit = 0 (8-bits)
- MOD = 11 (register mode)
- REG = 000 (code for AL)
- R/M = 011

OPCODE	D	W	MOD	REG	R/M
100010	0	0	11	000	011

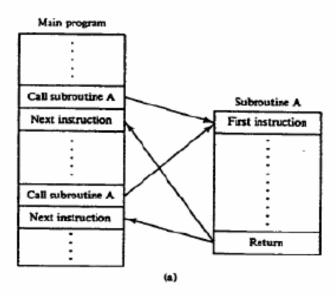
### Subroutines and Subroutine Handling Functions

✓A subroutine is a special segment of a program that can be called for execution from any point in the program

✓A RET instruction must be included at the end of the subroutine to initiate the return sequence to the main program environment

Examples. Call 1234h
Call BX
Call [BX]

Two calls
•intrasegment
•intersegment



Mnemonic	Meaning	Format	Operation	Flags Affected
CALL	Subroutine call	CALL operand	Execution continues from the address of the subroutine specified by the operand. Information required to return back to the main program such as IP and CS are saved on the stack.	None

(b)

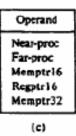


Figure 6-20 (a) Subroutine concept. (b) Subroutine call instruction. (c) Allowed operands.

## Calling a NEAR proc

- ✓ The CALL instruction and the subroutine it calls are in the same segment.
- ✓ Save the current value of the IP on the stack.
- ✓ load the subroutine's offset into IP (nextinst + offset)

Calling Program	Subroutine	Stack
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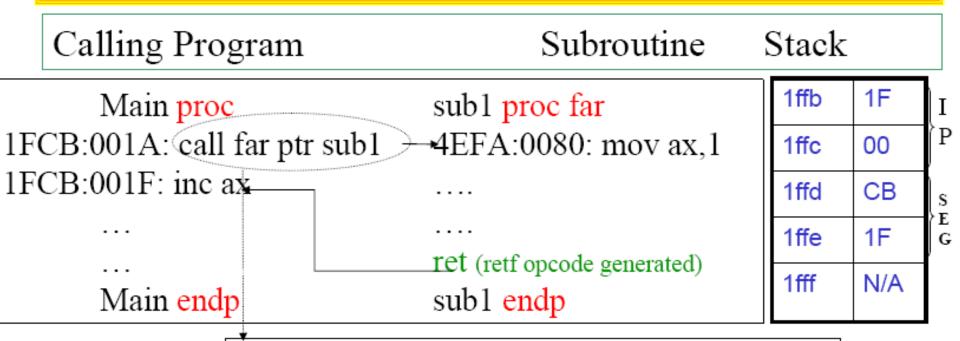
Main proc sub1 proc 001A: call sub1 0080: mov ax,1 ... ret
Main endp sub1 endp

1ffd	1D
1ffe	00
1fff	(not used)

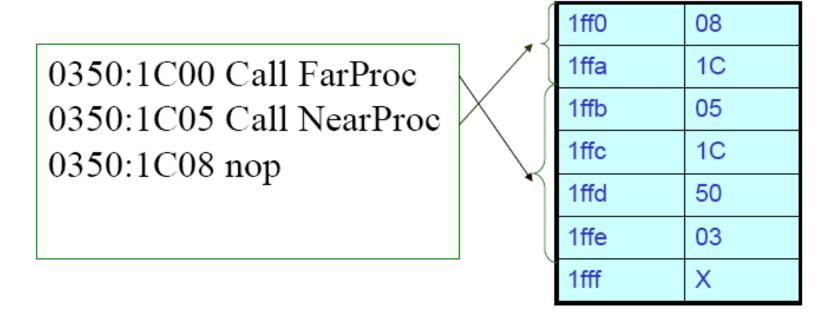
# Calling a FAR proc

- ✓ The CALL instruction and the subroutine it calls are in the "Different" segments.
- ✓ Save the current value of the CS and IP on the stack.
- ✓ Then load the subroutine's CS and offset into IP.

Opcode 8000 FA4E



### **Example on Far/Near Procedure Calls**



### **Nested Procedure Calls**

### A subroutine may itself call other subroutines.

<u>Examp</u>	<u>le:</u>	
	000 4	main proc
	000A	call subr1
	000C	mov ax,
		main endp

0	050	subr2 proc nop
00	)60	call subr3 ret subr2 endp



Q: show the stack contents at 0079?

0030	nop
0040	call subr2
	subr1 endp

subrl proc

0070	subr3 proc nop
0079 007A	nop ret subr3 endp

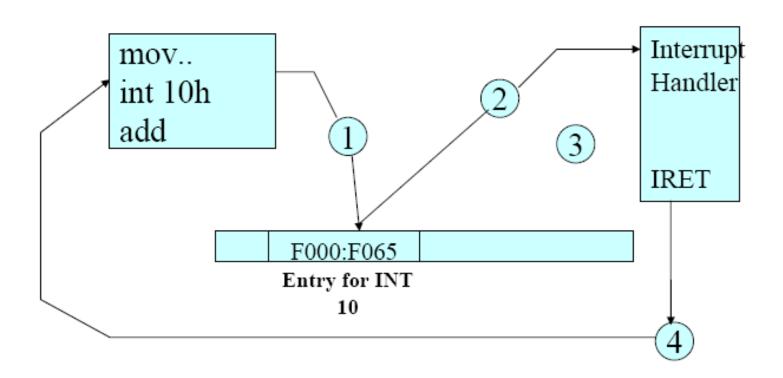
### 80x86 Interrupts

- An interrupt is an event that causes the processor to suspend its present task and transfer control to a new program called the interrupt service routine (ISR)
- There are three sources of interrupts
  - Processor interrupts
  - Hardware interrupts generated by a special chip, for ex: 8259 Interrupt Controller.
  - Software interrupts
- Software Interrupt is just similar to the way the hardware interrupt actually works!. The INT Instruction requests services from the OS, usually for I/O. These services are located in the OS.
- INT has a range 0→ FFh. Before INT is executed AH usually contains a function number that identifies the subroutine.

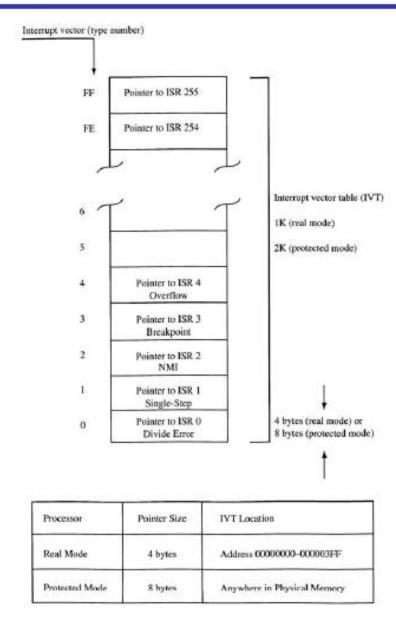
- Each interrupt must supply a type number which is used by the processor as a pointer to an interrupt vector table (IVT) to determine the address of that interrupt's service routine
- Interrupt Vector Table: CPU processes an interrupt instruction using the interrupt vector table (This table resides in the lowest 1K memory)
- Each entry in the IVT=32 bit segment+offset adress in OS, points to the location of the corresponding ISR.
- Before transferring control to the ISR, the processor performs one very important task
  - It saves the current program address and flags on the stack
  - Control then transfers to the ISR
  - When the ISR finishes, it uses the instruction IRET to recover the flags and old program address from the stack
- Many of the vectors in the IVT are reserved for the processor itself and others have been reserved by MS-DOS for the BIOS and kernel.
  - 10-1A are used by the BIOS
  - 20 3F are used by the MS-DOS kernel

### 80x86 Interrupts

 The number after the mnemonic tells which entry to locate in the table. For example INT 10h requests a video service.



# Interrupt Vector Table



### Interrupts

- There are some extremely useful subroutines within BIOS or DOS that are available to the user through the INT (Interrupt) instruction.
- The INT instruction is like a FAR call; when it is invoked
  - It saves CS:IP and flags on the stack and goes to the subroutine associated with that interrupt.
  - Format:
    - INT xx ; the interrupt number xx can be 00-FFH
  - This gives a total of 256 interrupts
  - Common Interrupts
    - INT 10h Video Services
    - INT 16h Keyboard Services
    - INT 17h Printer Services
    - INT 21h MS-DOS services
  - Before the services, certain registers must have specific values in them, depending on the function being requested.

## Some Software Interrupts

- INT 10H Function 06 (AH = 06) Scroll a screen windows.
  - Moves the data on the video display up or down. As screen is rolled the bottom is replaced by a blank line. Rows:0-24 from top, bottom: 0-79 from the left. (0,0) to (24,79). Lines scrolled can not be recovered!
  - AL = number of lines to scroll (with AL=00, window will be cleared)
  - BH = Video attribute of blank rows
  - CH, CL = Row, Column of upper left corner
  - DH, DL = Row, Column of lower right corner

00,00 00,79

12,39

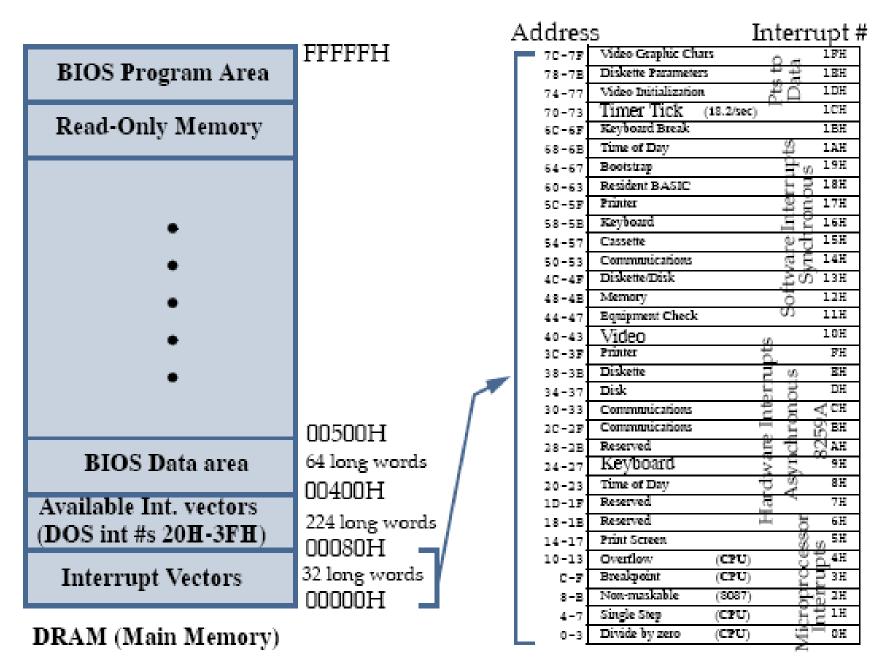
24,00 24,79

Cursor Locations

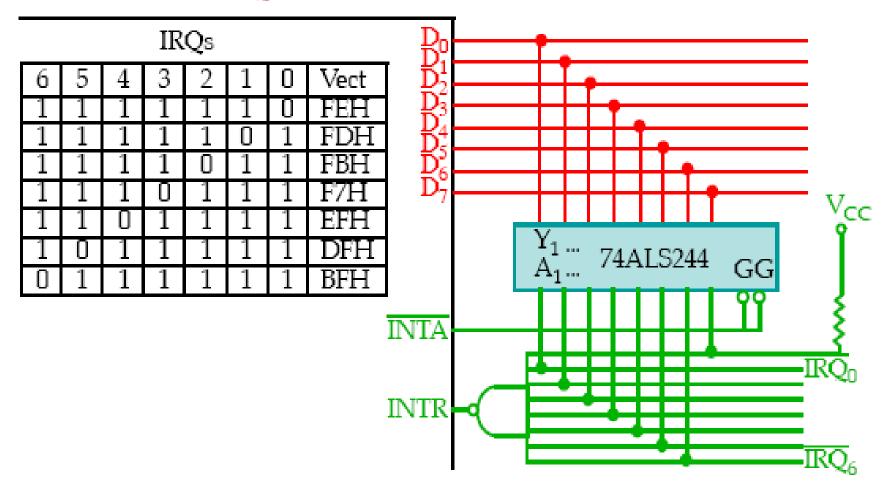
Example: Clear the screen by scrolling it upward with a normal attribute

mov ah,6h mov al,0h mov ch,0h mov cl,0h mov dh,24h mov dl,01h mov bh,7h int 10h

#### Interrupt Vectors (DOS PC)



#### Handling more than 1 IRQ

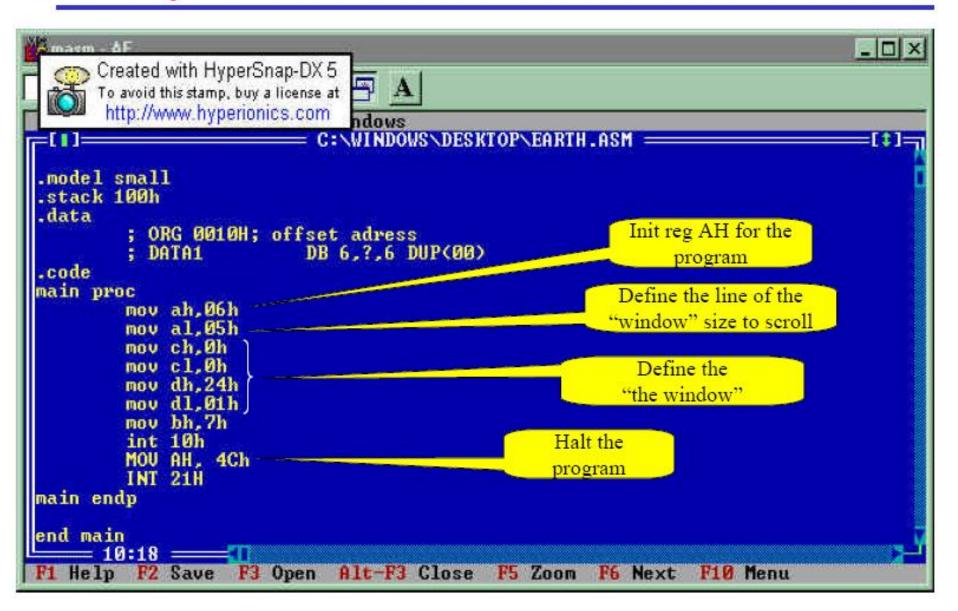


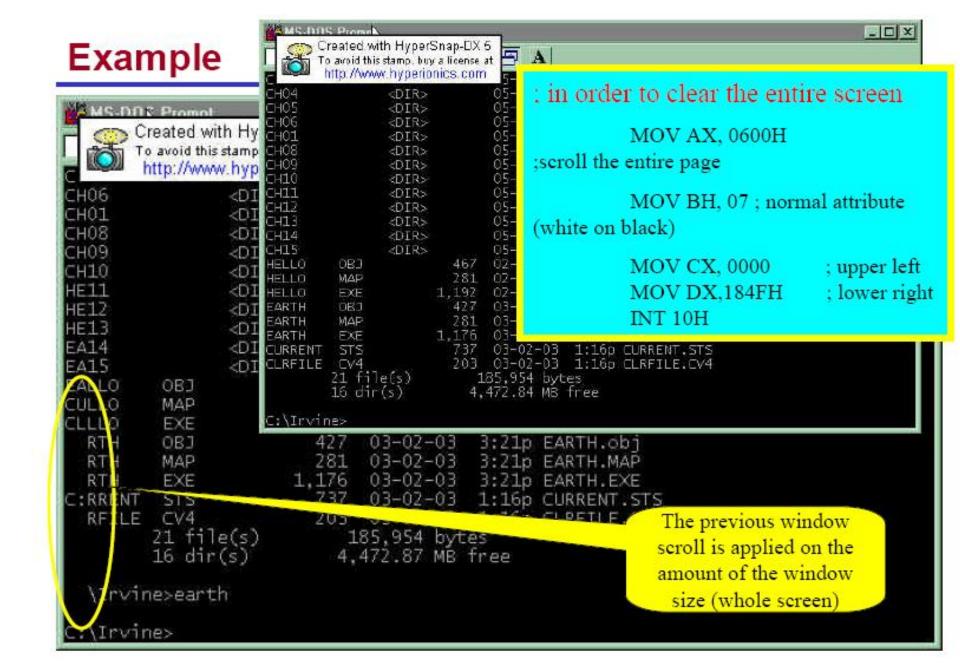
If any of  $\overline{IRQ}_{v}$  goes low, the NAND goes low requesting an interrupt.

Note that if more than one IRQ goes low, a unique interrupt vector is generated and an interrupt priority needs to be defined.

The Interrupt Vector table must be expanded to accommodate this.

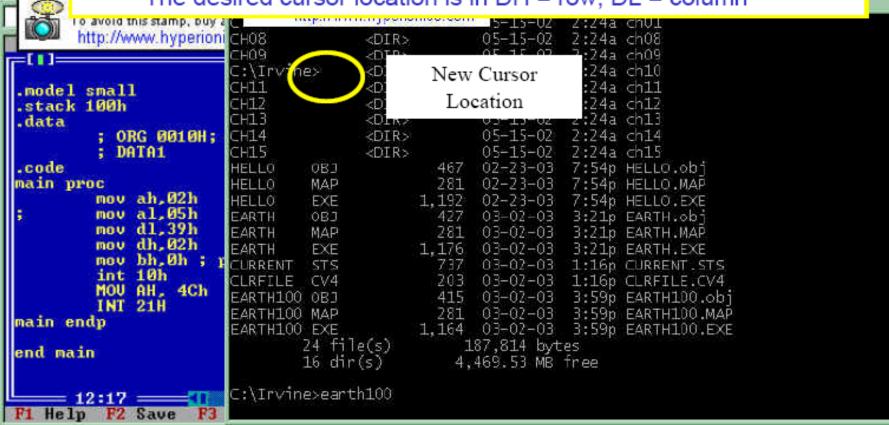
## Example Int10 06





#### Int 10 02H

- •INT 10H function 02; setting the cursor to a specific location
  - -Function AH = 02 will change the position of the cursor to any location.
  - -The desired cursor location is in DH = row, DL = column



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#### Int 10 03

#### •INT 10H function 03; get current cursor position

MOV AH, 03 MOV BH, 00 INT 10H

- •Registers DH and DL will have the current row and column positions and CX provides info about the shape of the cursor.
- •Useful in applications where the user is moving the cursor around the screen for menu selection

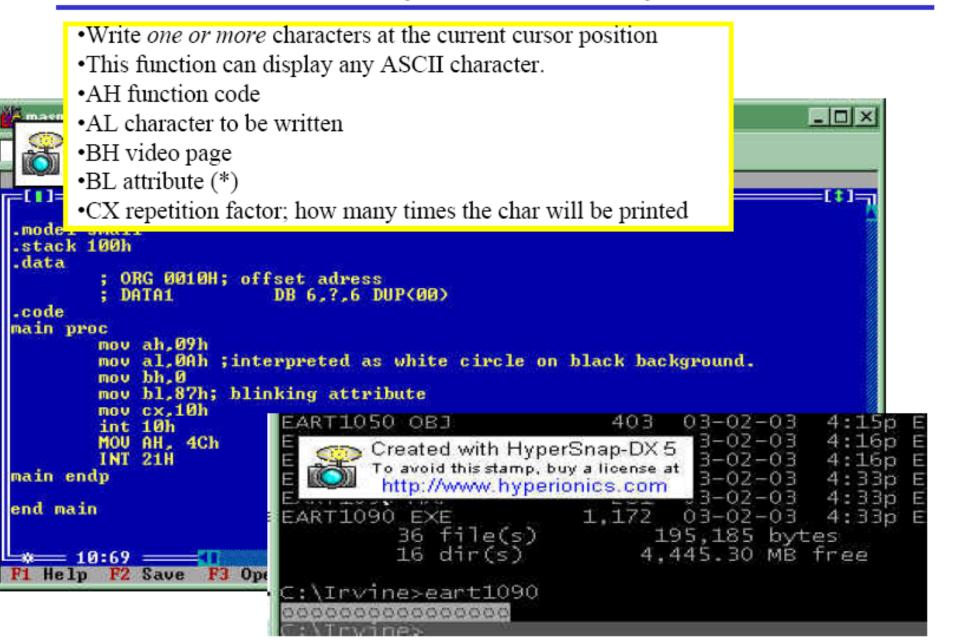
### Int 10 05

#### •INT 10H function 05; switch between video modes by adjusting AL

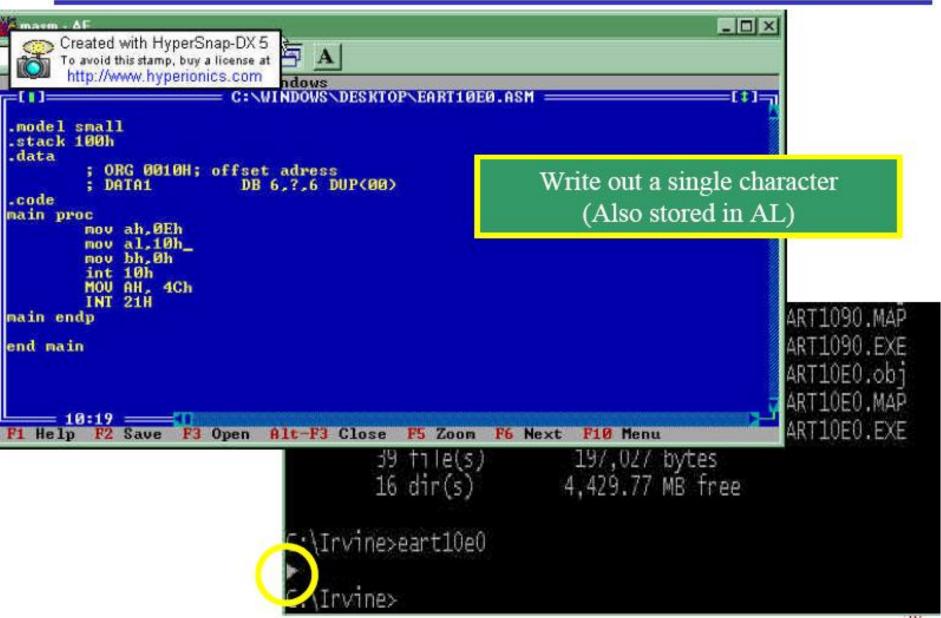
MOV AH, 05h MOV AL, 01H; switch to video page1 INT 10H ; below will switch to video page 0 MOV AH, 05h MOV AL, 00H; switch to video page0 INT 10H

Extremely useful in text modes that support multiple pages! This is what we had before Windows™

## INT 10 - 09h or 0A (\* no attribute)



### Int 10 - 0e



### INT 21h

### •INT 21H Option 01: Inputs a single character with echo

-This function waits until a character is input from the keyboard, then echoes it to the monitor. After the interrupt, the input character will be in AL.

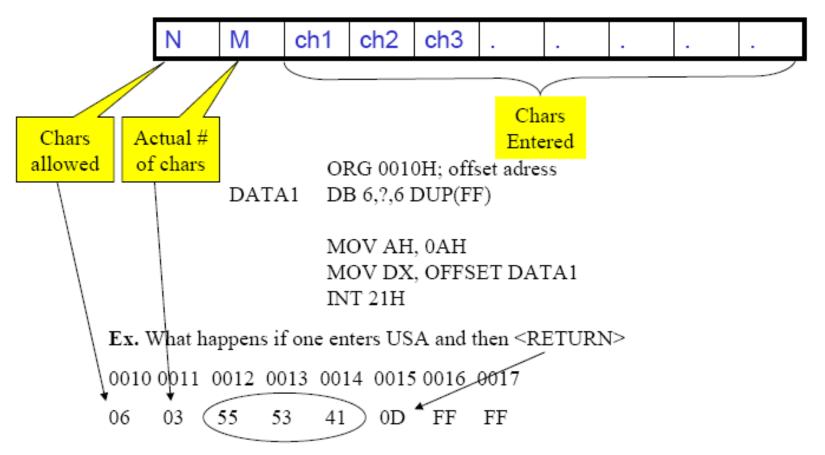


#### INT 21h

#### •INT 21H Option 0AH/09H: Inputs/outputs a string of data stored at DS:DX

-AH = 0AH, DX = offset address at which the data is located

-AH = 09, DX = offset address at which the data located



## INT 16h Keyboard Services

Checking a key press, we use INT 16h function AH = 01

```
MOV AH, 01
INT 16h
```

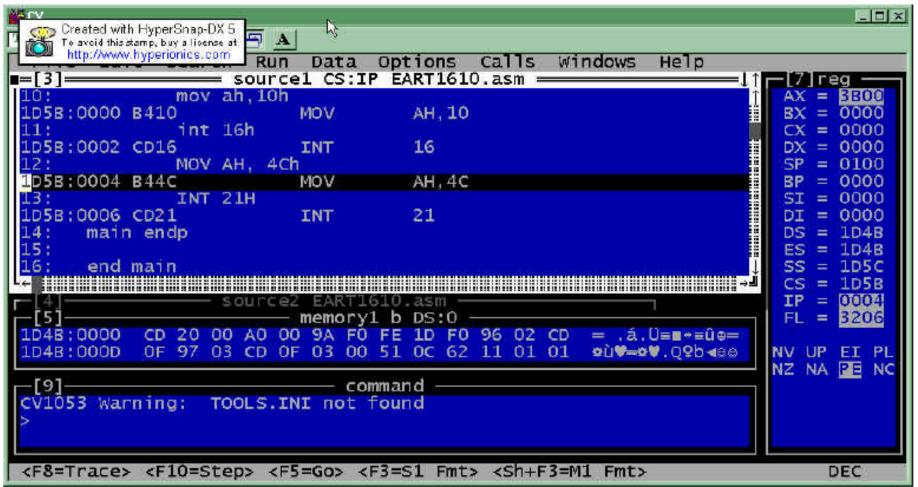
- Upon return, ZF = 0 if there is a key press; ZF = 1 if there is no key press
- Whick key is pressed?
- To do that, INT 16h function can be used immediately after the call to INT 16h function AH=01

```
MOV AH,0
INT 16h
```

Upon return, AL contains the ASCII character of the pressed key

### INT 16 – option 10 or 00

- BIOS Level Keyboard Input (more direct)
- Suppose F1 pressed (Scan Code 3BH). AH contains the scan code and AL contains the ASCII code (0).



### Example. The PC Typewriter

- Write an 80x86 program to input keystrokes from the PC's keyboard and display the characters on the system monitor. Pressing any of the function keys F1-F10 should cause the program to end.
- Algorithm:
  - Get the code for the key pressed
  - If this code is ASCII, display the key pressed on the monitor and continue
  - 3. Quit when a non-ASCII key is pressed
- INT 16, BIOS service 0 Read next keyboard character
  - Returns 0 in AL for non-ASCII characters or the character is simply stored in AL
- To display the character, we use INT 10, BIOS service 0E- write character in teletype mode. AL should hold the character to be displayed.
- INT 20 for program termination

### Example

MOV DX, OFFSET MES

MOV AH,09h

INT 21h; to output the characters starting from the offset

AGAIN: MOV AH,0h

INT 16h; to check the keyboard

CMP AL,00h

JZ QUIT ;check the value of the input data

MOV AH, 0Eh

INT 10h; echo the character to output

JMP AGAIN

QUIT: INT 20h

MES DB 'type any letter, number or punctuation key'

DB 'any F1 to F10 to end the program"

DB 0d,0a,0a,'\$'

