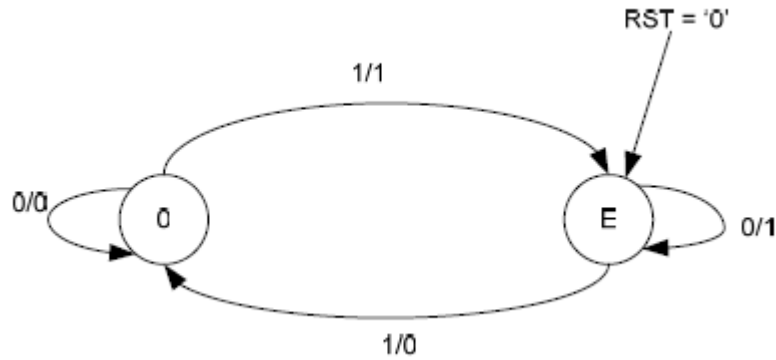


For the state diagram given below, draw the schematic circuit with D-flipflops



Assume you have two States: O and E, one input : X, one output: Z

Problem 5-12:

Reduce the number of states in the following table and tabulate the reduced state table.

PRESENT STATE	NEXT STATE		OUTPUT	
	X=0	X=1	X=0	X=1
a	f	b	0	0
b	d	c	0	0
c	f	e	0	0
d	g	a	1	0
e	d	c	0	0
f	f	b	1	1
g	g	h	0	1
h	g	a	1	0