Galala University, Faculty of Computer Science and Engineering

CSE 131 Logic Design

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## Homework 3

Draw the logical diagrams for the following Boolean expressions: (a)Y=A'B'+B(A+C).

## <u>Problem: 1-34</u>

Assume a 3-input AND gate with output F and a 3-input OR gate with G output. Show the signals of the outputs F and G as functions of the three inputs ABC. Use all 8 possible combinations of ABC.