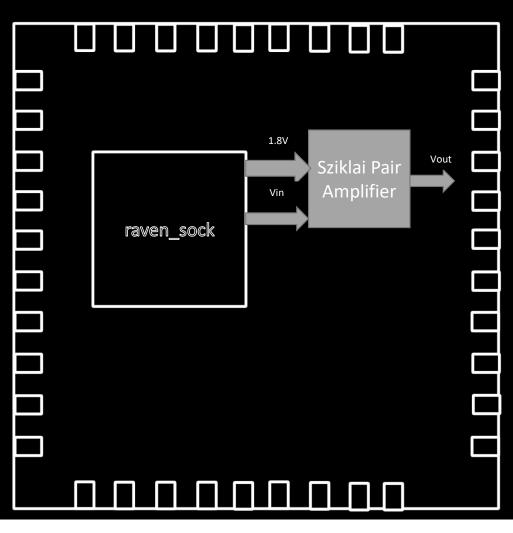
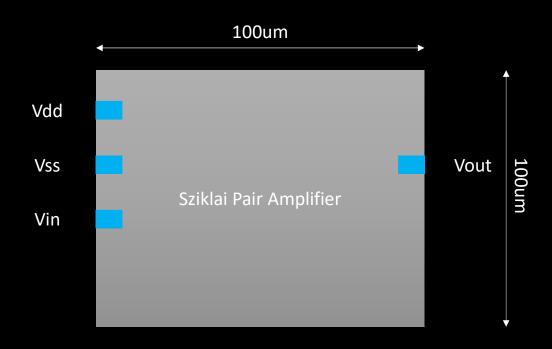
Sziklai Pair Amplifier(avsdspamp_3v3) spec sheet for 180nm tech node

- Specs Released under APACHE LICENSE 2.0
- Please contact Kunal at kunalghosh@gmail.com in case of any doubts

Application Note for avsdspamp 3v3



avsdspamp 3v3 preferred dimensions, pin locations and metal layers



Vdd, Vss, Vin, Vout(metal 3)pins
- 3um x 0.35um

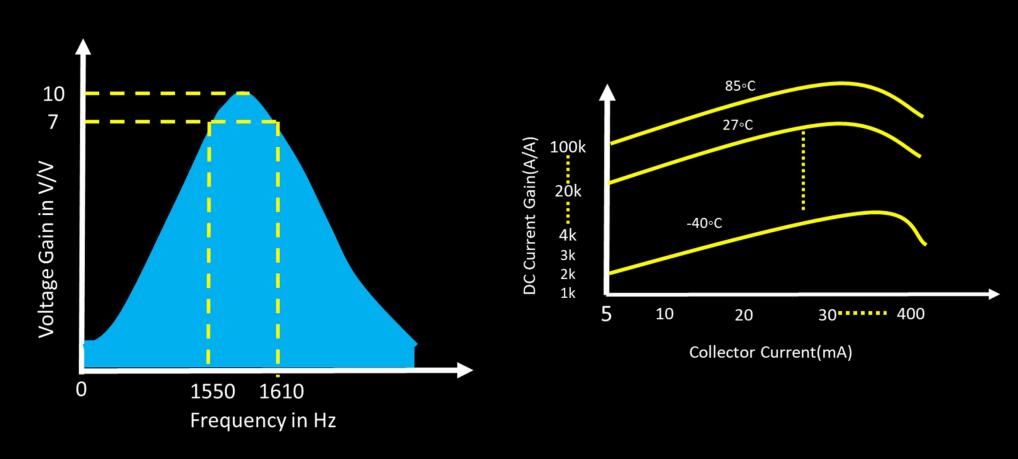
Features needed

- Operating frequency range 1550...1610 MHz
- High gain
- Low noise figure
- Output matching to 50Ω
- Temperature dependent or temperature independent

avsdspamp 3v3 Operating Modes

Parameter	Value			Unit
	min	typ	max	
Supply voltage	1.6	1.8	2.0	V
Operating temperature range	-40	+27	+85	°C
Operating input frequency	1550	-	1610	MHz
Noise figure	-	1.5	-	d B
Gain	-	10	-	V/V
Input VSWR	-	1.5	-	-
Output VSWR	-	1.5	-	-
Current limit	4.3	7.2	11.2	mA
Stand-by current	-	-	250	nA
Input 1dB compression point	-	-28	-	dBm

avsdspamp 3v3 Operating Modes



avsdspamp 3v3 Operating Modes

