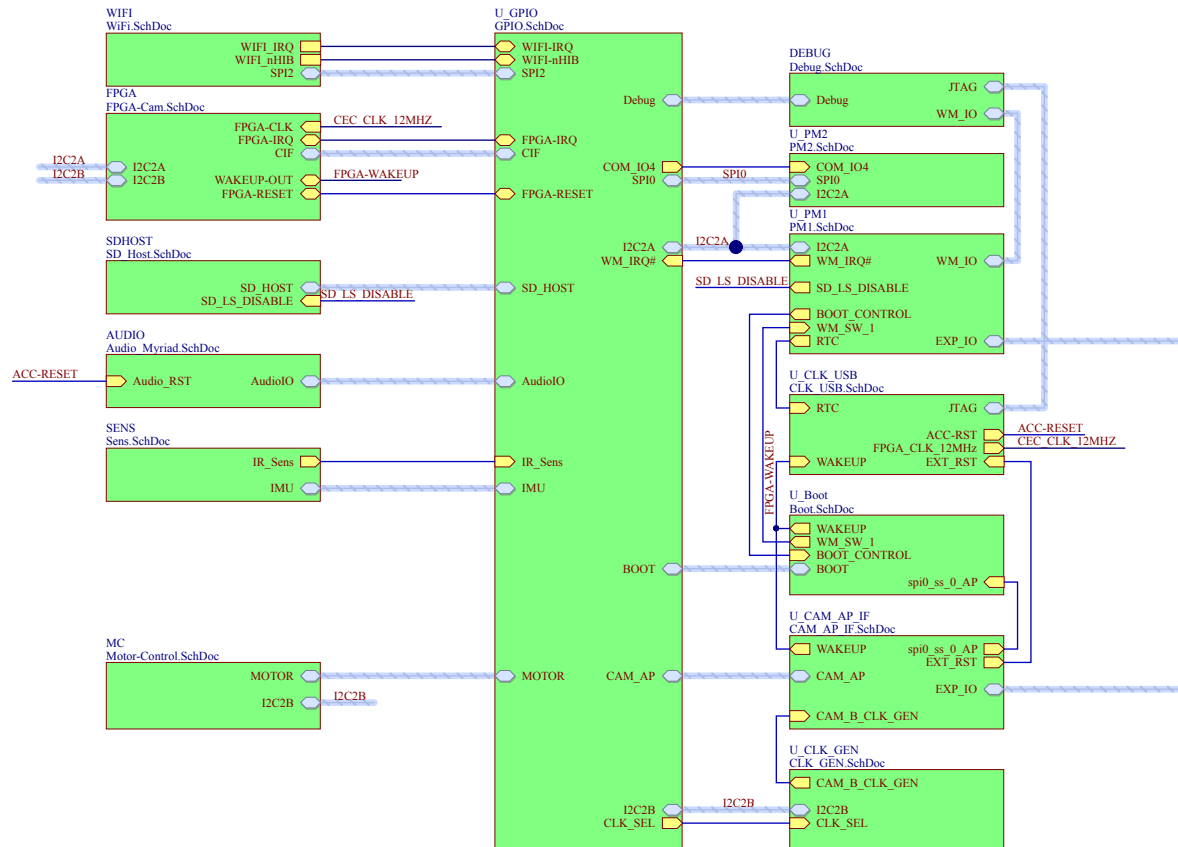



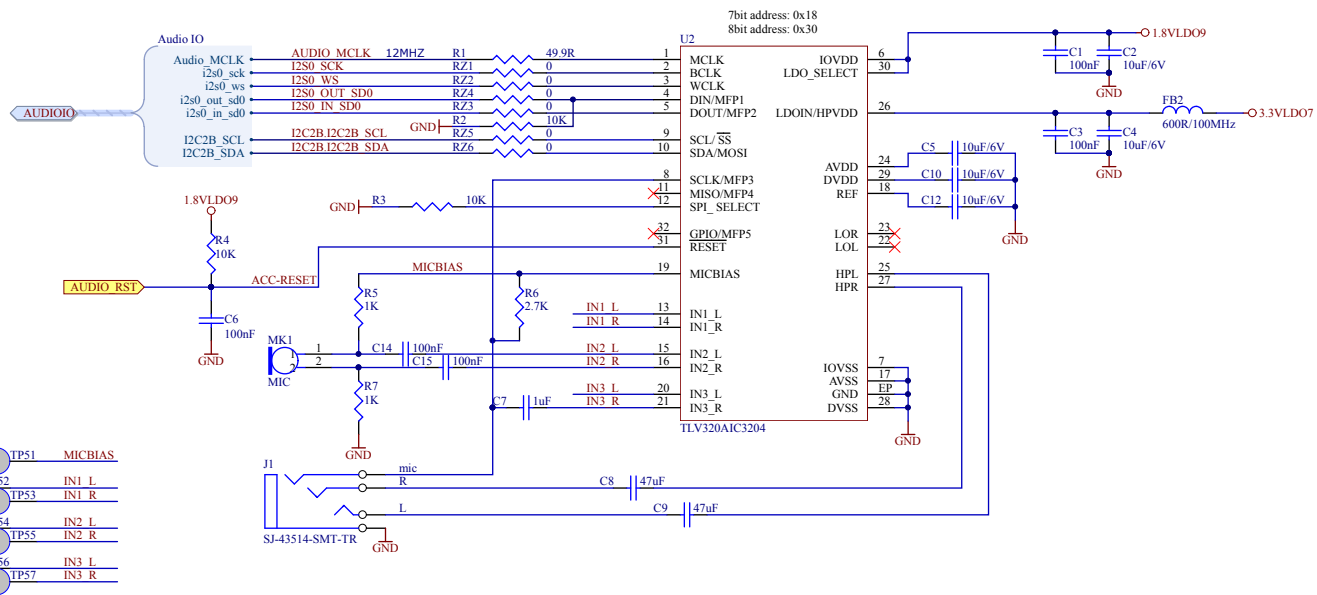
EoT NanEye Myriad2 DevBoard Schematic


Revision: R1 Aug-2015

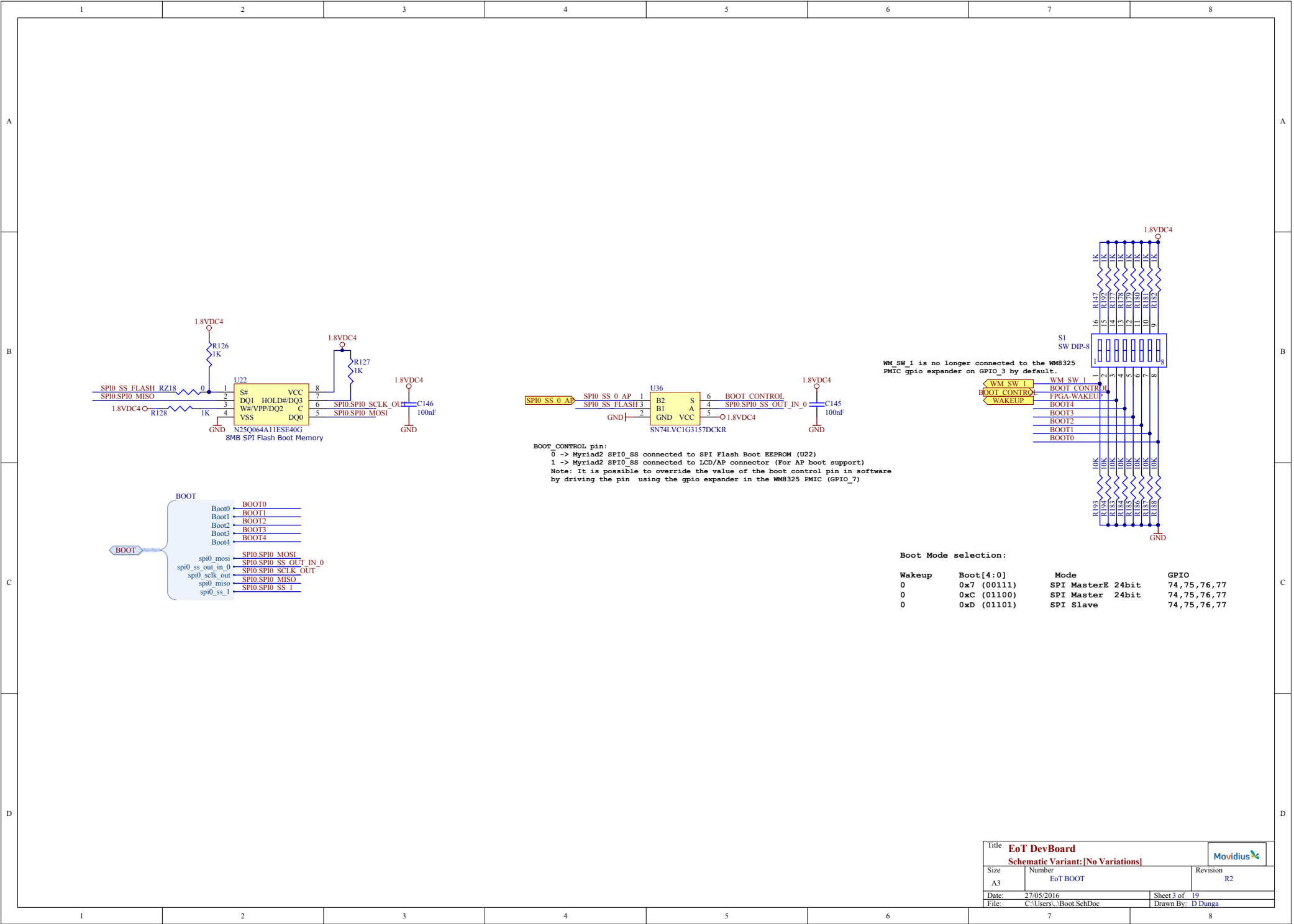
NOTES
Notes1.SchDoc

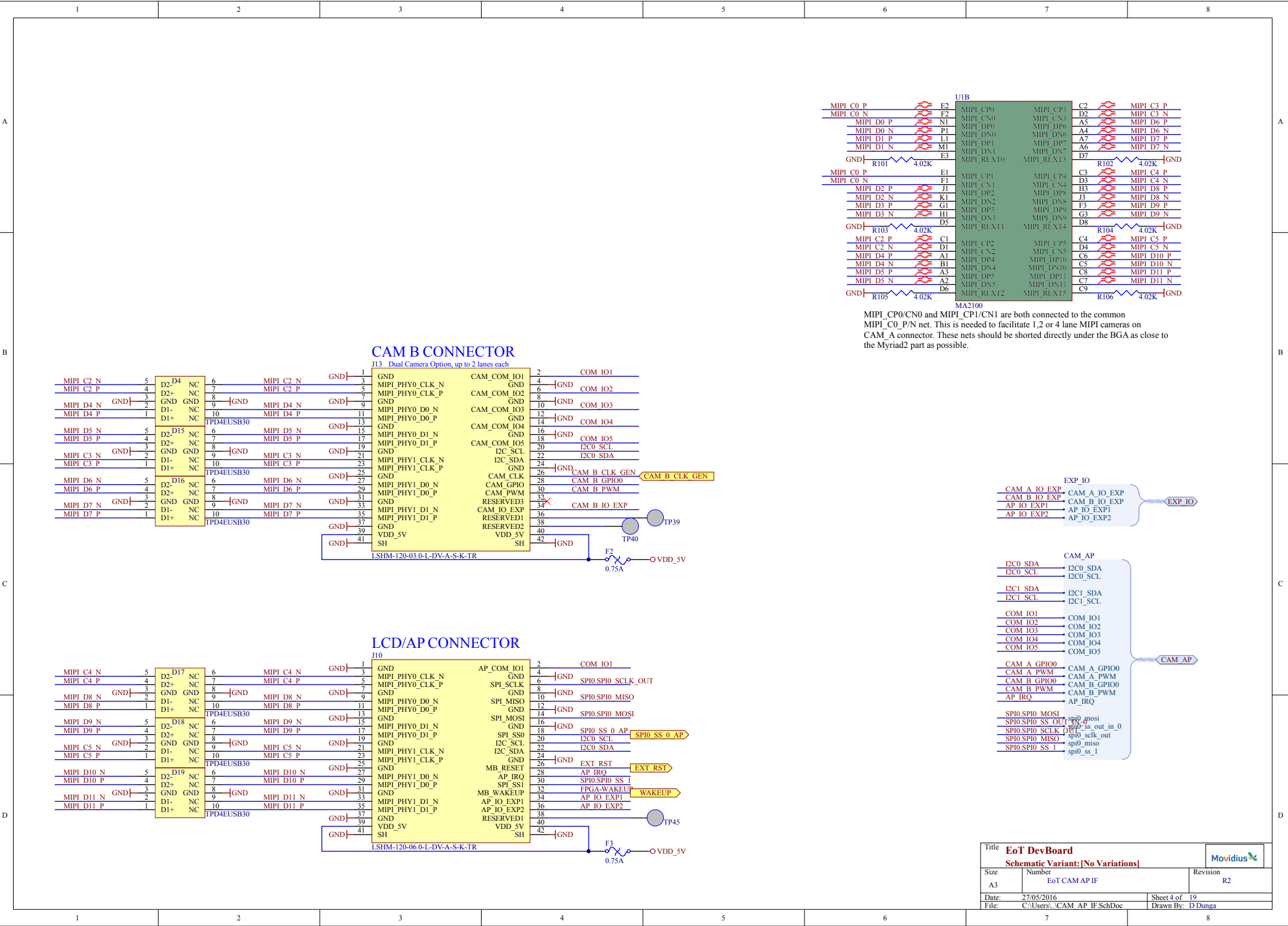


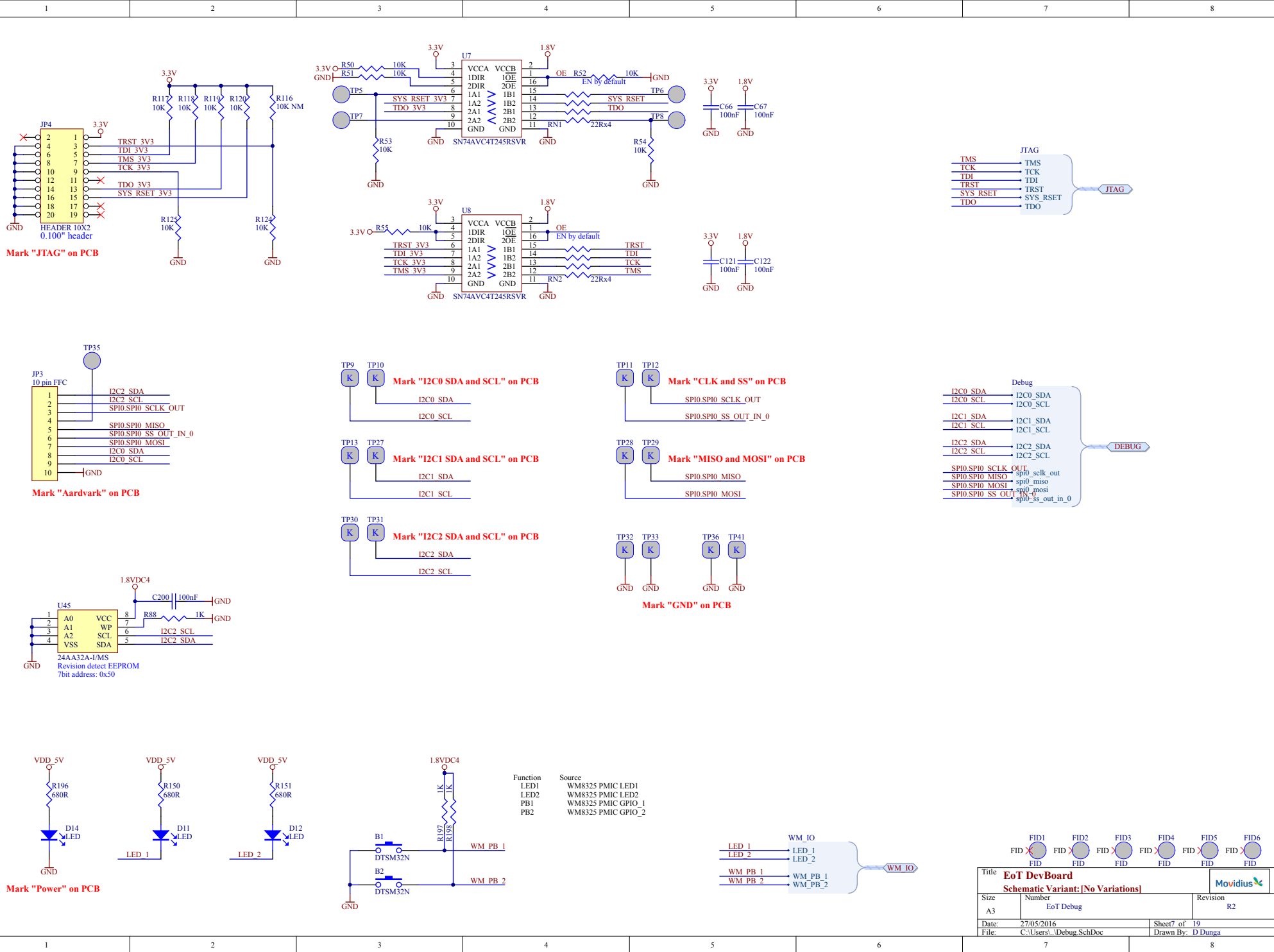
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Schematic Variant: [No Variations]				
Size	Number	Revision		
A3	EoT Dev Board Top Level		R2	
Date:	27/05/2016		Sheet 1 of	19
File:	C:\Users\...EoTDevBoard.SchDoc		Drawn By:	D Dunga

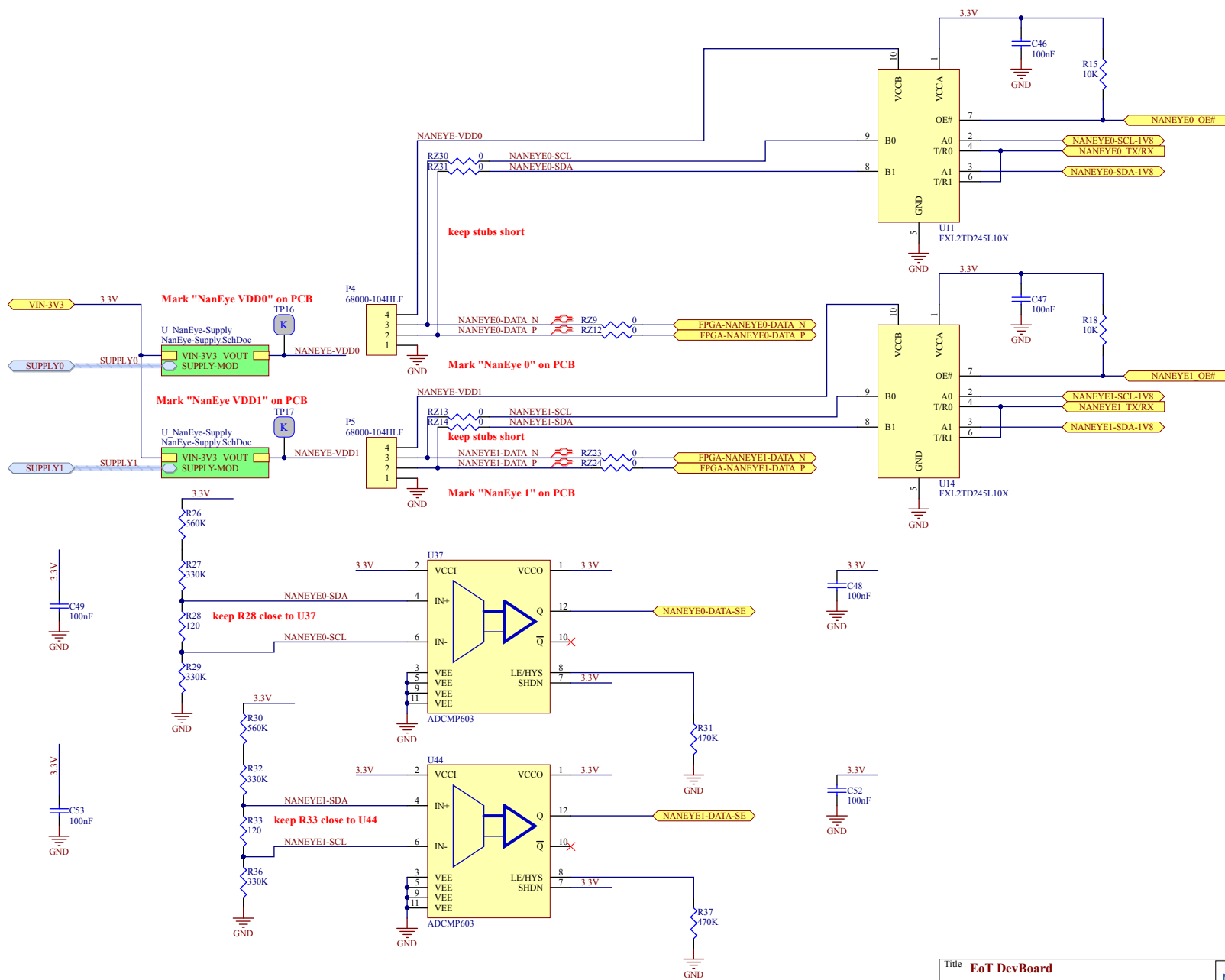



Title		EoT DevBoard		Movidius 
		Schematic Variant:[No Variations]		
Size	Number	Revision		
A3	EoT Audio IO	R2		
Date:	27/05/2016	Sheet2 of	19	
File:	C:\Users\Audio_Myriad.SchDoc	Drawn By:	D Dunga	



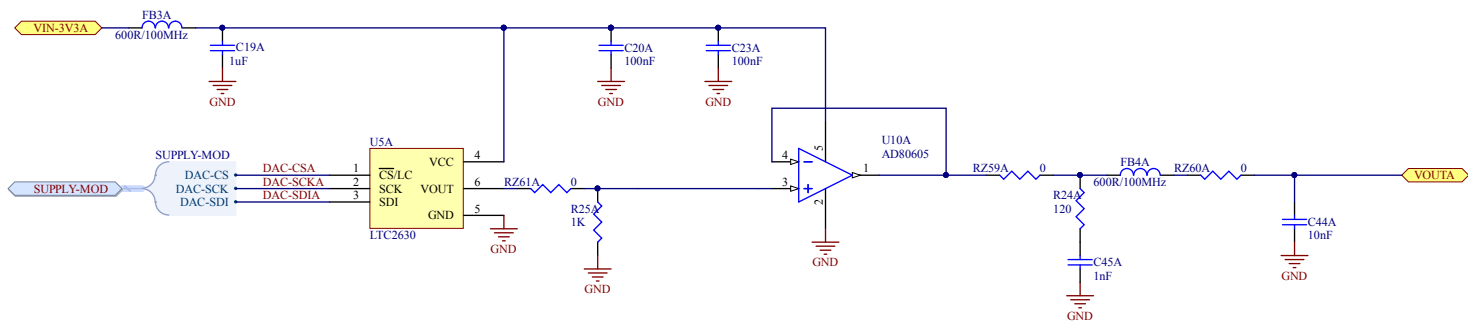





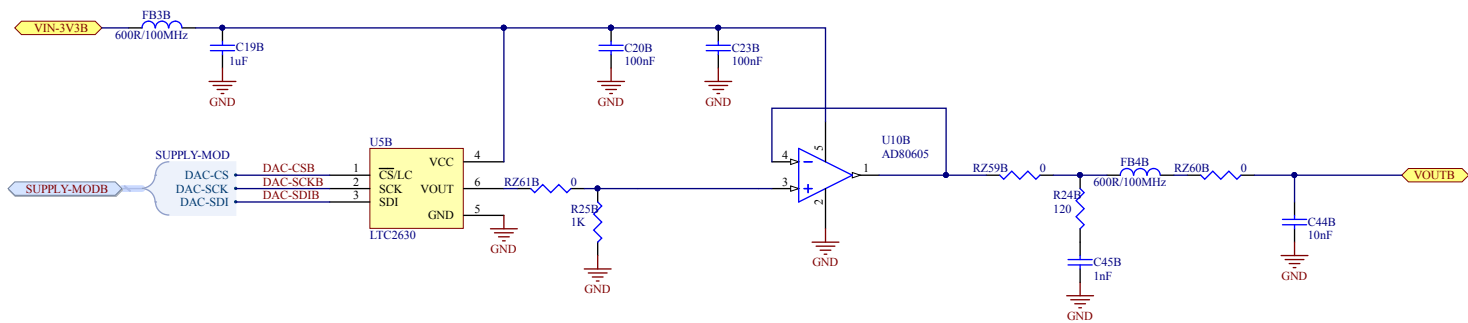



Title		EoT DevBoard		
		Schematic Variant: [No Variations]		
Size	Number	Revision		
A3	EoT NanEye Interface	R2		
Date:	27/05/2016	Sheet 9 of	19	
File:	C:\Users\...\NanEye-Interface.SchDoc	Drawn By:	E Chiu	

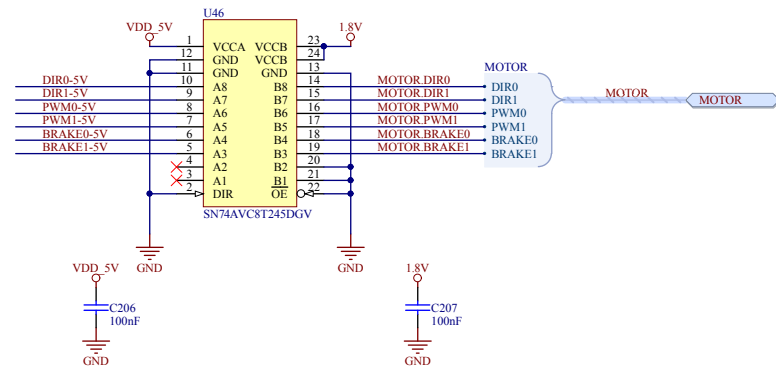
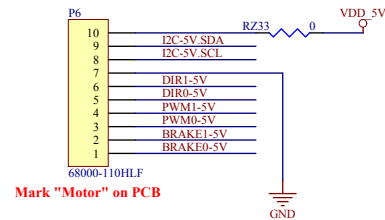
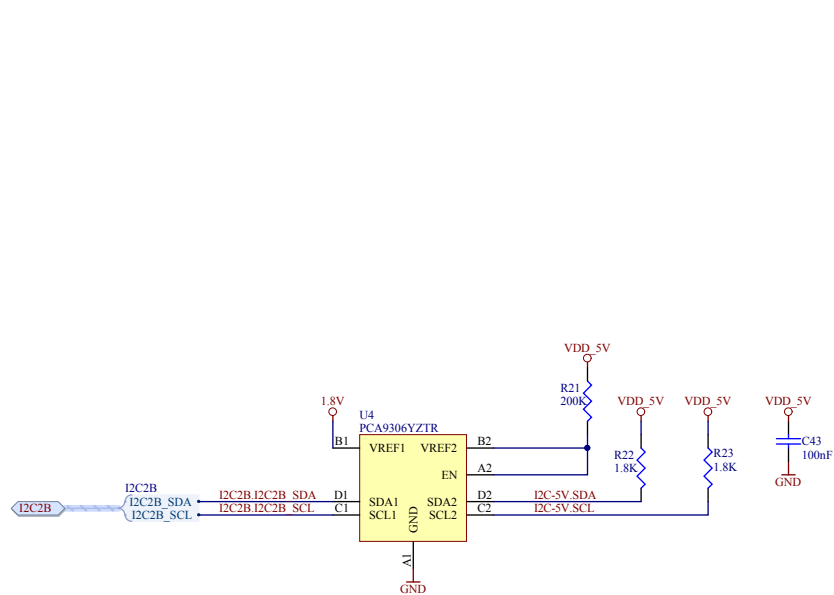





Title		EoT DevBoard		Movidius 
		Schematic Variant: [No Variations]		
Size	Number	Revision		
A3	EoT Power Supply Modulator	R2		
Date:	27/05/2016	Sheet 1 of	19	
File:	C:\Users\NanEye-Supply\SchDoc	Drawn By:	E Chiu	



Title		EoT DevBoard		Movidius 
		Schematic Variant:[No Variations]		
Size	Number	Revision		
A3	EoT Power Supply Modulator	R2		
Date:	27/05/2016	Sheet 1 of	19	
File:	C:\Users\NanEye-Supply\SchDoc	Drawn By:	E Chiu	




Title		EoT DevBoard		
		Schematic Variant: [No Variations]		
Size	Number	Revision		
A3	EoT Motor Control Interface	R2		
Date:	27/05/2016	Sheet 1 of	19	
File:	C:\Users\...Motor-Control SchDoc	Drawn By:	E Chiu	

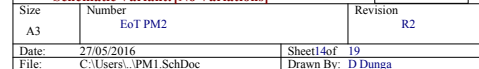
1	2	3	4	5	6	7	8	
A								A
B								B
C								C
D								D
1	2	3	4	5	6	7	8	

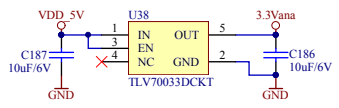
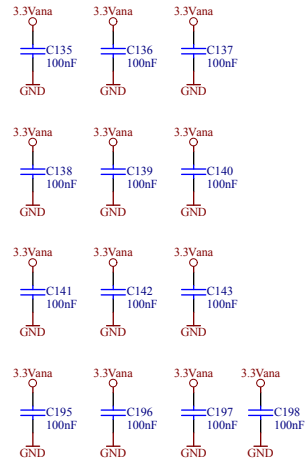
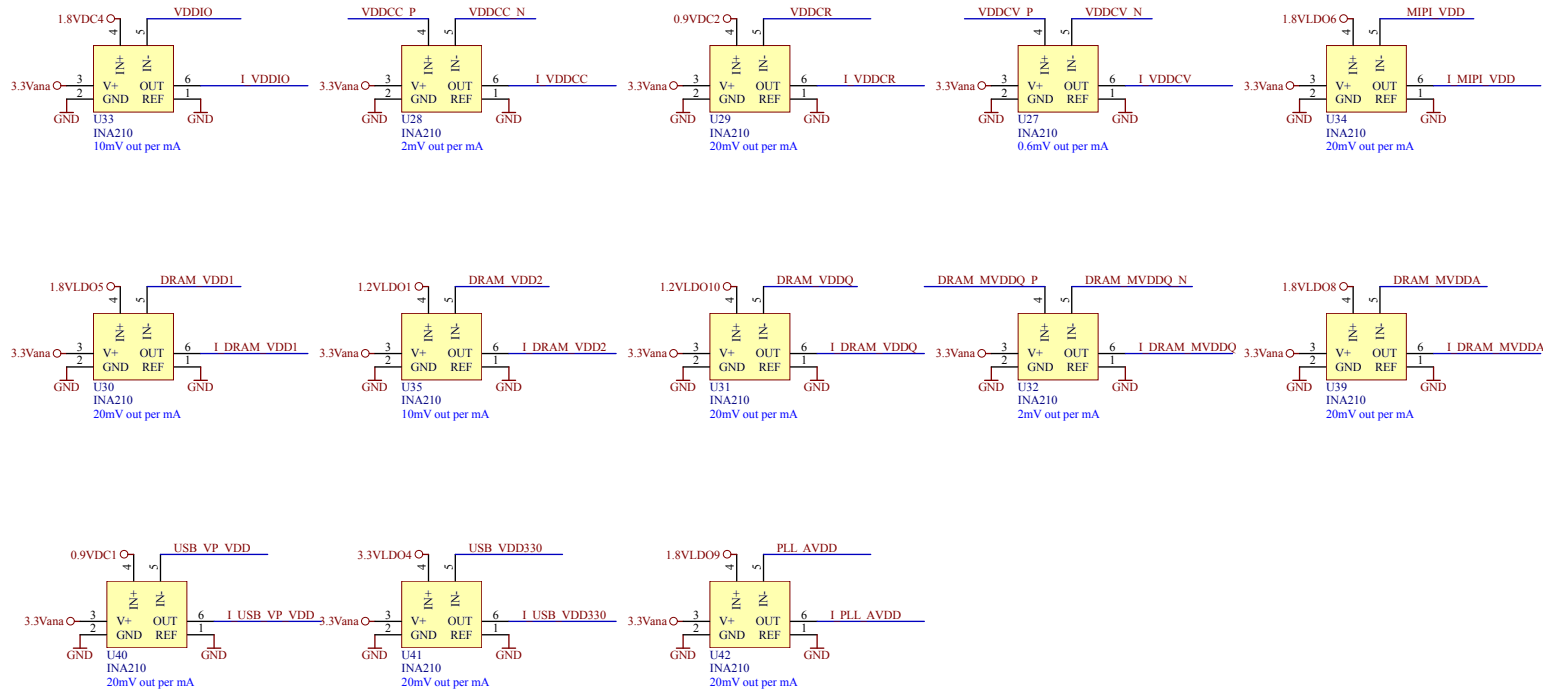
Schematic Revision History:
* R1
Initial Release. This design branched from MV0182 R4M0E0

Title		EoT DevBoard			
Size		Number		Revision	
A3		EoTDesign Notes		R2	
Date:		27/05/2016		Sheet 3 of 19	
File:		C:\Users\...Notes1.SchDoc		Drawn By: D Dunga	

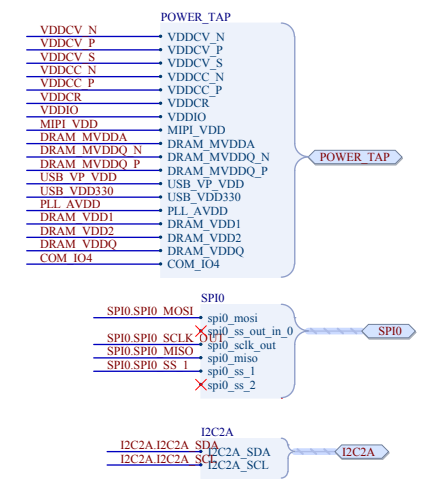
Schematic Revision History:
* R1
Initial Release. This design branched from MV0182 R4M0E0

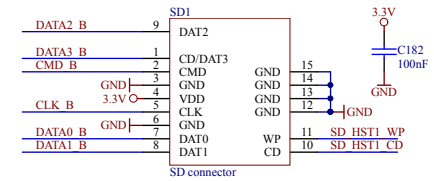
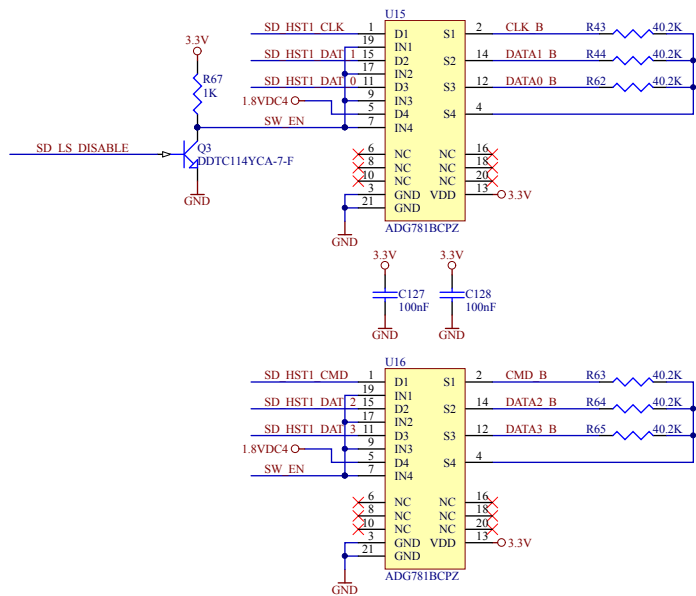
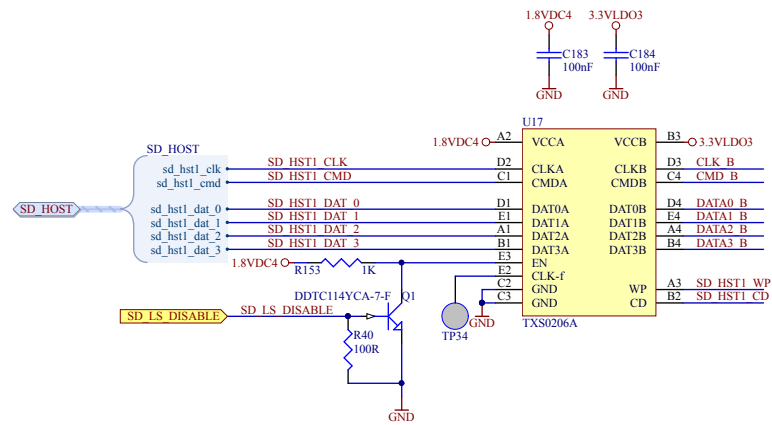
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Schematic Variant: [No Variations]							
Size		Number				Revision	
A3		EoTDesign Notes				R2	
Date:		27/05/2016				Sheet 13 of 19	
File:		C:\Users\...\Notes\I_SchDoc				Drawn By: D Dunga	




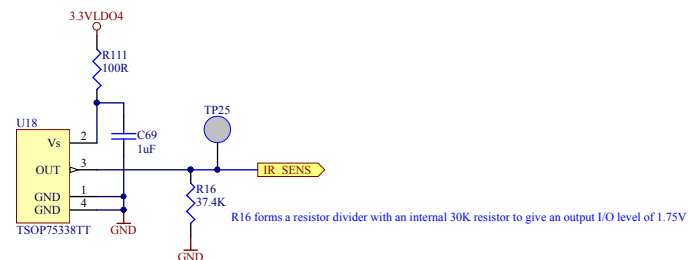


J8					
1 VDDIO	GND	1	2	2	GND
1 VDDCC	GND	3	4	4	1 USB VP VDD
1 VDDCR	GND	5	6	6	1 USB VDD330
1 VDDCV	GND	7	8	8	GND
1 VDDCV P	GND	9	10	10	1 PLL AVDD
1 VDDCV N	GND	11	12	12	VDDIO
1 MIPI VDD	GND	13	14	14	GND
1 DRAM VDD1	GND	15	16	16	VDDCV S
1 DRAM VDD2	GND	17	18	18	MIPI VDD
1 DRAM VDDQ	GND	19	20	20	GND
1 DRAM MVDDQ	GND	21	22	22	GND
1 DRAM MVDDQ P	GND	23	24	24	GND
1 DRAM MVDDQ N	GND	25	26	26	GND
1 DRAM MVDDA	GND	27	28	28	GND
1 DRAM MVDDQ	GND	29	30	30	COM IO4
1 DRAM MVDDQ P	GND	31	32	32	GND
1 DRAM MVDDQ N	GND	33	34	34	GND
1 DRAM MVDDA	GND	35	36	36	SPI0 SPI0 MOSI
1 DRAM MVDDQ	GND	37	38	38	SPI0 SPI0 SCLK OUT
1 DRAM MVDDQ P	GND	39	40	40	SPI0 SPI0 MISO
1 DRAM MVDDQ N	GND	41	42	42	GND
1 DRAM MVDDA	GND	43	44	44	GND
1 DRAM MVDDQ	GND	45	46	46	GND
1 DRAM MVDDQ P	GND	47	48	48	GND
1 DRAM MVDDQ N	GND	49	50	50	GND
1 DRAM MVDDA	GND	51	52	52	GND
1 DRAM MVDDQ	GND	53	54	54	GND
1 DRAM MVDDQ P	GND	55	56	56	GND
1 DRAM MVDDQ N	GND	57	58	58	GND
1 DRAM MVDDA	GND	59	60	60	GND
1 DRAM MVDDQ	GND	61	62	62	GND
1 DRAM MVDDQ P	GND	63	64	64	GND
1 DRAM MVDDQ N	GND	65	66	66	GND
1 DRAM MVDDA	GND	67	68	68	GND
1 DRAM MVDDQ	GND	69	70	70	GND
1 DRAM MVDDQ P	GND	71	72	72	GND
1 DRAM MVDDQ N	GND	73	74	74	GND
1 DRAM MVDDA	GND	75	76	76	GND
1 DRAM MVDDQ	GND	77	78	78	GND
1 DRAM MVDDQ P	GND	79	80	80	GND
1 DRAM MVDDQ N	GND	81	82	82	GND
1 DRAM MVDDA	GND	83	84	84	GND
1 DRAM MVDDQ	GND	85	86	86	GND
1 DRAM MVDDQ P	GND	87	88	88	GND
1 DRAM MVDDQ N	GND	89	90	90	GND
1 DRAM MVDDA	GND	91	92	92	GND
1 DRAM MVDDQ	GND	93	94	94	GND
1 DRAM MVDDQ P	GND	95	96	96	GND
1 DRAM MVDDQ N	GND	97	98	98	GND
1 DRAM MVDDA	GND	99	100	100	GND

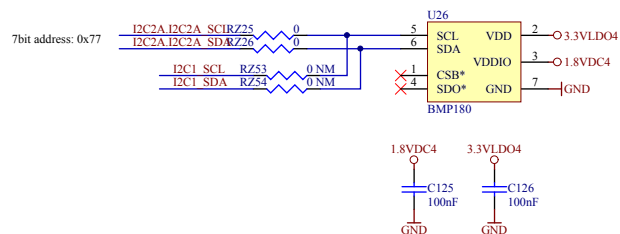
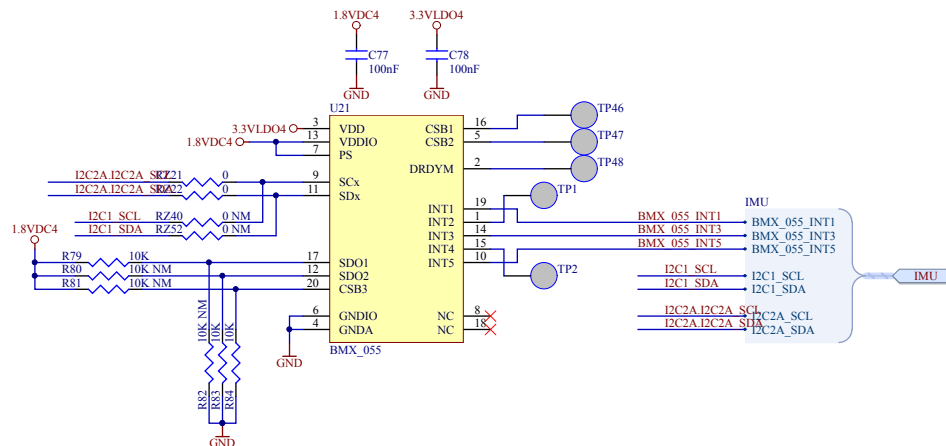





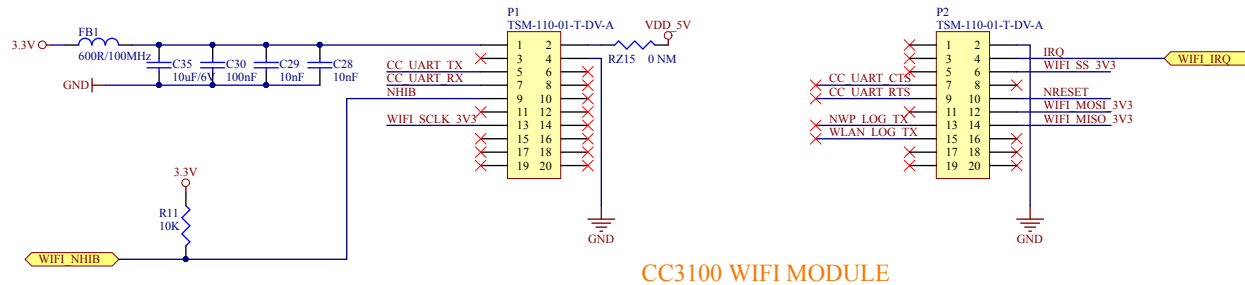
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Size	Number	Revision		
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Date:	27/05/2016	Sheet17of	19	
File:	C:\Users\SD_Host\SchDoc	Drawn By:	D Dunga	



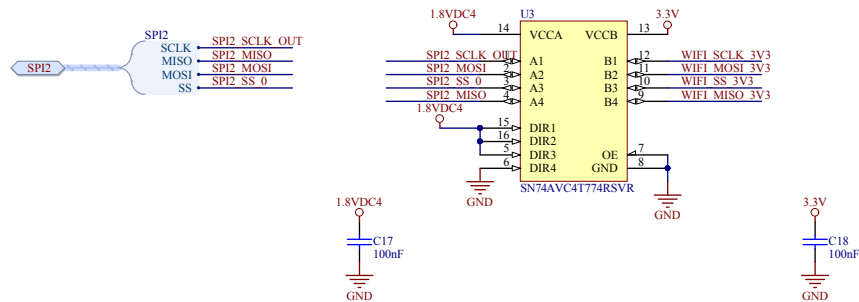
Acc
7bit address: 0x19 (0x18 optional)
Gyr
7bit address: 0x68 (0x69 optional)
Mag
7bit address: 0x11 (0x10, 0x12, 0x13)




Title		EoT DevBoard		
		Schematic Variant: [No Variations]		
Size	Number			Revision
A3	EoT Sensors			R2
Date:	27/05/2016	Sheet 1 of	19	
File:	C:\Users\...\Sens.SchDoc	Drawn By:	D Dunga	



CC3100 WIFI MODULE



Title		EoT DevBoard		
		Schematic Variant: [No Variations]		
Size	Number	Revision		
A3	EoT WIFI	R2		
Date:	27/05/2016	Sheet 1 of	19	
File:	C:\Users\...\WiFi\SchDoc	Drawn By:	D Dunga	