SPDX-License-Identifier: CERN-OHL-S-2.0
Source Location: https://github.com/EzechielXae/SegaCd_BackupRam.git

Address Bus Management :

U6 (74HCT138)
Very High Addresses:
A22~ A20

U3 (74HCT30)
High Addresses:
A19~ A12

U2 (74HCT30)
Low Addresses:
A11~ A4

X1 (Mega Drive Port)
Very Low Addresses:
A3~ A0

Subtlety:
Sega starts the naming of the cartridge-port
addresses at 1 and not 0!!!

Drilling :

Holes Ø 6.2 mm

Battery Life :

CR2032 (220 mAh):
2 uA   ~12.6 years (limited by self-discharge   ~5~ 10 years real)
4 uA   ~6.3 years (limited by self-discharge   ~5 years real)

CR2450 (600 mAh):
2 uA   ~34 years (limited by self-discharge   ~10~ 15 years real)
4 uA   ~17 years (limited by self-discharge   ~10~ 15 years real)

Bank Switching Sram 4Mbits :

U10 (74HCT245) :
B0-B2 Sets SRAM Size
000 - 64KBit/8k
001 - 128KBit/16k
010 - 256KBit/32k
011 - 512KBit/64k - Official RAM Cart Size
100 - 1MBit/128k
101 - 2MBit/256k
110 - 4MBit/512k - Max Size (Schematic Reflects)

TITLE:
Sega Back Up RAM MegaCD   REV: 1.0

Company: Ezechielxae, Co
Date: 2025-09-29   Drawn By: Ezechielxae   Sheet: 1/1