

THÈSE

Pour obtenir le grade de

DOCTEUR DE L'UNIVERSITÉ DE GRENOBLE

Spécialité : **Informatique**

Arrêté ministériel : 25 mai 2016

Présentée par

Tom CORNEBIZE

Thèse dirigée par **Arnaud LEGRAND**

préparée au sein du **Laboratoire d'Informatique de Grenoble**
et de l'École Doctorale **MSTII**

Le Titre de la Thèse

The English Title

Thèse soutenue publiquement le **1^{er} janvier 1970**,
devant le jury composé de :



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28th October 2020, 14:53:23

I dedicate this thesis to my grumpy cat.

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” *Elle est où la poulette ?*

— **Kadoc DE VANNES**

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Remerciements

(Acknowledgments)

I would like to thank everyone, except from Dobby the free elf.

Merci public !

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Abstract / Résumé

Abstract

The English abstract.

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Résumé

Le résumé en français.

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Introduction

To introduce my work, I will write a nice introduction in the following. Citation example for the Top500 website [@top500] and some random paper [Gra69].

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Performance prediction through simulation: the HPL case

2.1 High Performance Linpack

some text...

2.2 Simgrid/SMPI and the other simulators

some text...

2.3 Emulating HPL at large scale

some text...

2.4 Modeling HPL kernels and communications

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2.5 Validation

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2.6 Sensibility analysis

some text...

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Experimental control

3.1 Experimental Testbed and Experiment Engines

some text...

3.2 Randomizing matters!

some text...

3.3 Performance non-regression tests

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Conclusion

Your beautiful conclusion. Lorem ipsum dolor sit amet, consectetur adipiscing elit. Ut purus elit, vestibulum ut, placerat ac, adipiscing vitae, felis. Curabitur dictum gravida mauris. Nam arcu libero, nonummy eget, consectetur id, vulputate a, magna. Donec vehicula augue eu neque. Pellentesque habitant morbi tristique senectus et netus et malesuada fames ac turpis egestas. Mauris ut leo. Cras viverra metus rhoncus sem. Nulla et lectus vestibulum urna fringilla ultrices. Phasellus eu tellus sit amet tortor gravida placerat. Integer sapien est, iaculis in, pretium quis, viverra ac, nunc. Praesent eget sem vel leo ultrices bibendum. Aenean faucibus. Morbi dolor nulla, malesuada eu, pulvinar at, mollis ac, nulla. Curabitur auctor semper nulla. Donec varius orci eget risus. Duis nibh mi, congue eu, accumsan eleifend, sagittis quis, diam. Duis eget orci sit amet orci dignissim rutrum.

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Bibliography

- [@top500] *TOP500 Website*. URL: <https://www.top500.org/> (visited on Sept. 7, 2020).
cit. on p. 1
- [Gra69] Ronald L. Graham. “Bounds on multiprocessing timing anomalies”. In: *SIAM journal on Applied Mathematics* 17.2 (1969), pp. 416–429. cit. on p. 1

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List of Figures

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Abstract

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