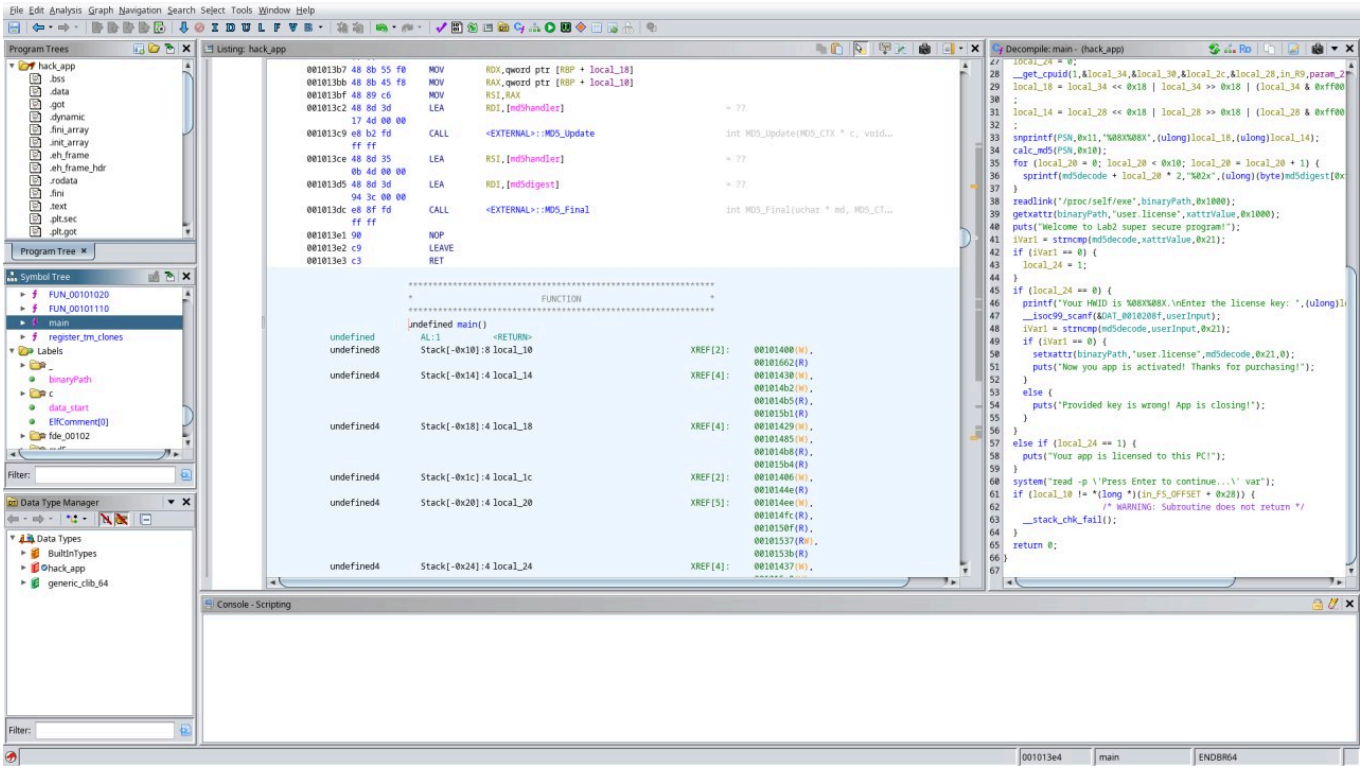
Lab 2

Ghidra exploring



Keygen

1 — Algorithm reverse-engineered from `main`

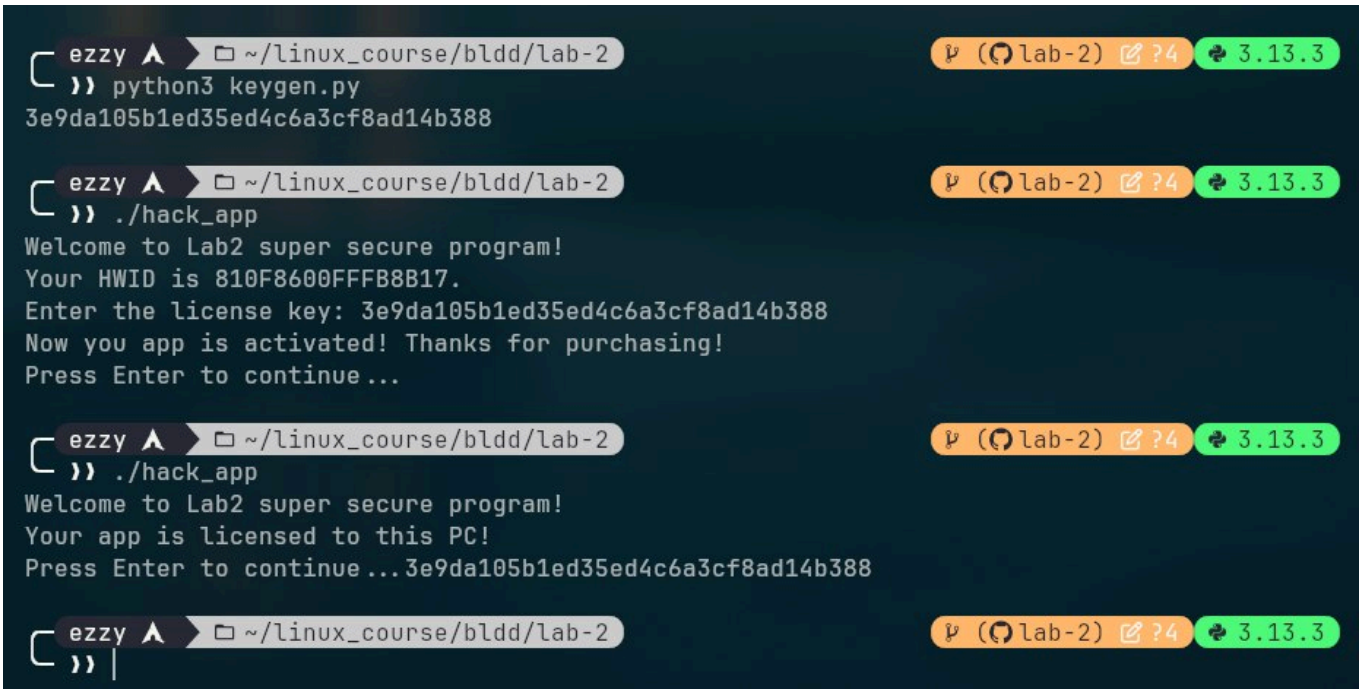
Step	Extracted from code
1	`__get_cpuid(1, …)` returns `EAX`, `EDX`
2	Byte-swap both registers → `swap32`
3	Format: `"%08X%08X"` ⇒ **HWID** (16 upper-case hex)
4	`MD5(HWID_ASCII)`
5	Reverse order of the 16 MD5 bytes
6	Output lowercase hex ⇒ **license key** (32 chars)

2 — Implementation (`keygen.py`)

```
#!/usr/bin/env python3
import argparse, hashlib, re, subprocess, sys

def swap32(v): return ((v<<24)&0xFF000000)|((v<<8)&0x00FF0000)|
((v>>8)&0x0000FF00)|((v>>24)&0xFF)

def cpuid():
    out = subprocess.check_output(["cpuid", "-r", "-l", "1"], text=True)
    eax = int(re.search(r"eax=0x([0-9a-f]{8})", out, re.I).group(1), 16)
    edx = int(re.search(r"edx=0x([0-9a-f]{8})", out, re.I).group(1), 16)
    return eax, edx

def hwid():
    eax, edx = cpuid()
    return f"{swap32(eax):08X}{swap32(edx):08X}"

def key(h): return "".join(f"{b:02x}" for b in
hashlib.md5(h.encode()).digest()[::-1])

if __name__ == "__main__":
    a = argparse.ArgumentParser()
    a.add_argument("--id", help="override HWID (16 hex)")
    args = a.parse_args()
    hw = args.id or hwid()
    if not re.fullmatch(r"[0-9A-F]{16}", hw): sys.exit("HWID missing; use --
id")
    print(key(hw))
```

No comments, < 100 LOC, single dependency — `cpuid` CLI.

3 — Usage & result



Binary-patch

1 — Locate the check

```
objdump -d hack_app | grep -n -A3 -B1 strncmp
103-
104:0000000000001150 <strncmp@plt>:
105-    1150:  f3 0f 1e fa             endbr64
106:    1154:  f2 ff 25 25 2e 00 00    bnd jmp *0x2e25(%rip)        # 3f80
<strncmp@GLIBC_2.2.5>
107-    115b:  0f 1f 44 00 00          nopl   0x0(%rax,%rax,1)
108-
109-0000000000001160 <system@plt>:
--
420-    1590:  48 8d 3d a9 3a 00 00    lea    0x3aa9(%rip),%rdi        # 5040
<md5decode>
421:    1597:  e8 b4 fb ff ff          call   1150 <strncmp@plt>
422-    159c:  85 c0                   test   %eax,%eax
423-    159e:  75 07                   jne    15a7 <main+0x1c3>
424-    15a0:  c7 45 e4 01 00 00 00    movl   $0x1,-0x1c(%rbp)
--
439-    15ee:  48 8d 3d 4b 3a 00 00    lea    0x3a4b(%rip),%rdi        # 5040
<md5decode>
440:    15f5:  e8 56 fb ff ff          call   1150 <strncmp@plt>
441-    15fa:  85 c0                   test   %eax,%eax
442-    15fc:  75 33                   jne    1631 <main+0x24d>
443-    15fe:  41 b8 00 00 00 00       mov    $0x0,%r8d
```

```
1597:  call  strncmp@plt        ; compare md5decode vs xattr
159c:  test  eax,eax
159e:  jne   15a7               ; if not equal → ask for key
```

- Virtual address of the conditional jump: **0x159e**
- File offset (ELF base is 0): **0x159e**
- Bytes: `75 07` (`JNE +0x07`)

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2 — Patch decision

Replace `JNE` with two `NOP`s ⇒ `test eax,eax` is preserved, but branch is neutralised → execution falls through as if comparison passed.

`75 07` -> `90 90`

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3 — Patcher script

```python
#!/usr/bin/env python3
import argparse, os, stat, sys

OFF, ORIG, PATCH = 0x159e, b"\x75\x07", b"\x90\x90"

def patch(src, dst):
    data = bytearray(open(src, "rb").read())
    if data[OFF:OFF+2] != ORIG:
        sys.exit("unexpected opcode")
    data[OFF:OFF+2] = PATCH
    open(dst, "wb").write(data)
    os.chmod(dst, os.stat(src).st_mode | stat.S_IXUSR)

if __name__ == "__main__":
    a = argparse.ArgumentParser()
    a.add_argument("infile"), a.add_argument("outfile")
    args = a.parse_args()
    patch(args.infile, args.outfile)
```

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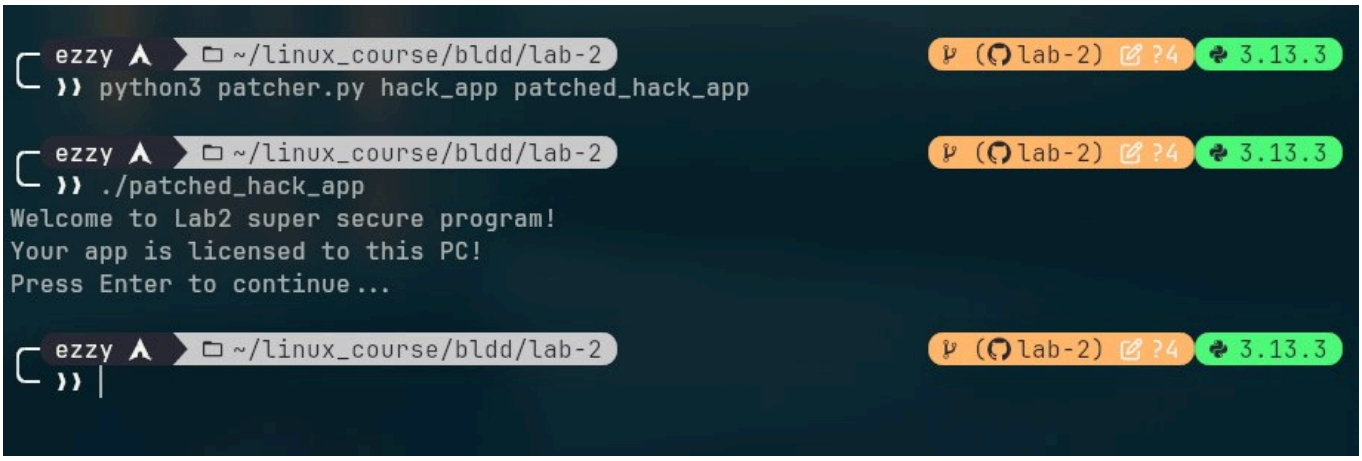
4 — Usage & result



Program starts licensed immediately; no key, no xattr — licensing logic disabled.