Modbus Kommunikationsprotokoll

COIL, BOOL, 1 BIT								INPUT-REGISTER, WORD, 16 BIT			HOLDING-REGISTER, WORD, 16 BIT								
Verwendungszweck	Arduino GPIO	Register Index	Direction 0 OUTPUT 1 INPUT / ADC	Register Index	Satus 0 LOW 1 HIGH	Register Index	Input Pullup 0 OFF 1 ON	Register Index	Wertebereich	Beschreibung	Register Index	Wertebereich	Beschreibung	Register Index	Wertebereich	Beschreibung	Register Index	Wertebereich	Beschreibung
	/	0	/	50	/	100	/	0	Res.	Res.	0	0-15	Vorrichtungscodierung	50	0-65535	a	100	0-65535	5c
SUB-D 38 / Reserve	A0	1	0/1	51	0/1	101	0/1	1	0-1023	ADC A00	1	0-32799	Reset system & Kanal Ein/Aus	51	0-65535	b	101	0-65535	5d
SUB-D 37 / Reserve	A1	2	0/1	52	0/1	102	0/1	2	0-1023	ADC A01	2	0-65535	Low Bytes Frequenzy (in Hz)	52	0-65535	c	102	0-65535	5e
SUB-D 36 / S2 Messen	A2	3	0/1	53	0/1	103	0/1	3	0-1023	ADC A02	3	0-65635	High Bytes Frequenzy (in Hz)	53	0-65535	d	103	0-65535	5f
SUB-D 35 / S1 Messen	A3	4	0/1	54	0/1	104	0/1	4	0-1023	ADC A03	4	0-1300	Amplitude 0 (mV)	54	0-65535	e	104	0-65535	1e
SUB-D 34 / 3,3V Messen	A4	5	0/1	55	0/1	105	0/1	5	0-1023	ADC A04	5	0-1300	Amplitude 1 (mV)	55	0-65535	1f	105	0-65535	1d
SUB-D 46 / Messung Starten	A5	6	0/1	56	0/1	106	0/1	6	0-1023	ADC A05	6	0-1300	Amplitude 2 (mV)	56	0-65535	20			
SUB-D 17 / Messen	A6	7	0/1	57	0/1	107	0/1	7	0-1023	ADC A06	7	0-1300	Amplitude 3 (mV)	57	0-65535	22			
Reserve	A7	8	0/1	58	0/1	108	0/1	8	0-1023	ADC A07	8	0-65535	Phase 0 (0-360)=(0-65535)	58	0-65535	23			
Reserve	A8	9	0/1	59	0/1	109	0/1	9	0-1023	ADC A08	9	0-65535	Phase 1 (0-360)=(0-65535)	59	0-65535	24			
SUB-D 47 / Reserve	A9	10	0/1	60	0/1	110	0/1	10	0-1023	ADC A09	10	0-65535	Phase 2 (0-360)=(0-65535)	60	0-65535	25			
Reserve	A10	11	0/1	61	0/1	111	0/1	11	0-1023	ADC A10	11	0-65535	Phase 3 (0-360)=(0-65535)	61	0-65535	26			
Reserve	A11	12	0/1	62	0/1	112	0/1	12	0-1023	ADC A11	12	360-10360	Poti Signal 0	62	0-65535	27			
Reserve	A12	13	0/1	63	0/1	113	0/1	13	0-1023	ADC A12	13	360-10360	Poti Signal 1	63	0-65535	28			
Reserve	A13	14	0/1	64	0/1	114	0/1	14	0-1023	ADC A13	14	360-10360	Poti Signal 2	64	0-65535	29			
Reserve	A14	15	0/1	65	0/1	115	0/1	15	0-1023	ADC A14	15	360-10360	Poti Signal 3	65	0-65535	2a			
Reserve	A15	16	0/1	66	0/1	116	0/1	16	0-1023	ADC A15	16	360-10360	Poti Signal 4	66	0-65535	2b			
Reserve	14	17	0/1	67	0/1	117	0/1				17	360-10360	Poti Signal 5	67	0-65535	2c			
Reserve	15	18	0/1	68	0/1	118	0/1				18	360-10360	Poti Signal 6	68	0-65535	2d			
Reserve	16	19	0/1	69	0/1	119	0/1				19	360-10360	Poti Signal 7	69	0-65535	2e			
Reserve	17	20	0/1	70	0/1	120	0/1				20	0-5000	Offset Signal 0	70	0-65535	2f			
Reserve	18	21	0/1	71	0/1	121	0/1				21	0-5000	Offset Signal 1	71	0-65535	30			
Reserve	19	22	0/1	72	0/1	122	0/1				22	0-5000	Offset Signal 2	72	0-65535	31			
I2C Reserve	20	23	0/1	73	0/1	123	0/1				23	0-5000	Offset Signal 3	73	0-65535	32			
I2C Reserve	21	24	0/1	74	0/1	124	0/1				24	0-5000	Offset Signal 4	74	0-65535	33			
Codierung	23	25	0/1	75	0/1	125	0/1				25	0-5000	Offset Signal 5	75	0-65535	34			
Codierung	24	26	0/1	76	0/1	126	0/1				26	0-5000	Offset Signal 6	76	0-65535	35			
Codierung	25	27	0/1	77	0/1	127	0/1				27	0-5000	Offset Signal 7	77	0-65535	36			
Codierung	26	28	0/1	78	0/1	128	0/1				28	1000-5000	DAC Ref	78	0-65535	37			
Messen	27	29	0/1	79	0/1	129	0/1				29	0-5000	DAC Ch 1	79	0-65535	3e			
Messen	28	30	0/1	80	0/1	130	0/1				30	0-5000	DAC Ch 2	80	0-65535	3f			
Relay Signal T1o0, T1-o1	29	31	0/1	81	0/1	131	0/1				31	0-5000	DAC Ch 3	81	0-65535	40			
Relay BUS0/1	30	32	0/1	82	0/1	132	0/1				32	0-5000	DAC Ch 4	82	0-65535	41			
V+Variable_1	31	33	0/1	83	0/1	133	0/1				33	0-5000	DAC Ch 5	83	0-65535	42			
	32	34	0/1	84	0/1	134	0/1				34	0-5000	DAC Ch 6	84	0-65535	43			
V+Variable_2	33	35	0/1	85	0/1	135	0/1				35	0-5000	DAC Ch 7	85	0-65535	44			
	34	36	0/1	86	0/1	136	0/1				36	0-65535	PowerDown DAC Offset (0-7)	86	0-65535	45			
FET1_N	35	37	0/1	87	0/1	137	0/1				37	0-65535	PowerDown DAC Ref(0) + Reserve (1-7)	87	0-65535	47			
FET1_P	36	38	0/1	88	0/1	138	0/1				38	0-1	Reset Frequenzgenerator-Register (AD9106)	88	0-65535	50			
FET2_N	37	39	0/1	89	0/1	139	0/1				39	0-65535	Fehler Code Frequenzgenerator (AD9106)	89	0-65535	51			
FET2_P	38	40	0/1	90	0/1	140	0/1				40	0-65535	0	90	0-65535	52			
FET3_N	39	41	0/1	91	0/1	141	0/1				41	0-65535	1	91	0-65535	53			
FET3_P	40	42	0/1	92	0/1	142	0/1				42	0-65535	2	92	0-65535	54			
Relay I2C logic_lvl	41	43	0/1	93	0/1	143	0/1				43	0-65535	3	93	0-65535	55			
Reserve	42	44	0/1	94	0/1	144	0/1				44	0-65535	4	94	0-65535	56			
Relay Signal 4o6, 5o7	43	45	0/1	95	0/1	145	0/1				45	0-65535	5	95	0-65535	57			
Relay Signal RIo4o6, RI-o5o7	44	46	0/1	96	0/1	146	0/1				46	0-65535	6	96	0-65535	58			
Relay Signal T2o2, T2-o3	45	47	0/1	97	0/1	147	0/1				47	0-65535	7	97	0-65535	59			
		48	Res.	98	Res.	148	Res.				48	0-65535	8	98	0-65535	5a			
		49	Res.	99	Res.	149	Res.				49	0-65535	9	99	0-65535	5b			