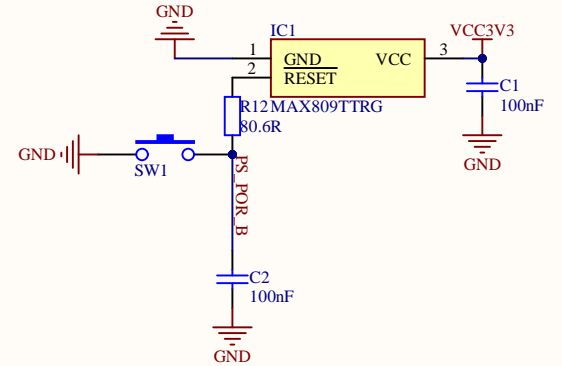
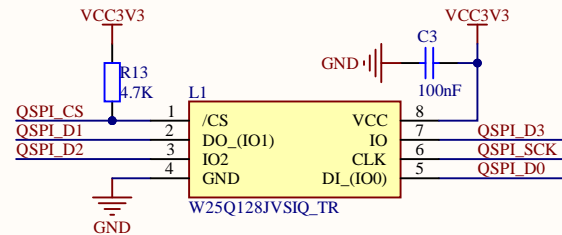
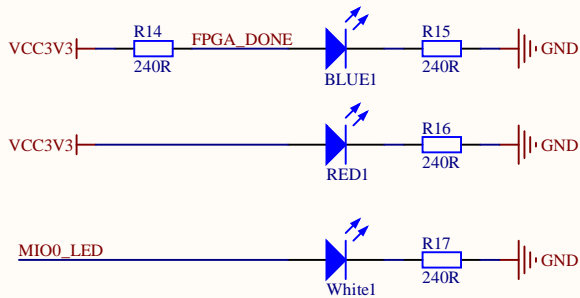


BOOT	开关1	开关2
QSPI	1	0
SD	0	0
JTAG	1	1



ZYNQ7020 BANK0

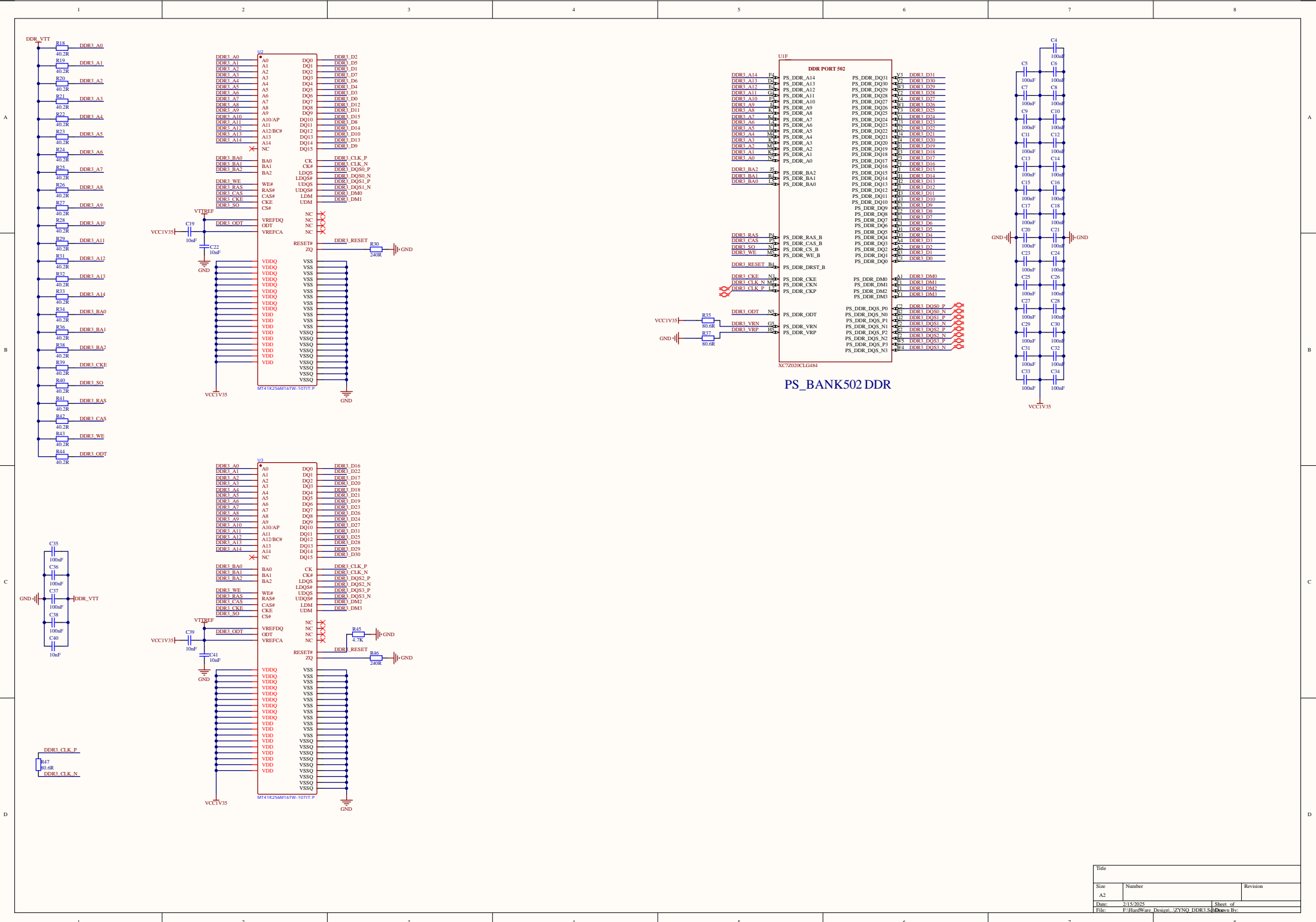
启动模式配置

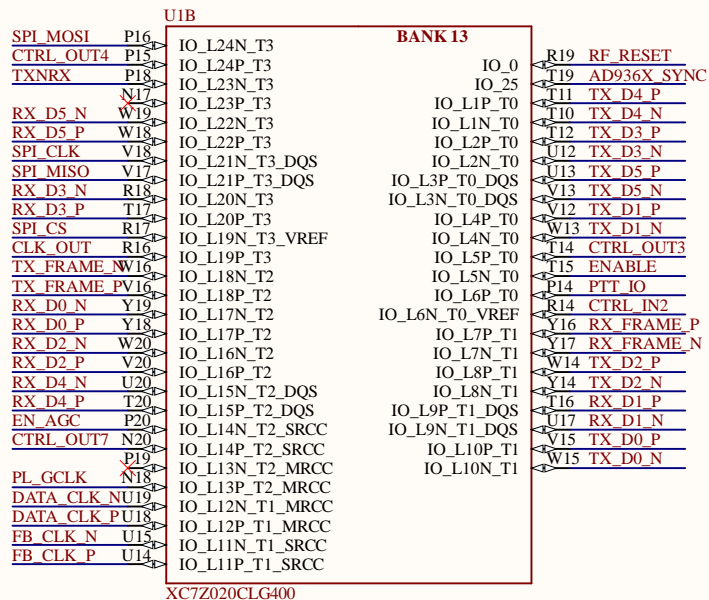


W25Q128 Flash

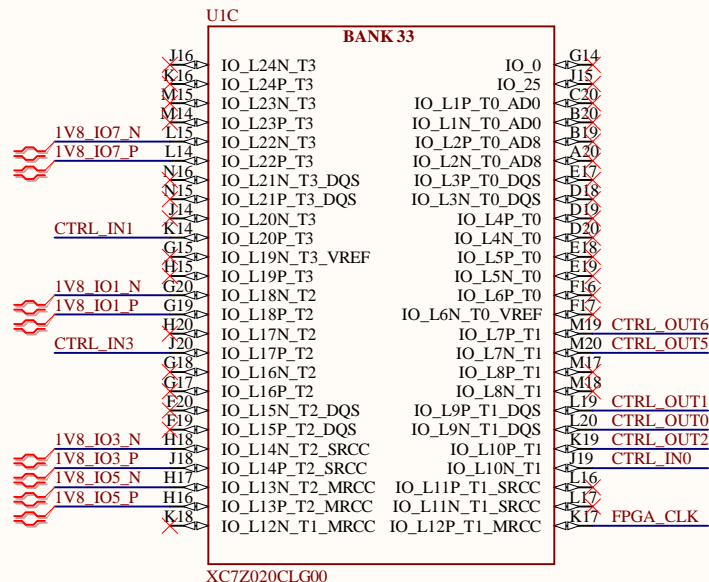
指示灯

Title		
Size A4	Number	Revision
Date: 2/15/2025	Sheet of	
File: F:\HardWare_Design\...\ZYNQ_Core_Sch	Drawn By:	

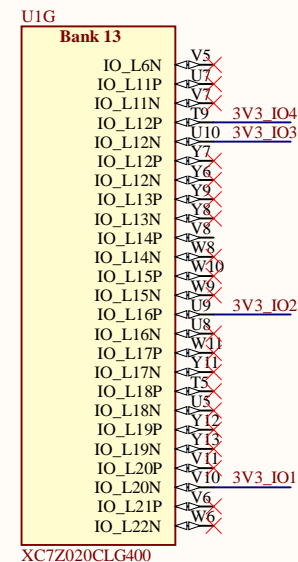




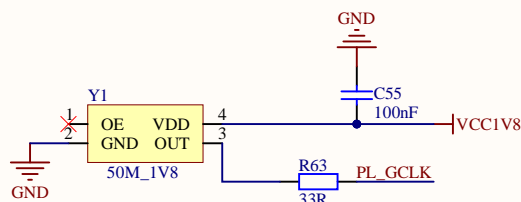
PL端BANK34



PL端BANK35

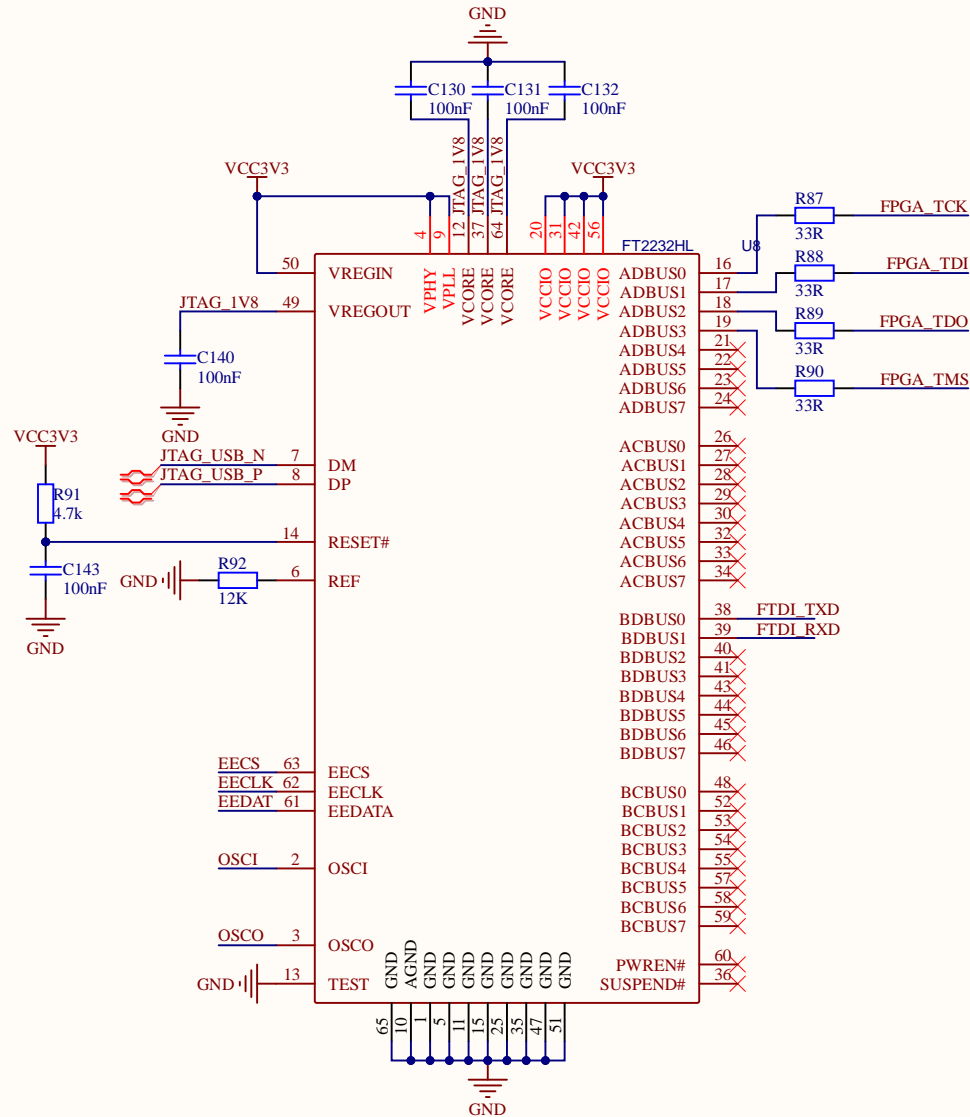
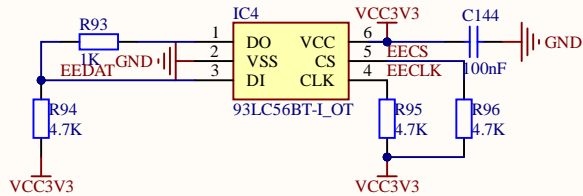
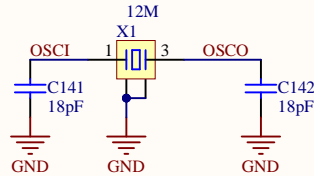
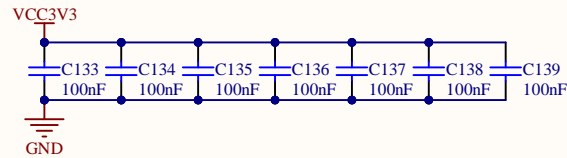


PL端BANK13



PL端时钟50M

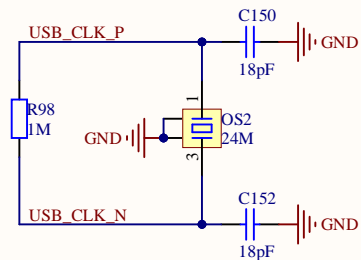
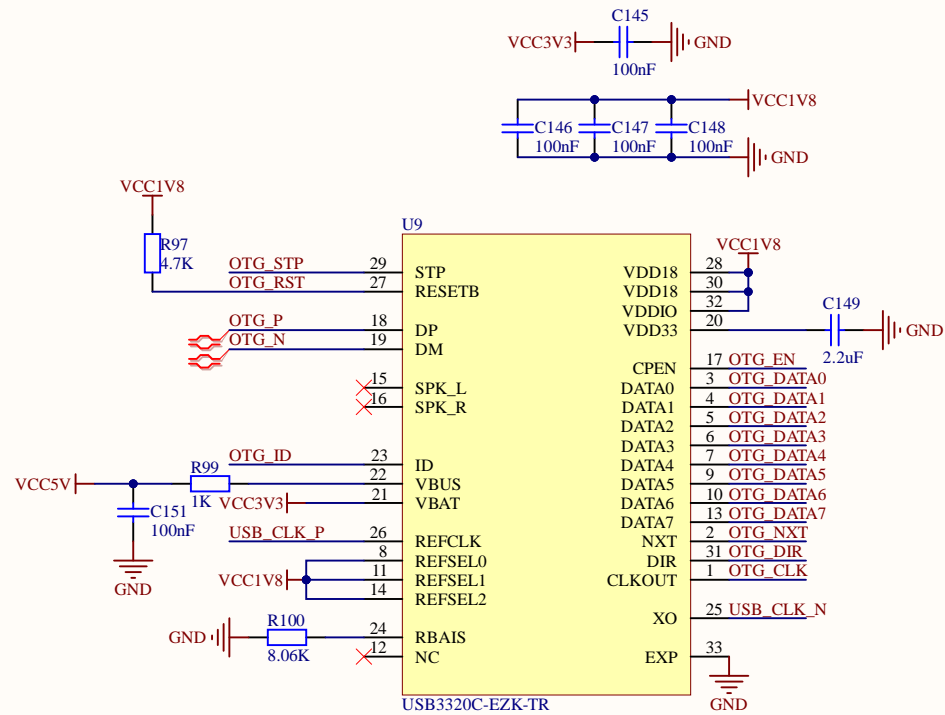
Title		
Size	Number	Revision
A4		
Date:	2/15/2025	Sheet of
File:	F:\HardWare_Design\...\ZYNQ_PL_BANK13SCH.DOC	10



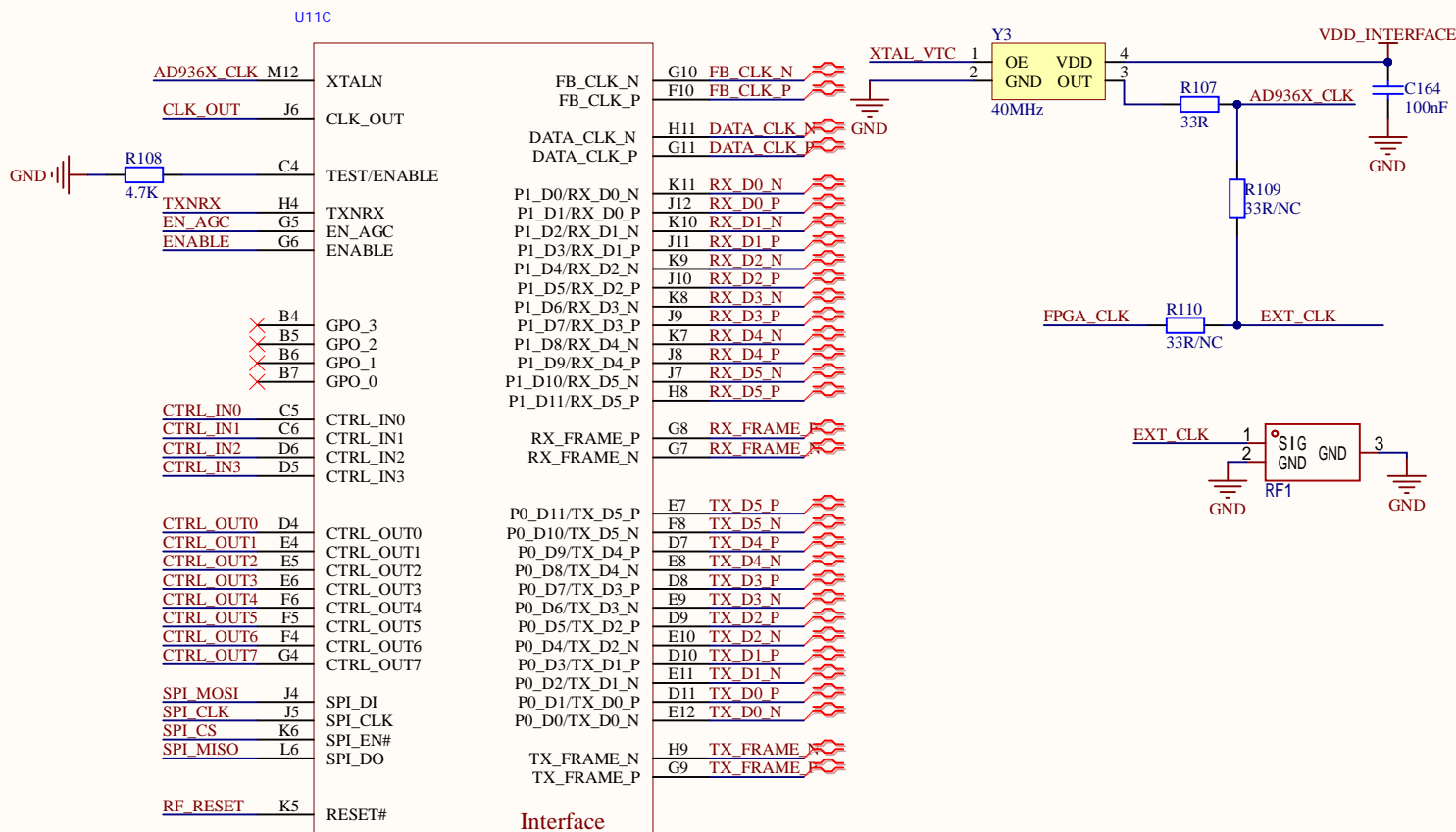
FTDI_TXD UART_RXD

FTDI_RXD UART_TXD

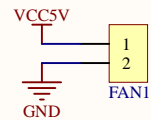
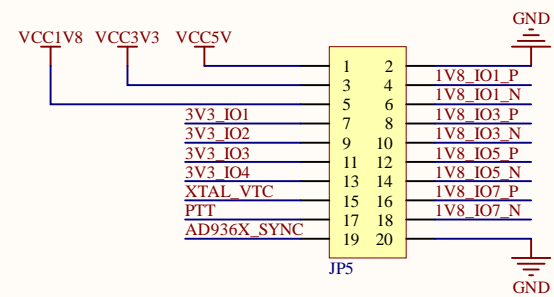
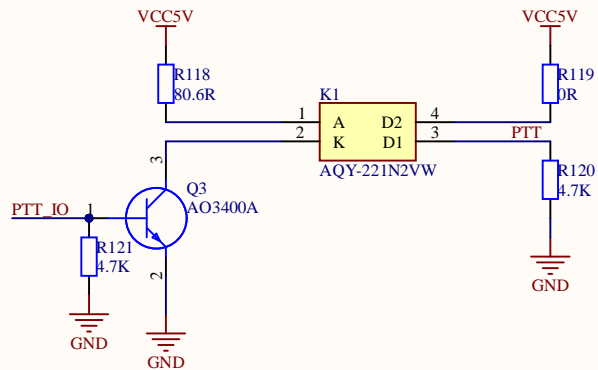
Title		
Size	Number	Revision
A4		
Date:	2/15/2025	Sheet of
File:	F:\HardWare_Design\...\ZYNQ_JTAG.Sch	Down By:



Title		
Size	Number	Revision
A4		
Date:	2/15/2025	Sheet of
File:	F:\HardWare_Design\...\ZYNQ_USB_PHY\...	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	2/15/2025	Sheet of
File:	F:\HardWare_Design\...\AD936X_Digital_Solider By:	



Title		
Size	Number	Revision
A4		
Date:	2/15/2025	Sheet of
File:	F:\HardWare_Design\...\EXT_IO.SchDoc	Drawn By: