

Model-based Software Product Lines

Variability Models (Part 2)

Mathieu Acher

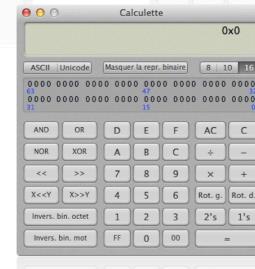
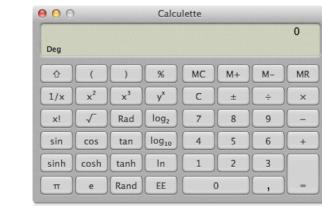
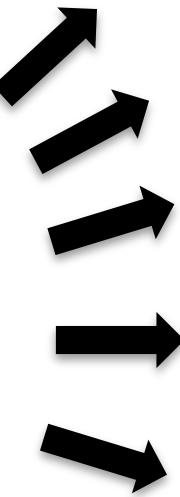
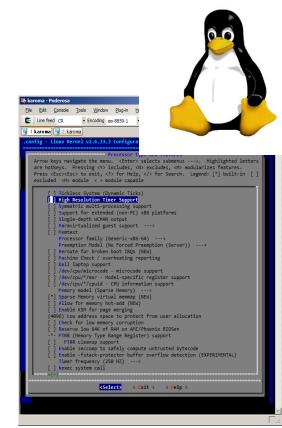
Maître de Conférences

mathieu.acher@irisa.fr

Material

<http://mathieuacher.com/teaching/M2R/>

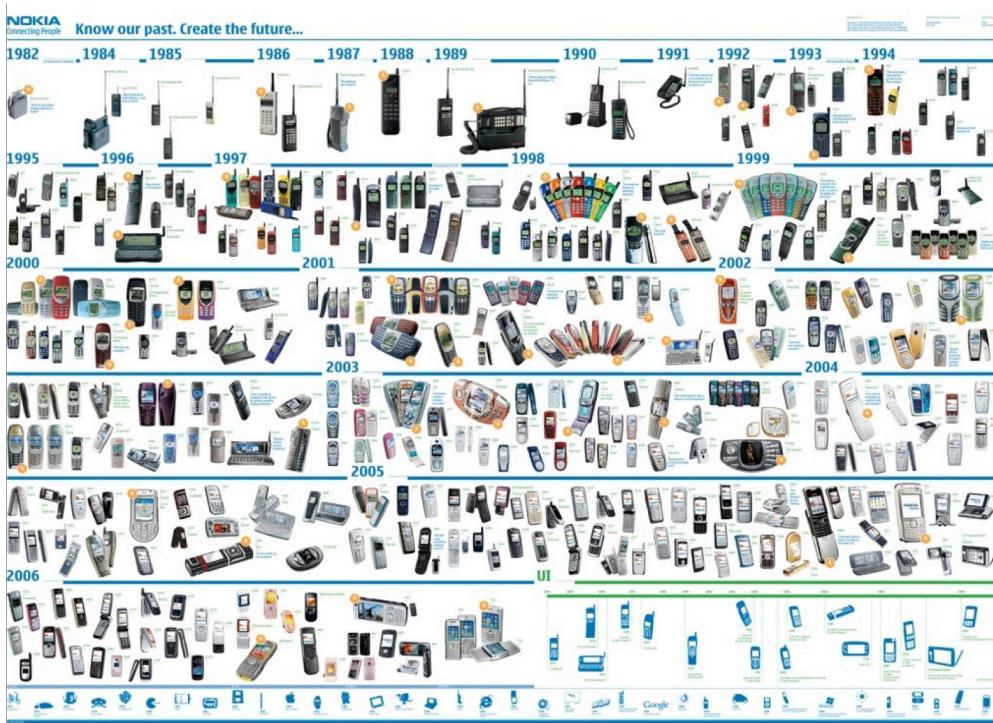
Previously



Linux Kernel



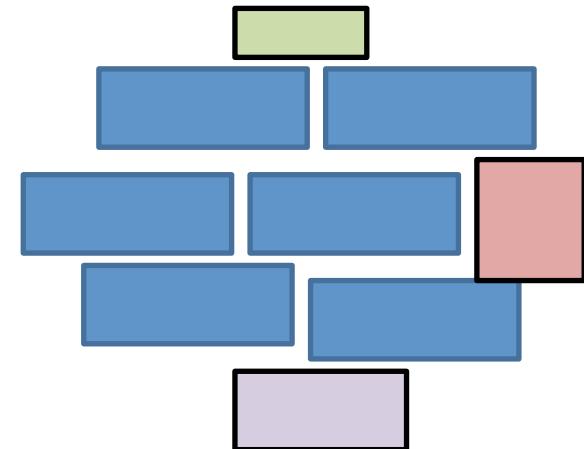
2000 features



Software Product Line Engineering

Factoring out **commonalities**

for **Reuse** [Krueger et al., 1992] [Jacobson et al., 1997]



Managing **variabilities**

for Software **Mass Customization** [Bass et al., 1998] [Krueger et al., 2001], [Pohl et al., 2005]



Variability Management

Common features

print – connect with computer...

Variable features

fax – scan – USB port...

Product-specific features

serial port



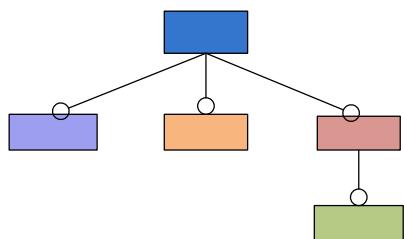
Variability

“the ability of a system to be efficiently extended, changed, customized or configured for use in a particular context”

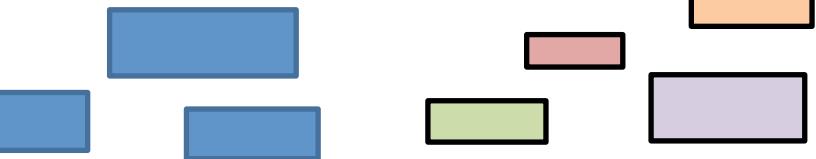
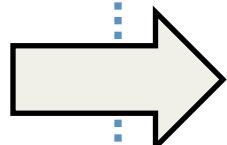
Mikael Svahnberg, Jilles van Gurp, and Jan Bosch (2005)



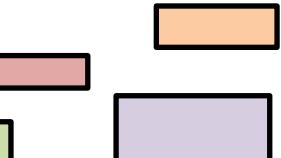
Domain engineering (development for reuse)



Variability Model



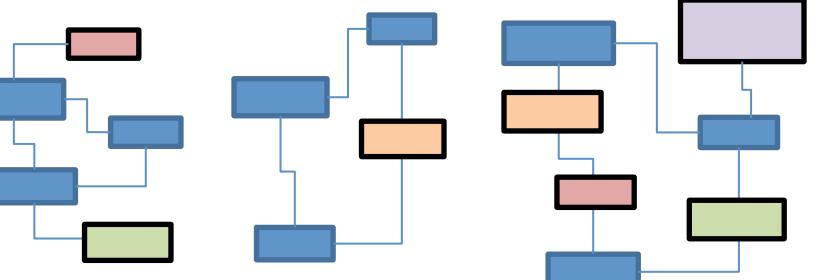
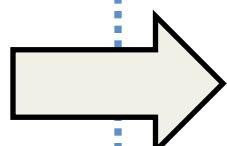
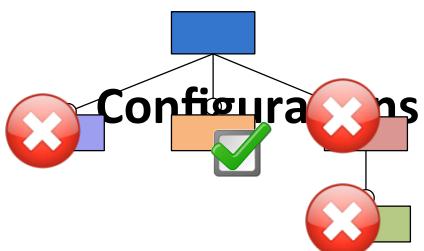
Common assets



Variants

Reusable Assets

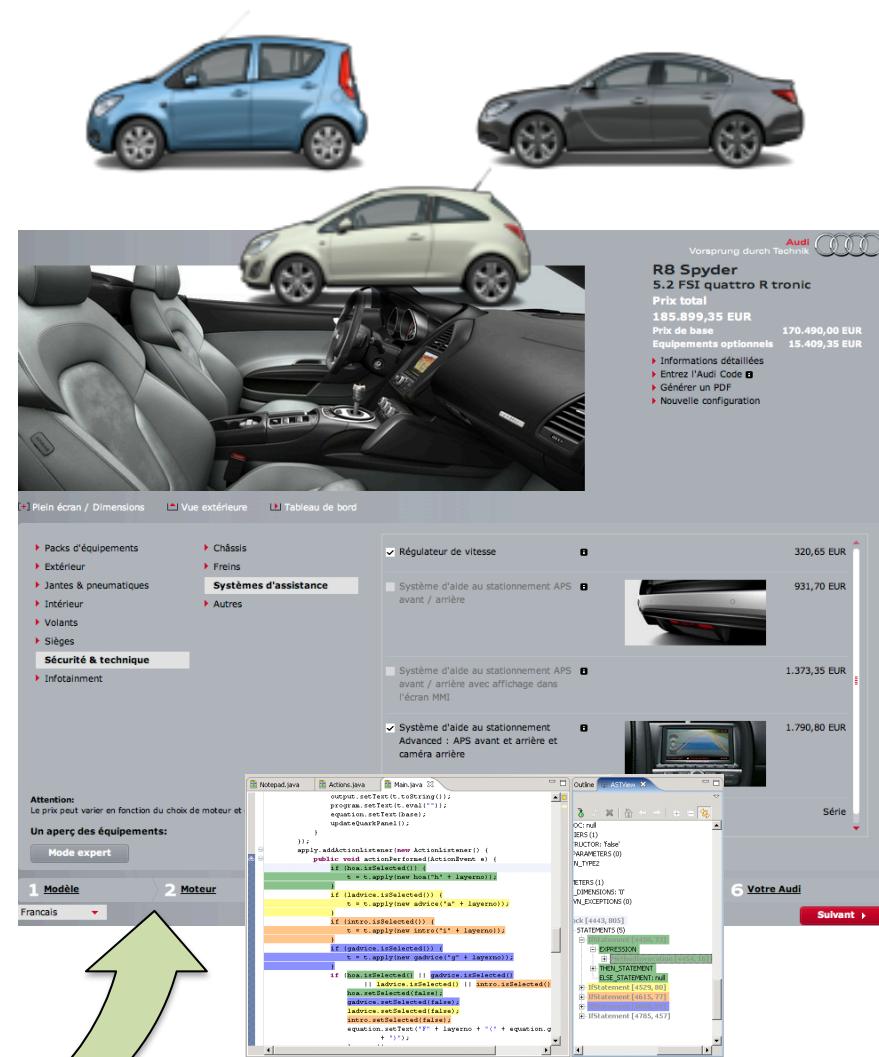
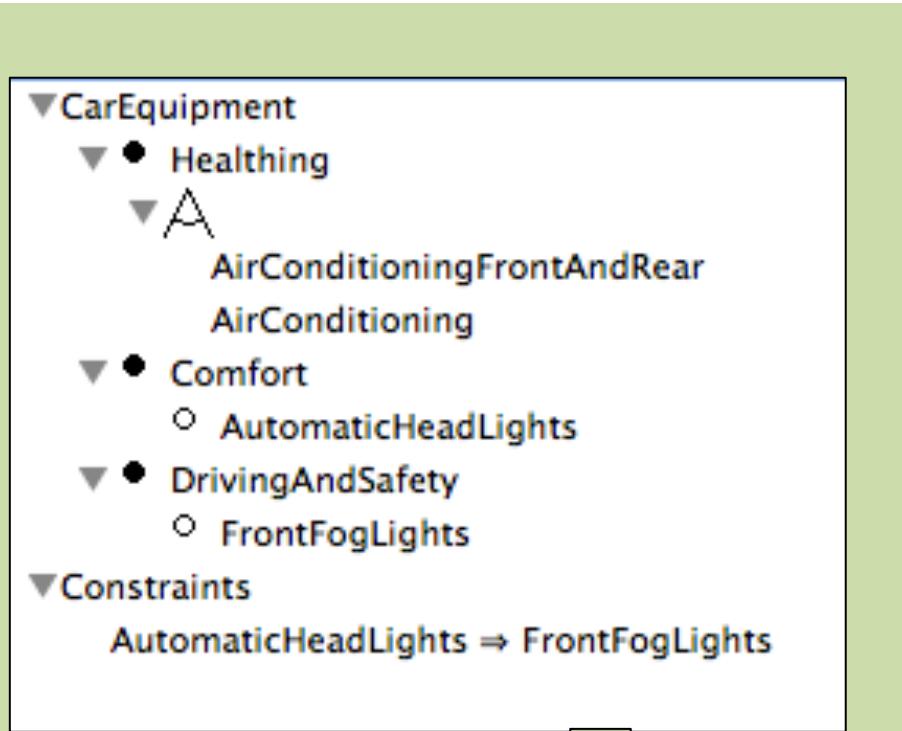
(e.g., models or source code)



Application engineering (development with reuse)

Modeling Variability: Why Should I Care?

Variability Models



#1 precise specification is needed

A screenshot of the karmo configuration tool. It shows a list of kernel features with checkboxes. Some features are grouped under titles like "Processor Type and features". A legend at the bottom indicates: "Arrow keys navigate the menu. <Enter> selects submenus -->. Highlighted letters are hotkeys. Pressing <?> includes, <>> excludes, <Mod> modularizes features. Press <Ctrl>+<Mod>+<Mod> to search. Legend: [+] built-in [] excluded [Mod] module [Mod] <> module capable".

Variability Model



A screenshot of an IDE showing Java code. The code includes annotations for variability analysis, such as "SELECTED" and "EXCLUDED" blocks. The right side of the screen shows a detailed analysis of the code, including statements, expressions, and method invocations.

Modeling variability in main artifacts (e.g., source code)



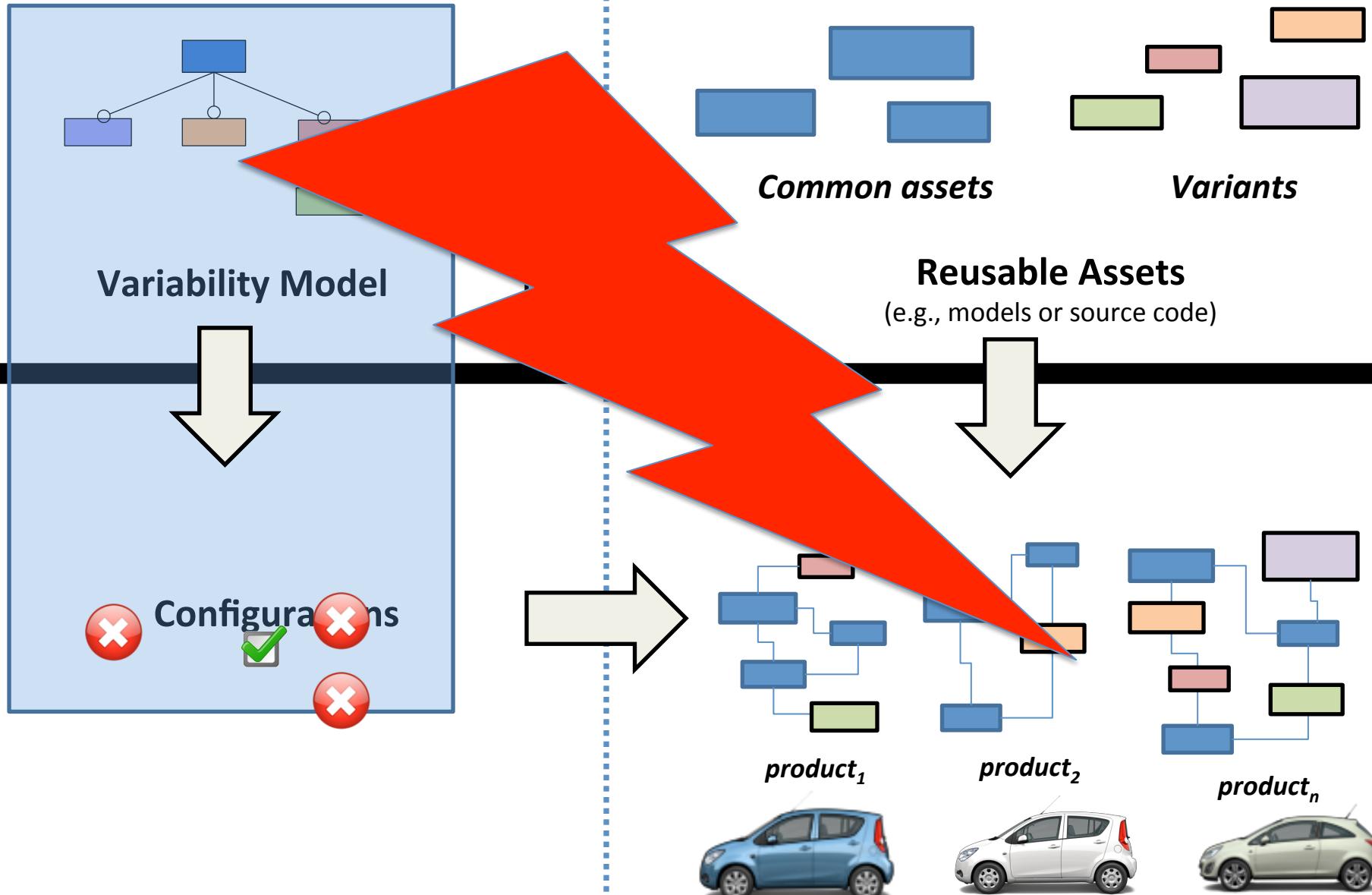
Configuration



is crucial



Domain engineering (development for reuse)



Application engineering (development with reuse)

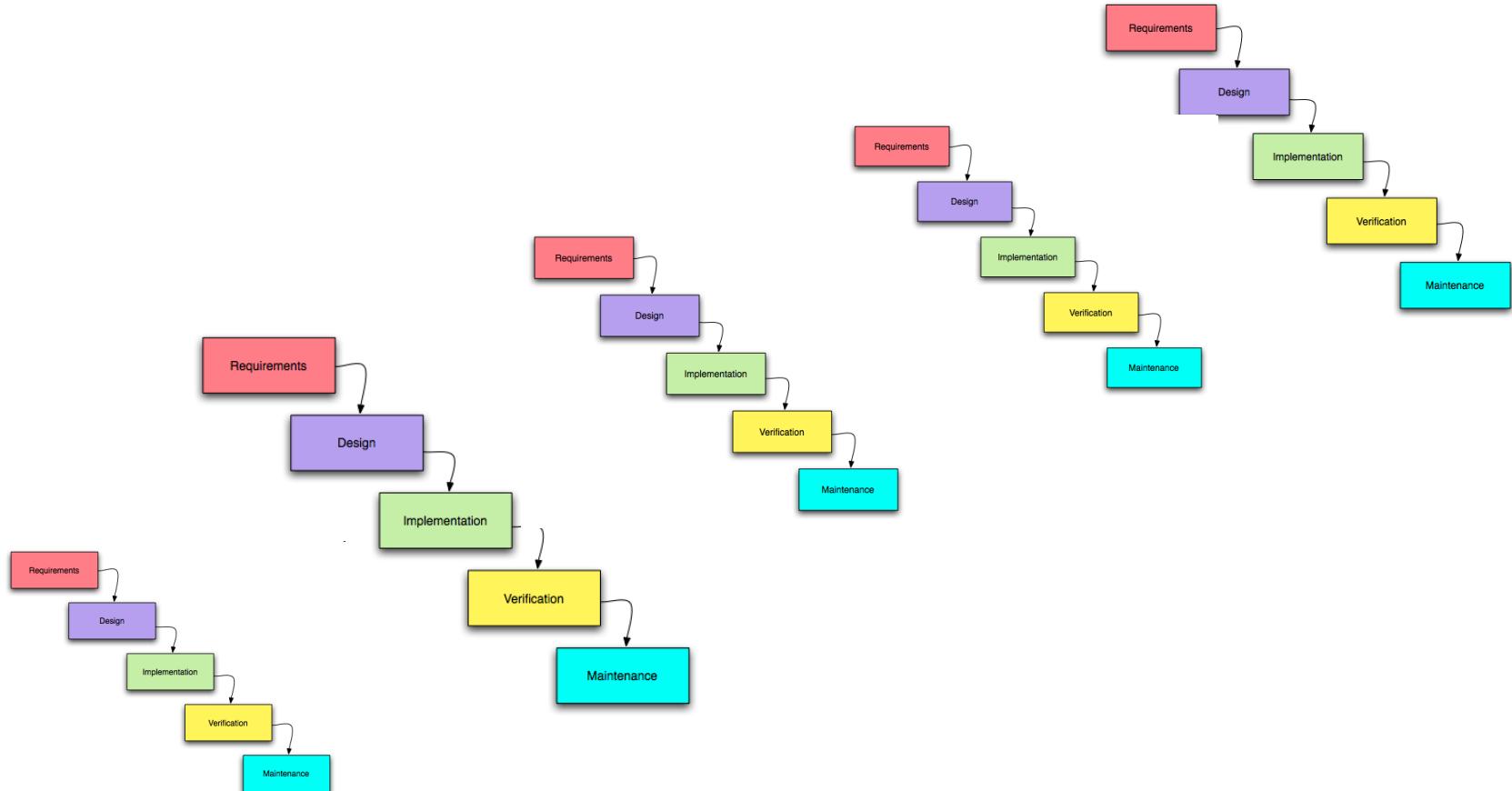


Illegal variant

Unused flexibility



**#2 variability cross cuts all
artefacts (from requirements to
code)**



httpd.conf -- win32 Apache

Building a Web Server, for Windows

Listen 80

ServerRoot "/www/Apache2"

DocumentRoot "/www/webroot"

ServerName localhost:80

ServerAdmin admin@localhost

ServerSignature On

ServerTokens Full

DefaultType text/plain

AddDefaultCharset ISO-8859-1

UseCanonicalName Off

HostnameLookups Off

Error Log	Identifier	License	Language	Storage	LicenseCostFee	RSS	Unicode
PBwiki	Confluence	Commercial	Java	Database	US10	Yes	Yes
MoinMoin	Nolimit		No	No	Yes	Yes	No
DokuWiki	GPL	Python	Files		No	Yes	Yes
PmWiki	GPL2	PHP	Files		No	Yes	Yes
DrupalWiki	GPL2	PHP	Database	Different Licences	Yes	Yes	
TWiki	GPL	Perl	FilesRCS	Community	Yes	Yes	
MediaWiki	GPL	PHP	Database	No	Yes	Yes	

KeepAlive On

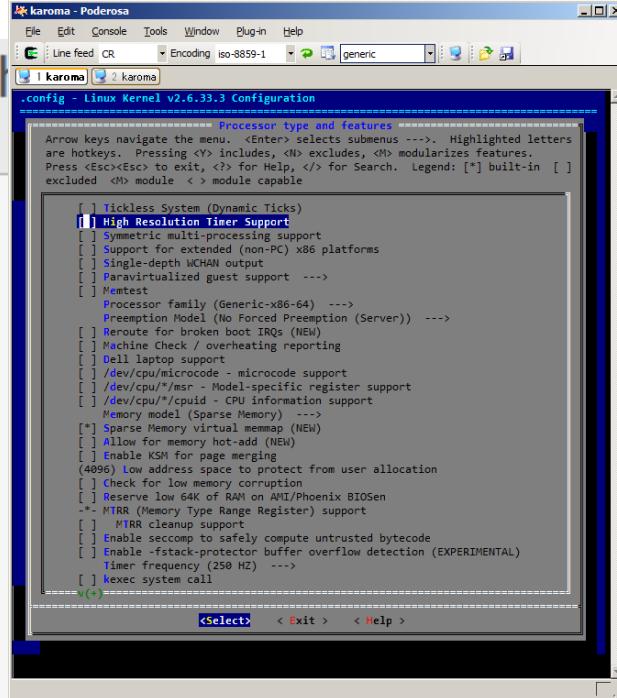
MaxKeepAliveRequests 100

KeepAliveTimeout 15

```
<IfModule mpm_winnt.c>
```

ThreadsPerChild 250

MaxRequestsPerChild 0

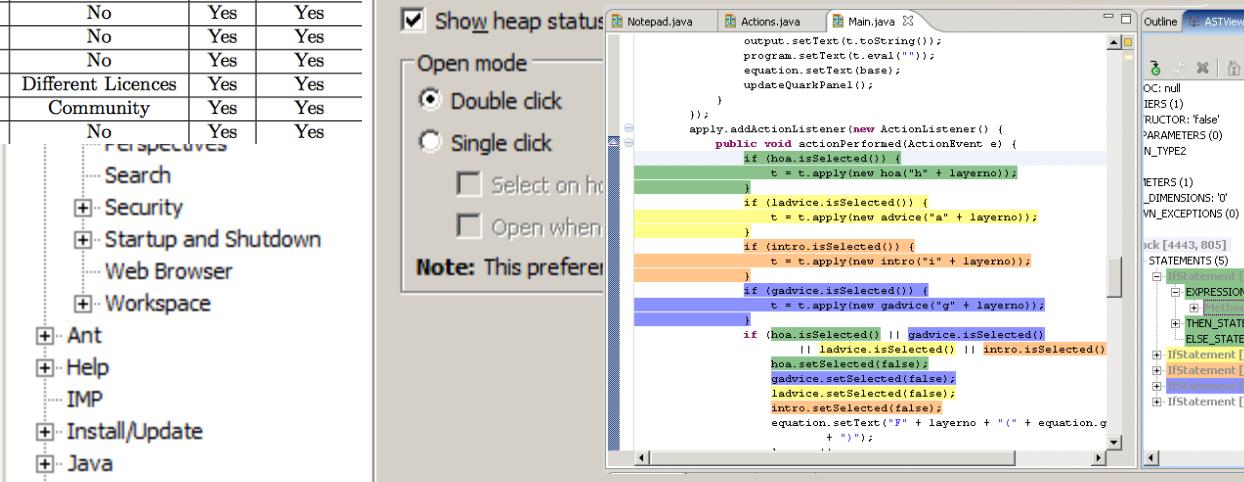
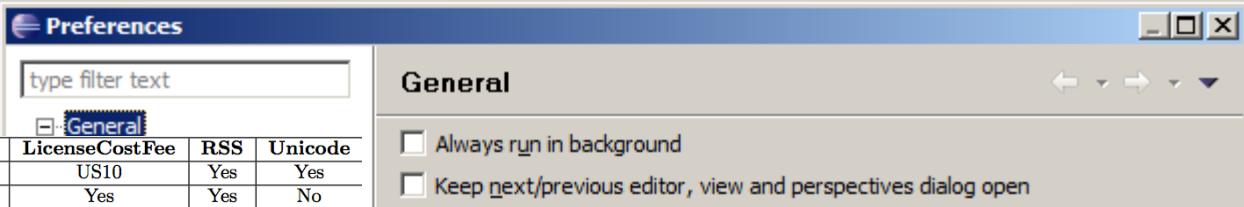


RENAULT VANS

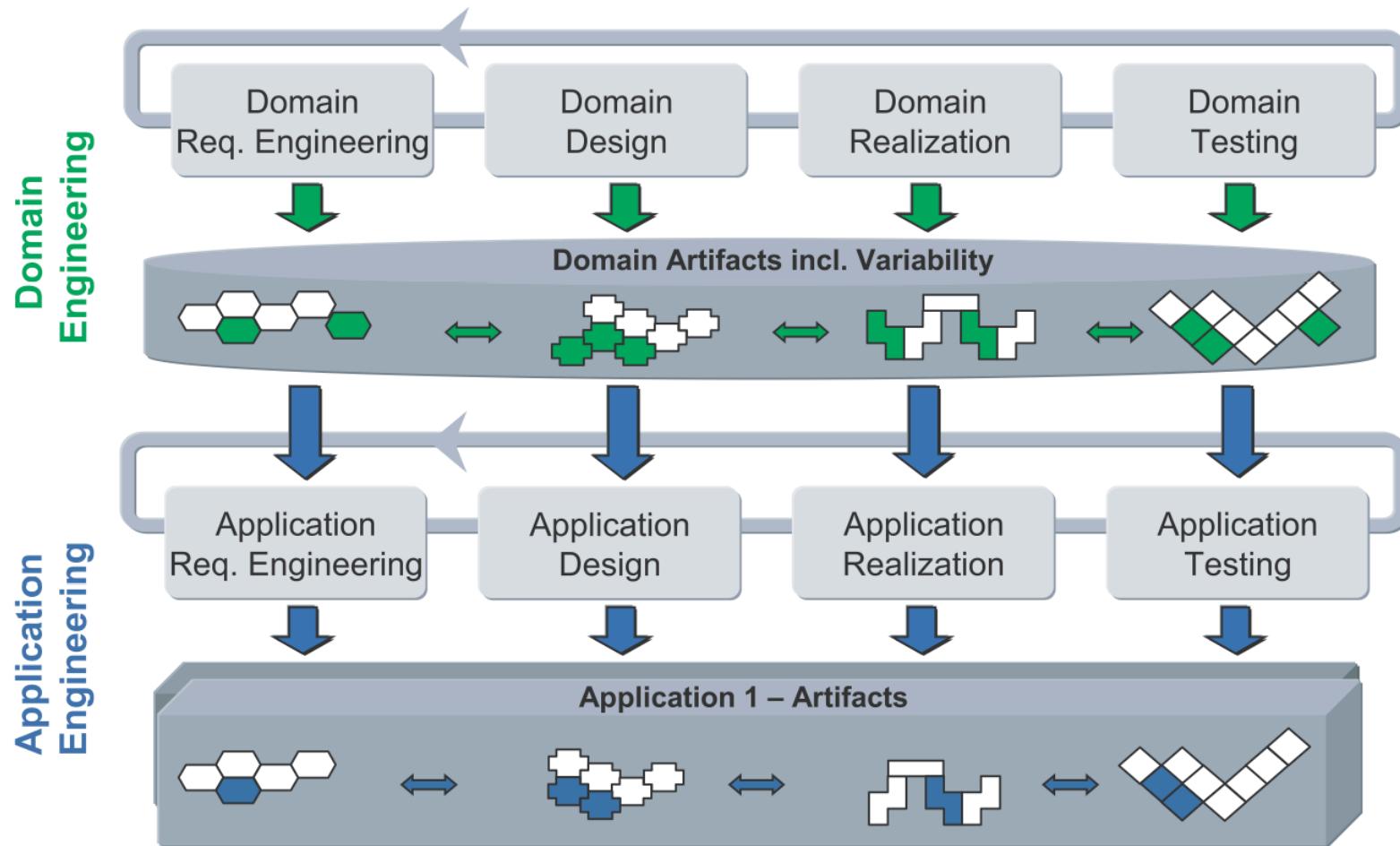
CARS | VANS | ELECTRIC VEHICLES | RENAULT BUSINESS | USED CARS | OWNER SERVICES | ABOUT RENAULT | RENAULT SHOP
Renault UK > Renault Vans > New Kangoo Van Range > Kangoo Van > Build your own Kangoo Van > Selected Options

NEW KANGOO VAN RANGE

A screenshot of the Renault Vans 'Build your own Kangoo Van' configuration page. It shows three tabs: '01 Preferences', '02 Version', and '03 Equipment & options'. Under 'OPTIONS', there are sections for 'COMFORT', 'DRIVING', and 'SAFETY & SECURITY' with various checkboxes and price tags. To the right is an image of a white Renault Kangoo van.



Software Product-Line Engineering



#3 automation (exponential number of products)

Variability = Complexity

33 features



a unique variant for every
person on this planet

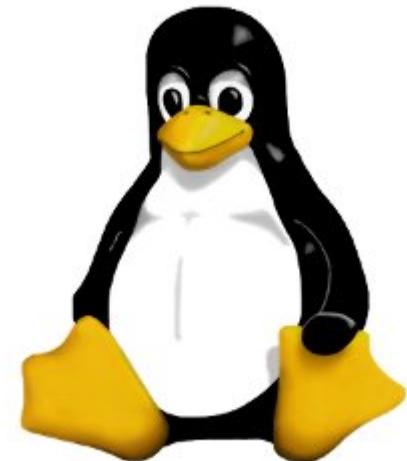
320^{optional, independent}
features

more variants than estimated
atoms in the universe



2000 features

10000
features



Variability Model

- **Abstraction** of some aspect of a system under study (SUS)
 - Some details are hidden or removed to **simplify** and focus attention
 - **general** concepts can be formulated by abstracting common properties of instances
- In a form that can be **mechanically analyzed**
 - properties of a SUS
 - model transformation
 - Generation of another abstraction (including code)

Variability Models: Why?

(3 arguments, summary)

precise specification

variability is orthogonal / cross cuts different
artefacts

automation

Variability models are needed. Now we are convinced...

#1 Formalism? Theory?
modeling principles, semantics, logics

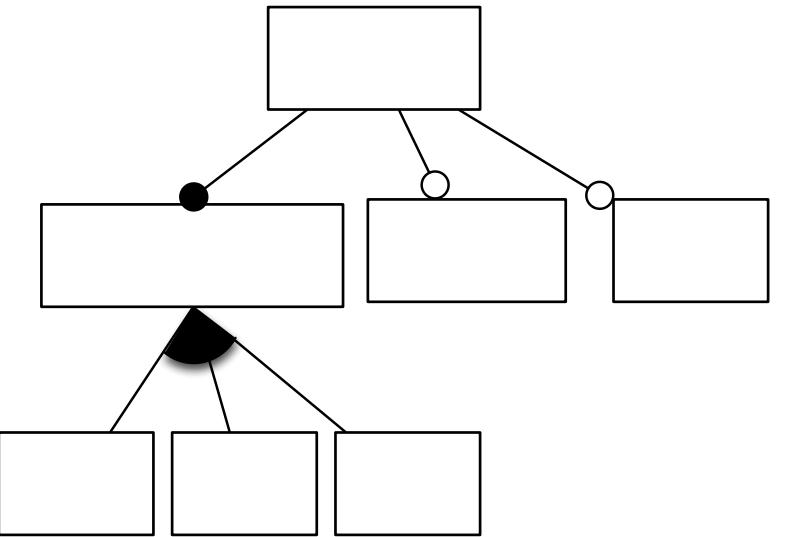
#2 Language?
to elaborate/build variability models

#3 Reasoning techniques?
to analyze properties of an SPL (scalable and automated way)

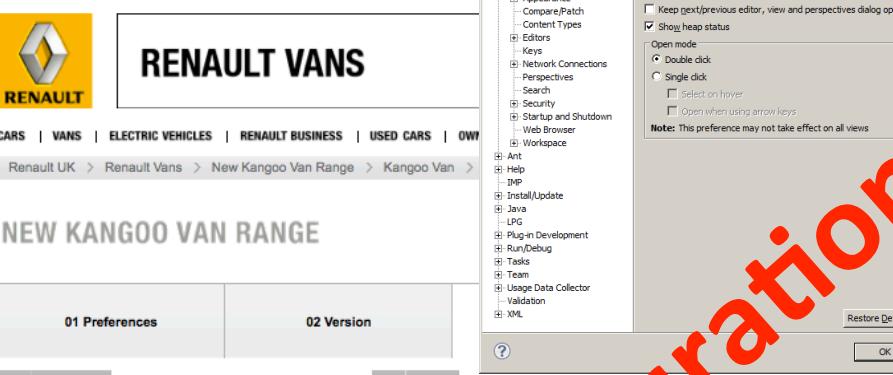
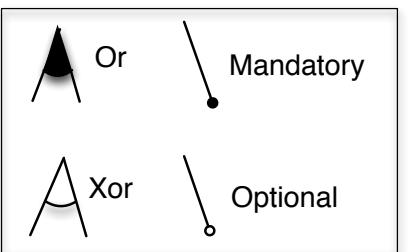
#4 Applications of variability models?
variability model and so what?

Feature models,
the defacto standard
for modeling variability

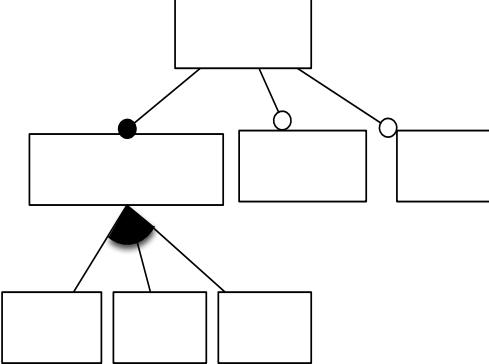
Feature Models



not, and, or, implies

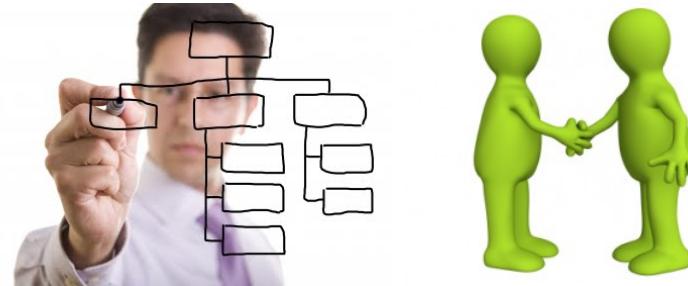


Feature Model

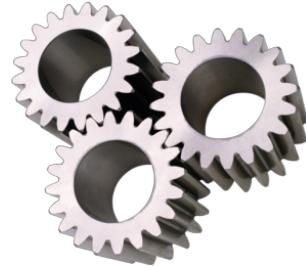


not, and, or, implies

Communicative



Analytic



Generative



Feature Model

de facto standard

- Research
 - 2500+ citations of [Kang et al., 1990] on Google Scholar
 - Central to many generative approaches
 - at requirements or code level
 - Tools & Languages (GUIDSL/FeatureIDE, SPLOT, FaMa, FAMILIAR)
- Industry
 - Tools (Gears, pure::variants),
- Common Variability Language (CVL)



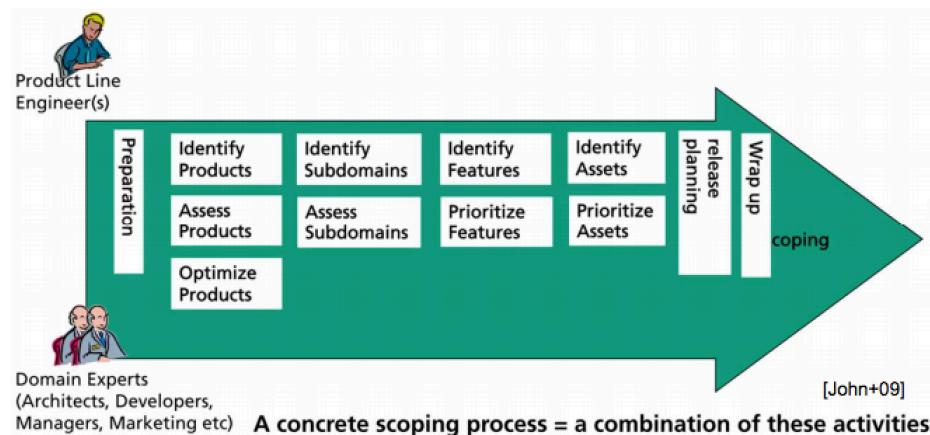
Feature Model: Applications

- Domain Analysis
- Configurators
- Automated Derivation of “Product”
- Testing
- Model Checking

Feature Models: Principles

Feature modeling is...

- ... about identifying
 - common features of concepts
 - variable features of concepts
 - and their dependencies
 - and documenting them in a coherent model, called a feature model
- Core activity of domain-engineering methods



Feature and Psychology

- The human mind processes lots of information
- Important function of human information processing: reducing the amount of information
- Describing concepts by **features**
 - Clustering
 - Abstraction (Classification/Generalization)
 - Vocabulary construction
- Classifying a perceptual unit as an instance of a concept
- Analysis of similarity
 - Features **common** to entities, activities, events, relationships, structures, etc.
- Analysis of variations
 - Features that are **unique** to entities, activities, events, relationships, structures, etc.

Modeling variability (and commonality)

- **Features** are a convenient way to think about commonalities and differences between family members
 - e.g. "MP3 player" – some phones have it, some don't
- Features are « usually » **coarse-grained abstractions**
 - Abstract from detailed requirements
 - e.g. "when in shuffle mode, the player selects the next track using a random function"
 - Abstract from design/implementation details
 - e.g. "MP3_Player is a concrete subclass of Media_Player »
- Depends on what you are modeling and for which purpose
 - Eliciting requirements with customers (informal document)
 - VS building a representation of your code (based on CPP directives)

What's a **feature**? (survey excerpt)

- [Kang et al.] “a prominent or distinctive **user-visible aspect, quality or characteristic** of a software system or systems”
- [Kang et al.] “distinctively identifiable **functional abstractions** that must be implemented, tested, delivered, and maintained”
- [Eisenecker and Czarnecki]. “**anything users** or client programs might want to **control about a concept**”
- [Bosch et al.] “A **logical unit of behaviour** specified by a set of **functional and non-functional requirements.**”
- [Chen et al.] “a **product characteristic** from user or customer views, which essentially consists of a cohesive **set of individual requirements**”
- [Batory] “an elaboration or augmentation of an entity(s) that introduces a **new service, capability** or relationship”

Requirements to Code

Feature Models: Syntax



R8 Spyder

5.2 FSI quattro R tronic

Prix total

171.216,00 EUR

Prix de base

170.490,00 EUR

Equipements optionnels

726,00 EUR

- ▶ Informations détaillées
- ▶ Entrez l'Audi Code
- ▶ Générer un PDF
- ▶ Nouvelle configuration



[+] Plein écran / Dimensions

▶ Fermer la capote

Habitacle

Tableau de bord

Packs

Aucun pack n'est proposé pour ce modèle.

Couleurs

Blanc Ibis

Noir

Prix: 0,00 EUR



Couleurs métallisées à partir de 0,00 EUR



Couleurs à effet perlé à partir de 0,00 EUR

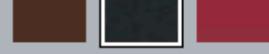


Couleurs personnalisées Audi exclusive



Couleur capote

Noir



Jantes

4 Jantes alu 5 BRANCHES ROTOR finition titane 8,5 x 19 à l'avant, 11 x 19 à l'arrière. Pneus 235/35 R19 à l'avant et 305 /30 R19 à l'arrière

Prix: 726,00 EUR

19" à partir de 0,00 EUR





R8 Spyder 5.2 FSI quattro R tronic

Prix total

185.899,35 EUR

Prix de base

170.490,00 EUR

Equipements optionnels

15.409,35 EUR

▶ Informations détaillées

▶ Entrez l'Audi Code

▶ Générer un PDF

▶ Nouvelle configuration

[+] Plein écran / Dimensions [+] Vue extérieure [+] Tableau de bord

- ▶ Packs d'équipements
- ▶ Extérieur
- ▶ Jantes & pneumatiques
- ▶ Intérieur
- ▶ Volants
- ▶ Sièges
- Sécurité & technique**
- ▶ Infotainment

- ▶ Châssis
- ▶ Freins
- Systèmes d'assistance**
- ▶ Autres

excludes



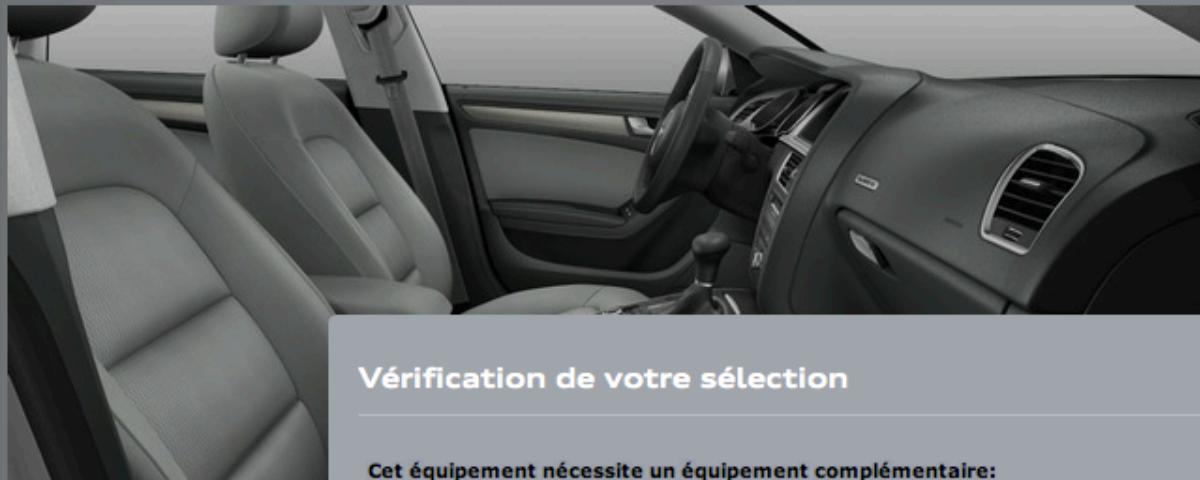
<input checked="" type="checkbox"/> Régulateur de vitesse	320,65 EUR
<input type="checkbox"/> Système d'aide au stationnement APS avant / arrière	931,70 EUR
<input type="checkbox"/> Système d'aide au stationnement APS avant / arrière avec affichage dans l'écran MMI	1.373,35 EUR
<input checked="" type="checkbox"/> Système d'aide au stationnement Advanced : APS avant et arrière et caméra arrière	1.790,80 EUR
<input checked="" type="radio"/> Audi hill assist : assistance au démarrage en côte	Série
<input type="checkbox"/> Réinitialiser la sélection	

Attention:

Le prix peut varier en fonction du choix de moteur et des équipements.

Un aperç des équipements:

Mode expert



A5 Sportback 3.0 TDI quattro S tronic

Prix total

54.460,15 EUR

Prix de base

50.570,00 EUR

Equipements optionnels

3.890,15 EUR

▶ Informations détaillées

▶ Entrez l'Audi Code

▶ Nouvelle configuration

Vérification de votre sélection

Cet équipement nécessite un équipement complémentaire:

GPS Plus avec disque dur



2.934,25 EUR

Voici les équipements complémentaires possibles:

Ordinateur de bord en couleur avec programme efficiency



181,50 EUR

Remarque: uniquement sur les modèles avec système Start-Stop et uniquement disponible en combinaison avec l'autoradio Concert, l'autoradio Symphony ou un système de navigation

Pack Intenso Plus



3.100,00 EUR

Sans appareil de navigation

Série

[+] Plein écran / Dimensions

- ▶ Packs d'équipements
- ▶ Extérieur
- ▶ Jantes & pneumatiques
- ▶ Intérieur
- ▶ Volants
- ▶ Sièges
- ▶ Sécurité & technique

Infotainment

Attention:

Le prix peut varier en fonction du choix de moteur et des équipements.

Un aperç des équipements:

Mode expert

Réinitialiser la sélection

1 Modèle

2 Moteur

3 Extérieur

4 Intérieur

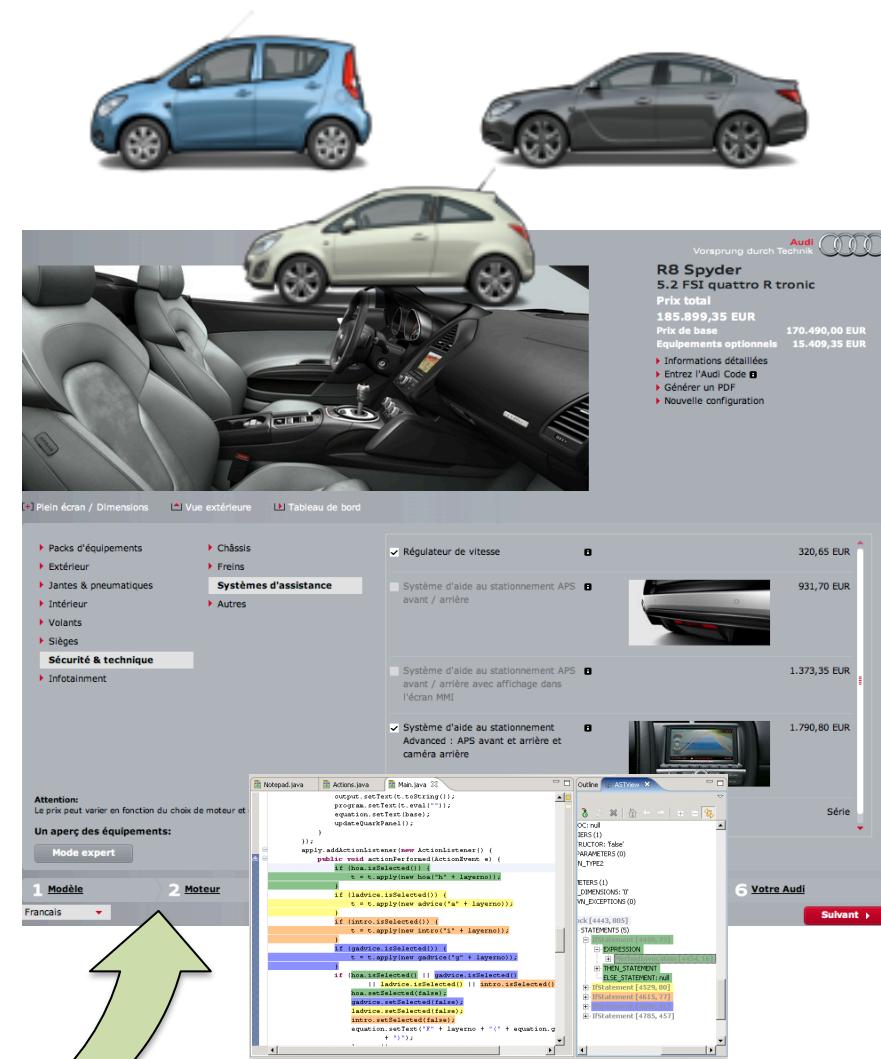
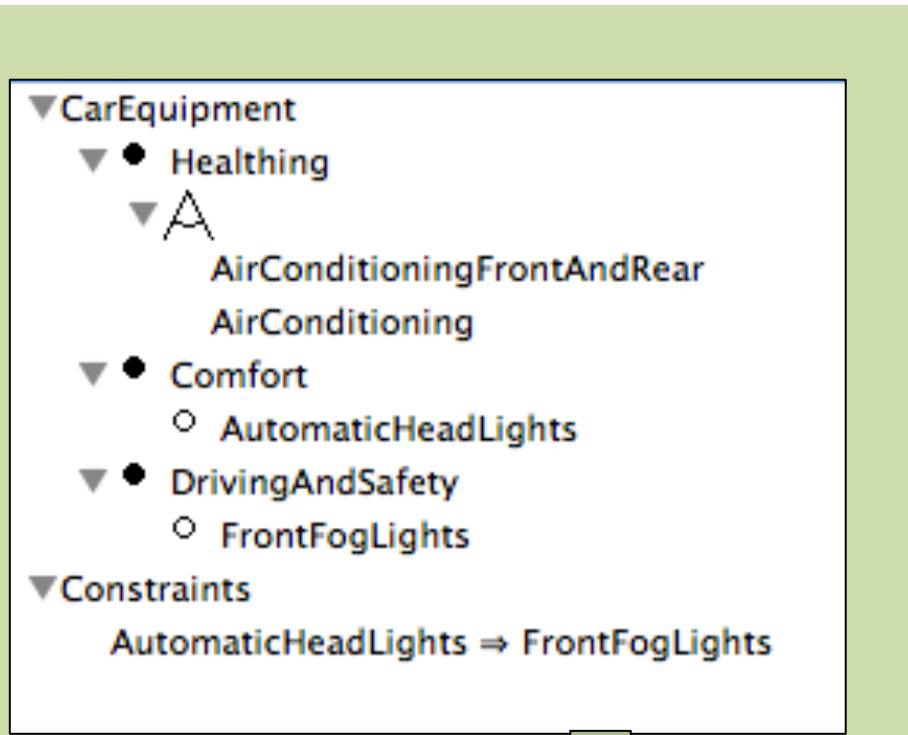
5 Option

6 Votre Audi

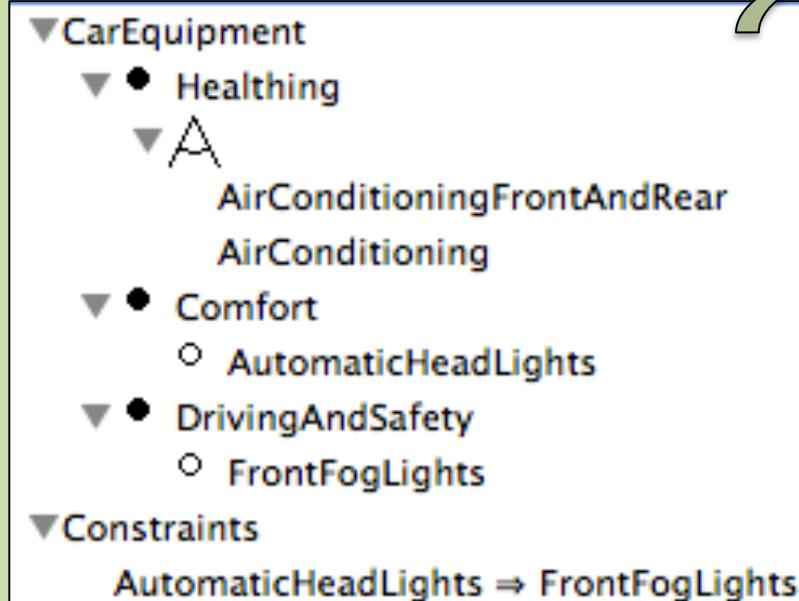
Français ▾

Suivant ▶

Feature Models



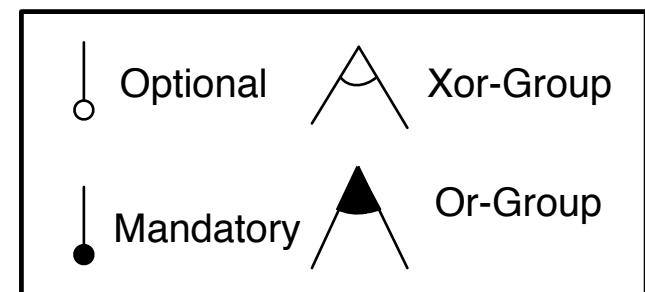
Feature Models (Background)

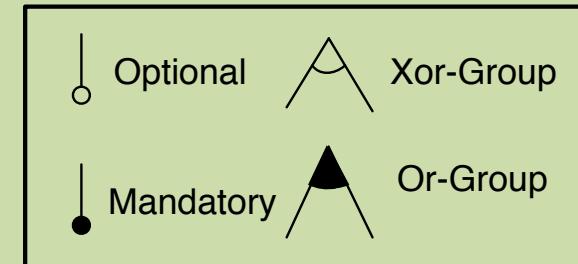
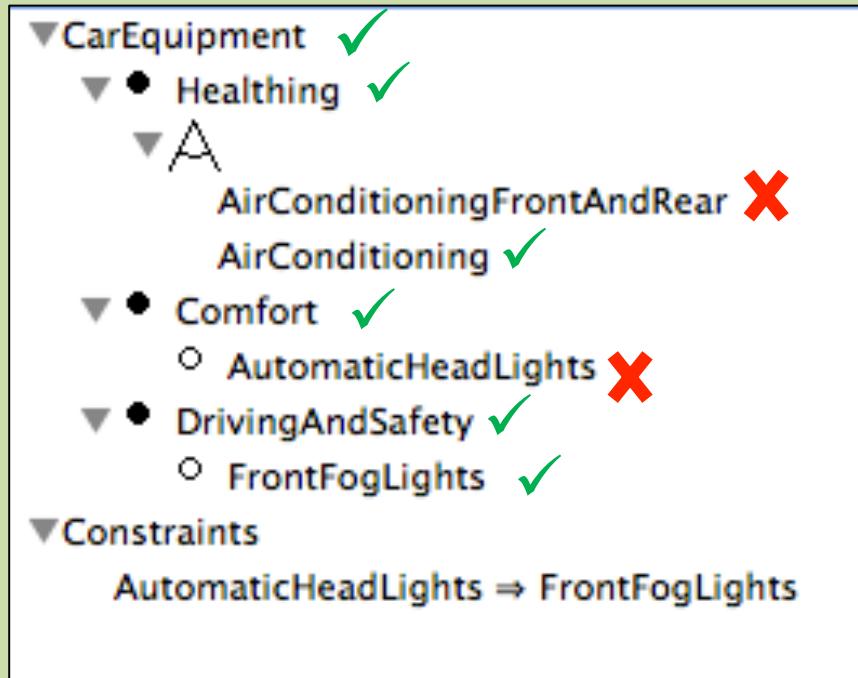


Hierarchy: rooted tree

Variability:

- mandatory,
- optional,
- Groups: exclusive or inclusive features
- Cross-tree constraints





Hierarchy + Variability

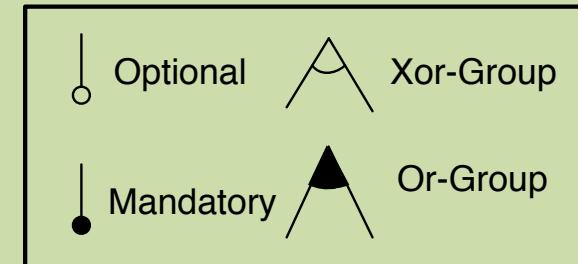
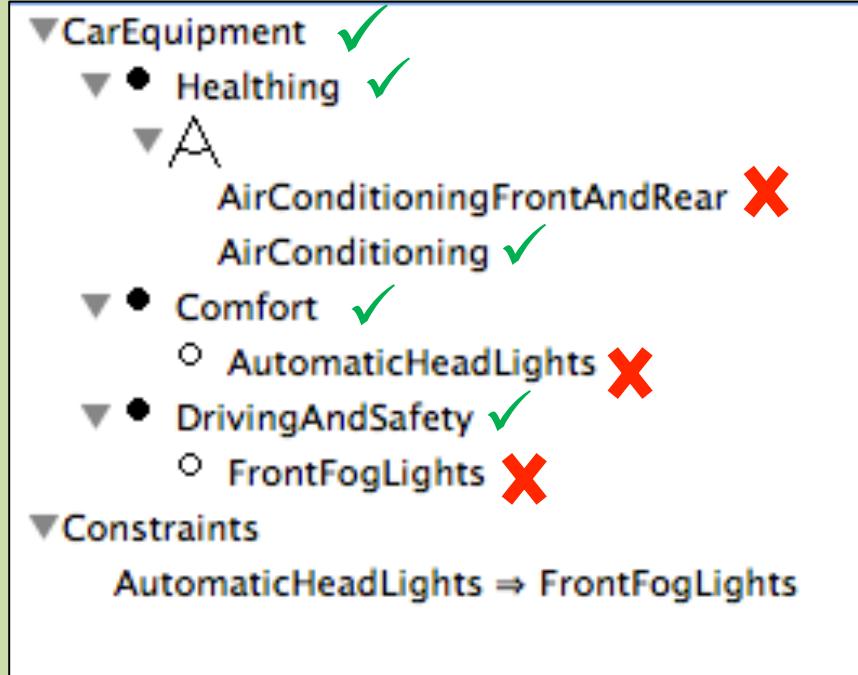
=

set of valid configurations

configuration = set of features selected

{CarEquipment, Comfort, DrivingAndSafety, Healthing, AirConditioning, FrontFogLights}





Hierarchy + Variability

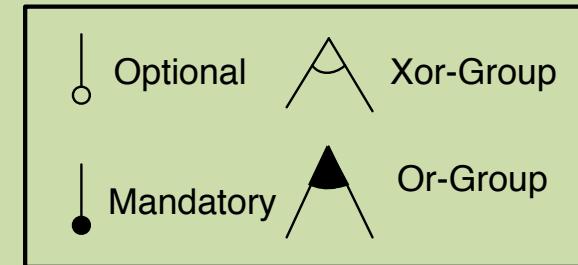
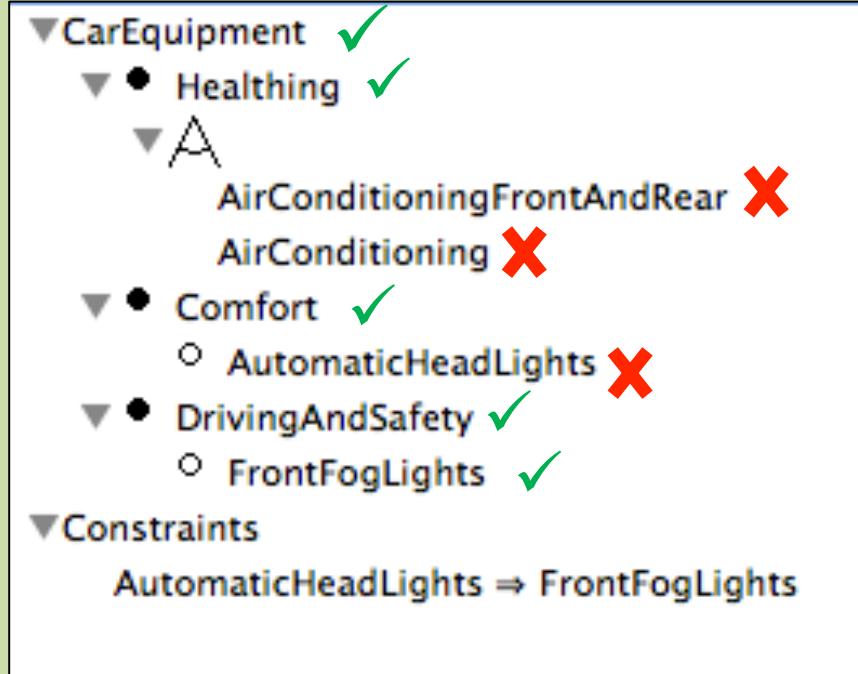
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set of valid configurations

configuration = set of features selected

{CarEquipment, Comfort, DrivingAndSafety, Healthing, AirConditioning}





Hierarchy + Variability

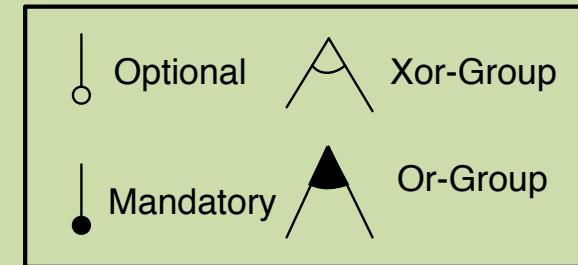
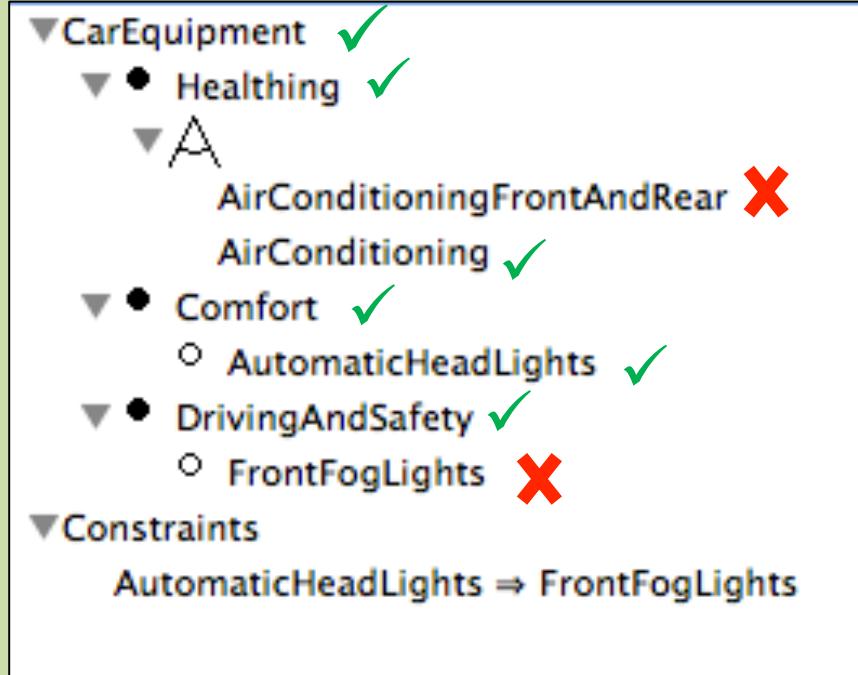
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set of valid configurations

configuration = set of features selected

{CarEquipment, Comfort, DrivingAndSafety, Healthing, AirConditioning, AirConditioningFrontAndRear, FrontFogLights}





Hierarchy + Variability

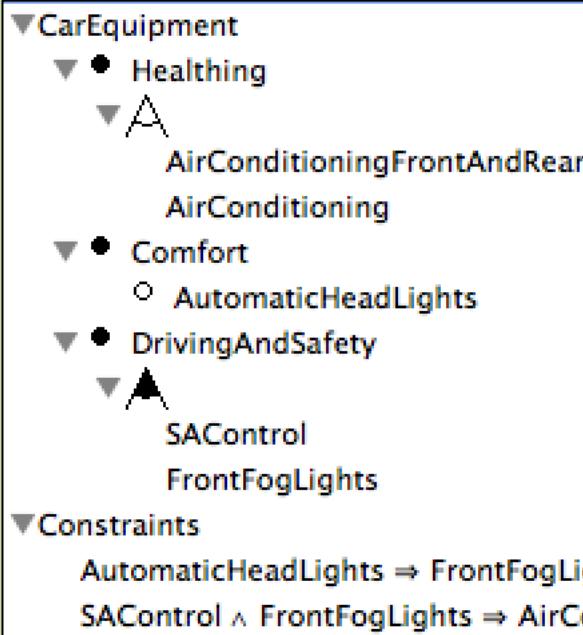
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set of valid configurations

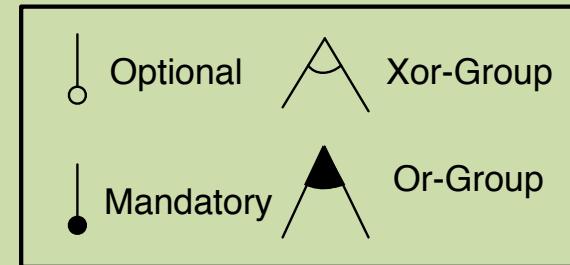
configuration = set of features selected

{CarEquipment, Comfort, DrivingAndSafety, Healthing, AirConditioning, AutomaticHeadLights}





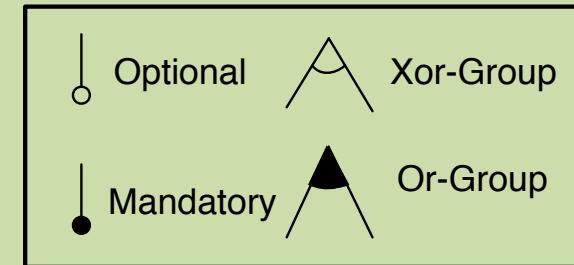
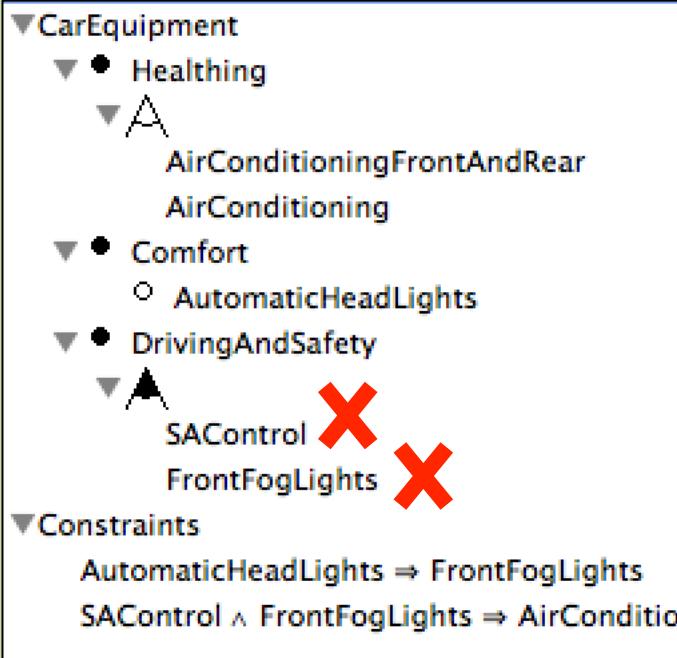
Boolean logic: \wedge , \vee , not, implies



Hierarchy + Variability
=

set of valid configurations





Hierarchy + Variability

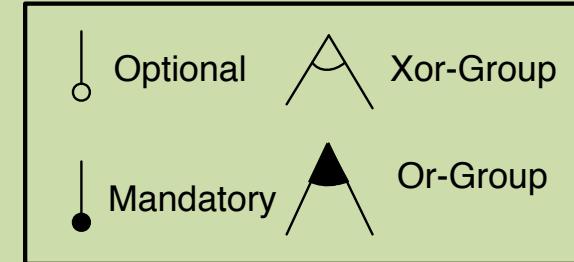
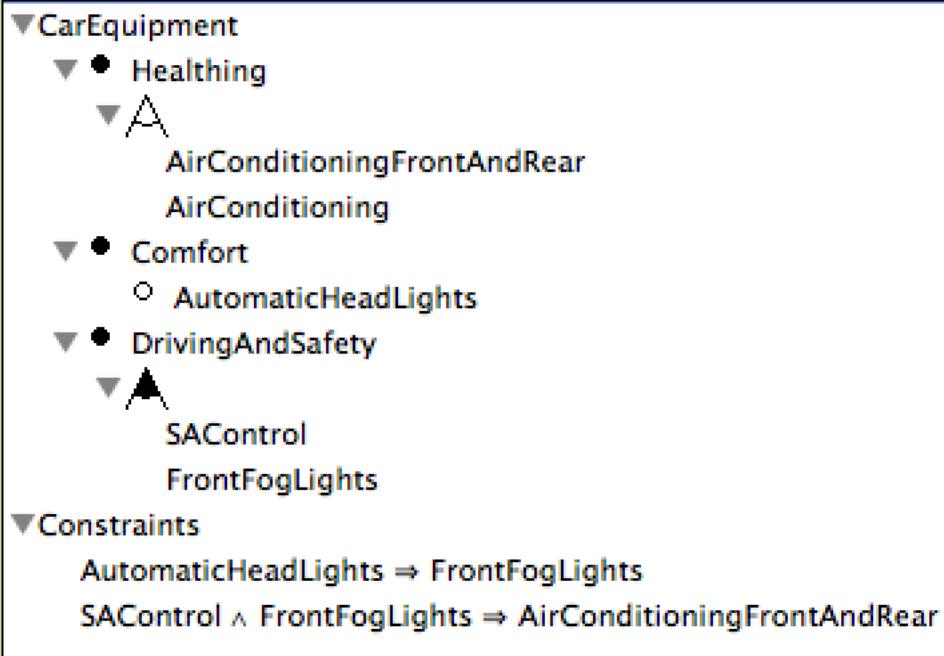
=

set of valid configurations



Or-group: at least one!

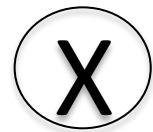




Hierarchy + Variability

=

set of valid configurations



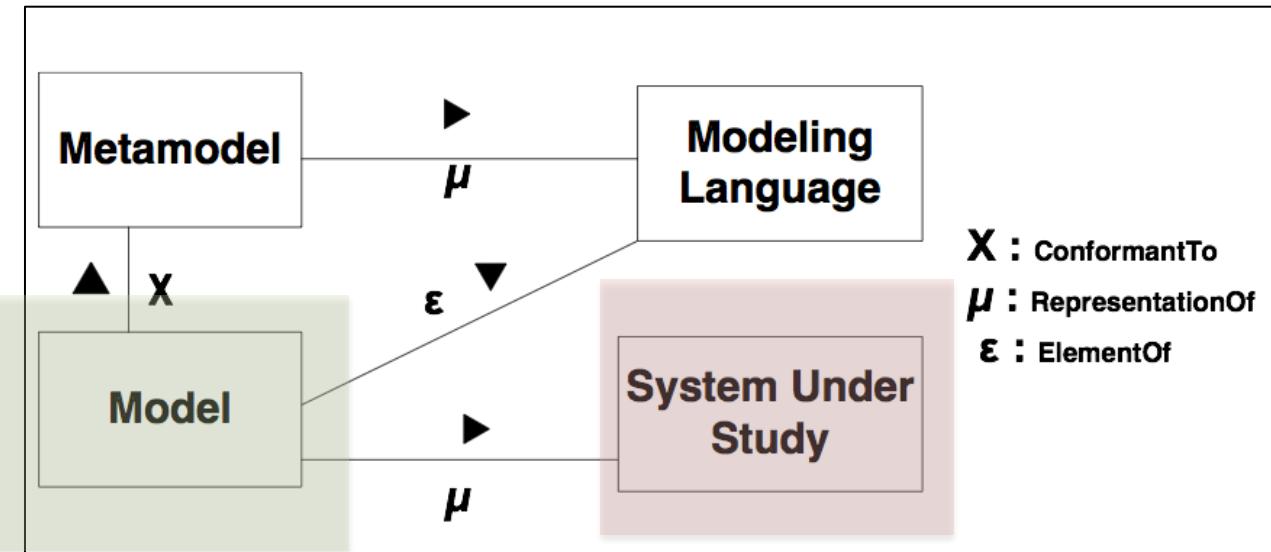
{CarEquipment, Comfort,
DrivingAndSafety,
Healthing}

- {AirConditioningFrontAndRear, FrontFogLights, SAControl}
- {AirConditioningFrontAndRear, SAControl}
- {AutomaticHeadLights, AirConditioning, FrontFogLights}
- {AirConditioningFrontAndRear, SAControl, AutomaticHeadLights, FrontFogLights}
- {FrontFogLights, AirConditioning}
- {AutomaticHeadLights, AirConditioningFrontAndRear, FrontFogLights}
- {FrontFogLights, AirConditioningFrontAndRear}
- {SAControl, AirConditioning}

Languages:

How to specify feature models?

Feature Models



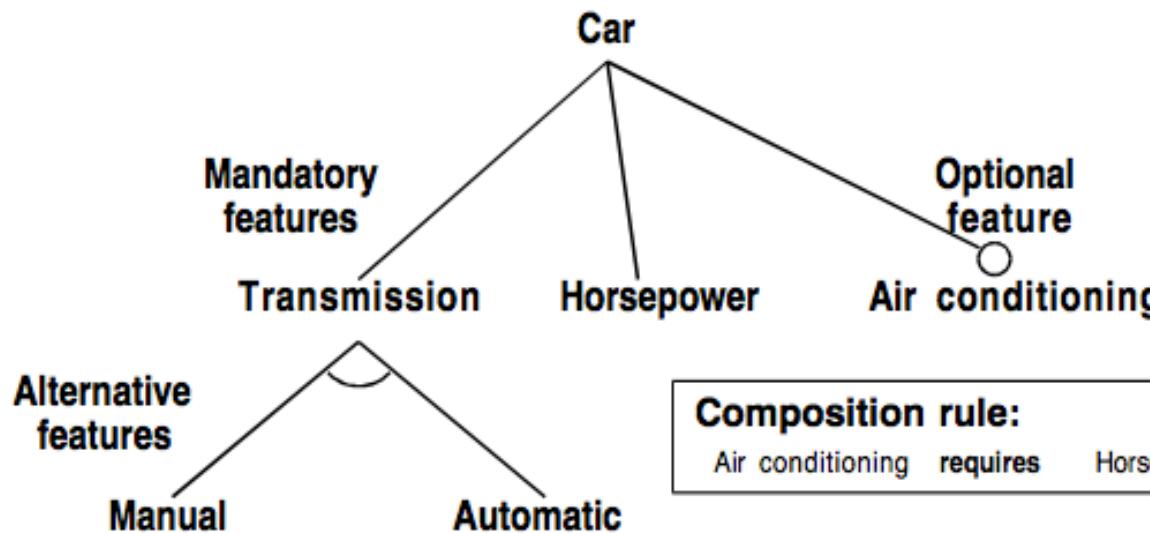
History of Feature Models

~ often called Feature Diagrams
(suggest a visual representation)

graphical languages

Feature Models

Kang et al. (1990)

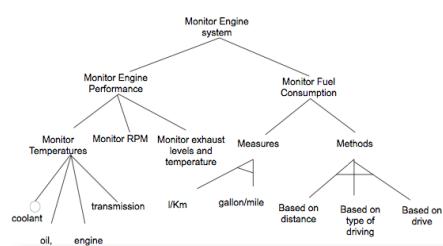


Composition rule:

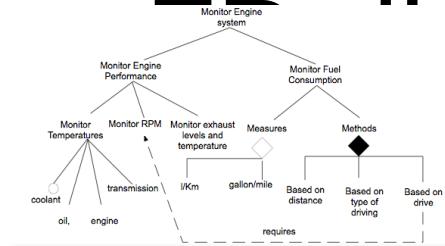
Air conditioning requires Horsepower > 100

Rationale:

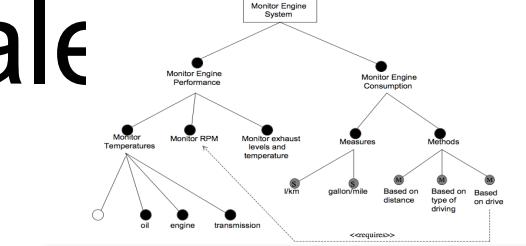
Manual more fuel efficient



FODA (OFT)
[Kang et al., 1990]

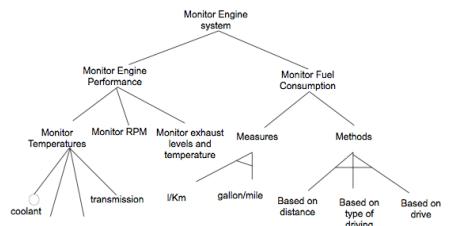


FeatuRSEB (RFD)
[Griss et al., 1998]

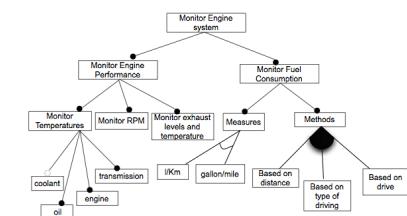


PLUSS (PFT)
[Eriksson et al., 2005]

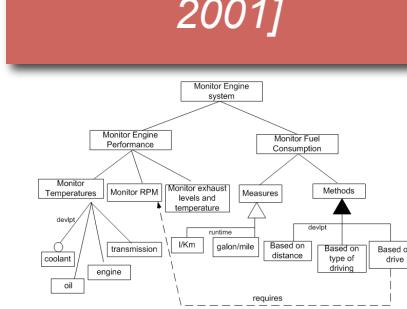
FORM (OFD)
[Kang et al., 1998]



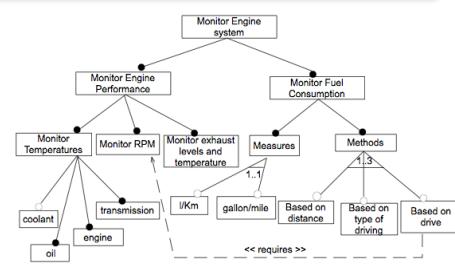
Gen. Prog. (GPFT)
[Czarnecki et al., 2000]



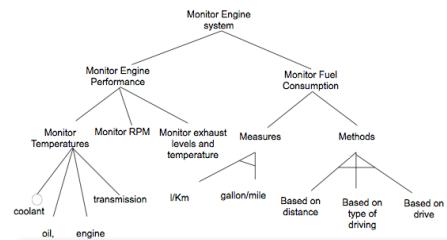
VBFD
[van Gurp et al., 2001]



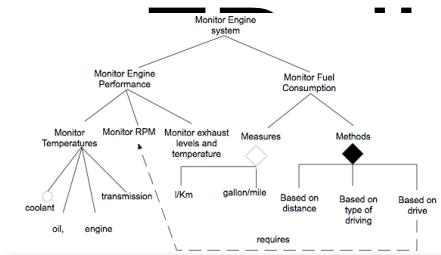
EFD
[Riebisch et al., 2002]



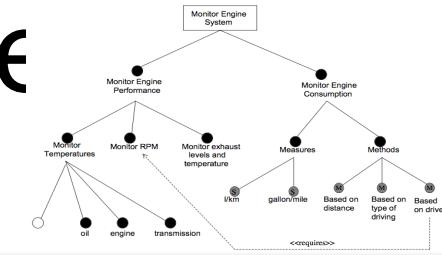
ale



FODA (OFT)
[Kang et al., 1990]



FeatuRSEB (RFD)
[Griss et al., 1998]



PLUSS (PFT)
[Eriksson et al., 2005]



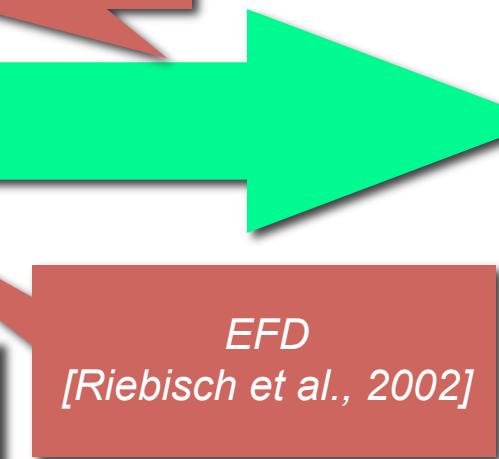
FORM (OFD)
[Kang et al., 1998]



Gen. Prog. (GPFT)
[Czarnecki et al., 2000]

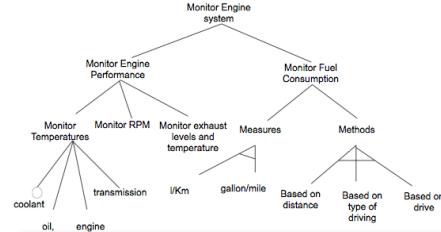


VBFD
[van Gurp et al., 2001]

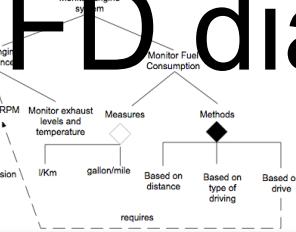


EFD
[Riebisch et al., 2002]

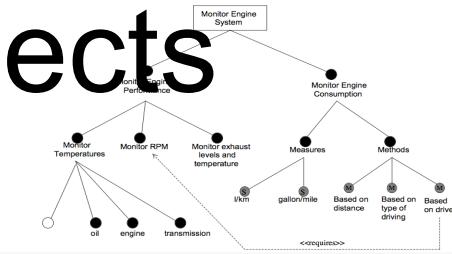
FD dialects



FODA (OFT)
[Kang et al., 1990]



FeatuRSEB (RFD)
[Griss et al., 1998]



PLUSS (PFT)
[Eriksson et al., 2005]

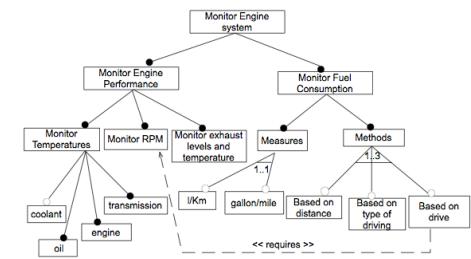
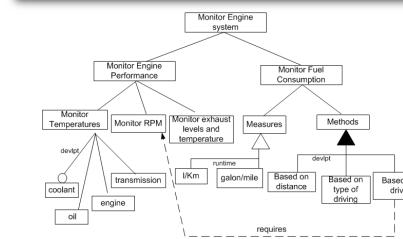
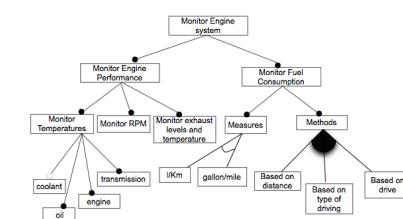
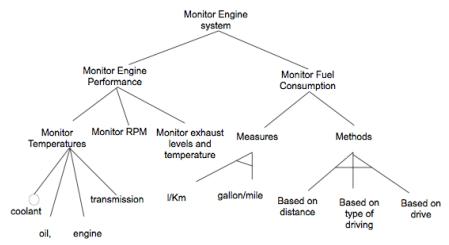
Aesthetic differences
Stronger claims

FORM (OFD)
[Kang et al., 1998]

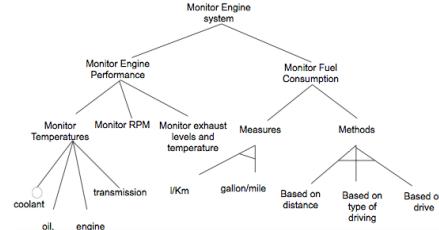
Gen. Prog. (GPFT)
[Czarnecki et al., 2000]

VBFD
[van Gurp et al., 2001]

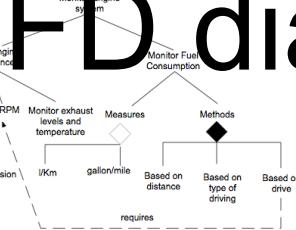
EFD
[Riebisch et al., 2002]



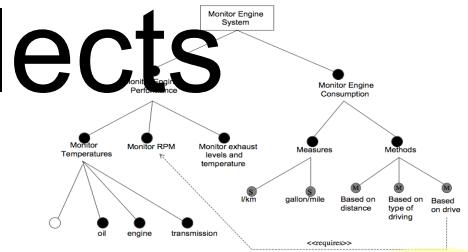
FD dialects



FODA (OFT)
[Kang et al., 1990]



FeatuRSEB (RFD)
[Griss et al., 1998]



PLUSS (PFT)
[Eriksson et al., 2001]

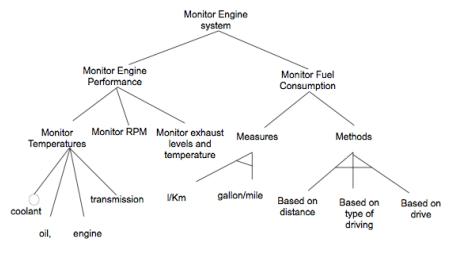
Aesthetic differences

Stronger claims

Vague use of terms
syntax, semantics,
expressiveness, ambiguity ...

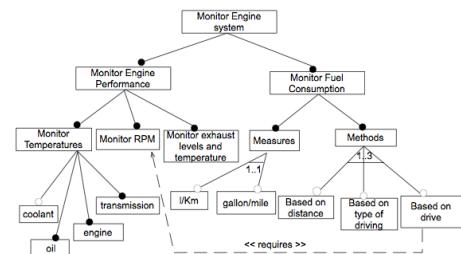
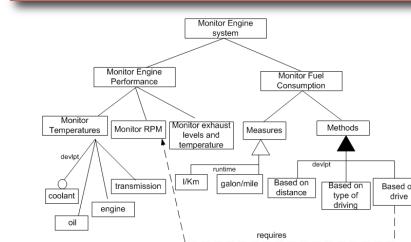
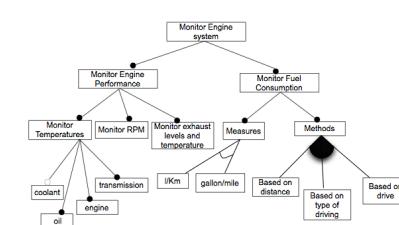
FORM (OFD)
[Kang et al., 1998]

Gen. Prog. (GPFT)
[Czarnecki et al., 2000]



VBFD
[van Gurp et al., 2001]

EFD
[Riebisch et al., 2002]



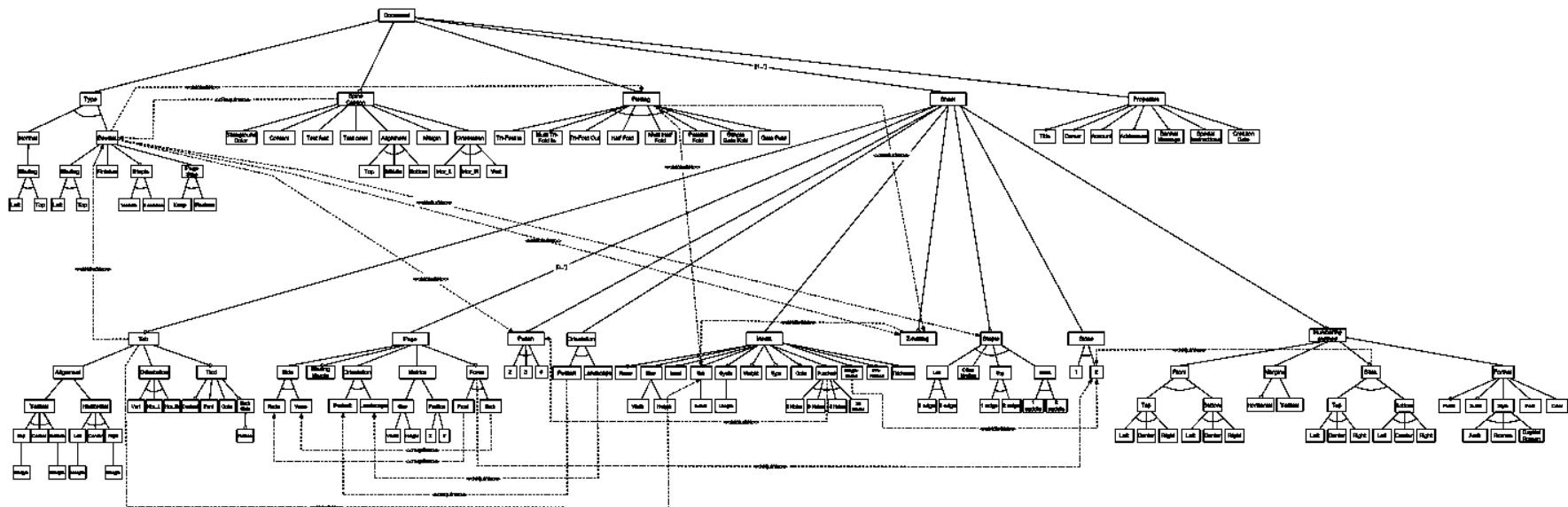
Concrete (graphical)
syntax matters...

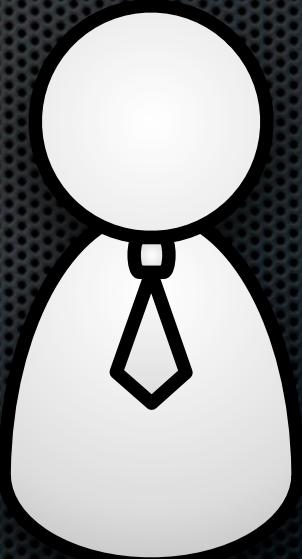
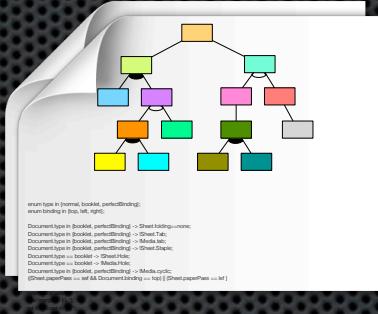
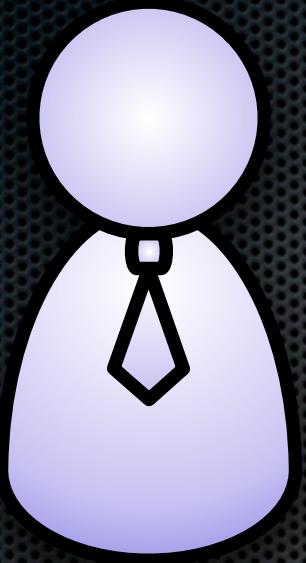
But semantics even more!

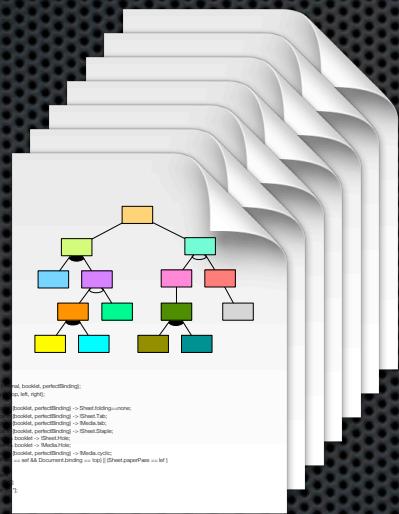
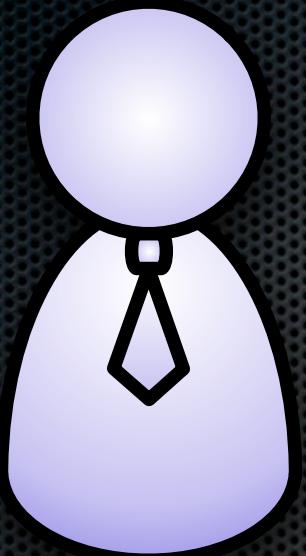
Feature Models

~ Feature Diagrams
(suggest a visual representation)

but that's not mandatory
that does not necessary scale
or respond to users' concerns







Visualisation will help, right ?

- Tools create their own problems
- Lock-in, dependance
- Some information is inevitably textual
 - OCL constraints in UML
 - Minispecs in StateCharts
 - Attributes and constraints in FD

Why not use text altogether ?

- There are powerful tools for that
- Helps with
 - Editing
 - Transformation
 - Versioning
 - Information exchange
 - ...
- Graphical views can be generated anyway

Not a new idea

Language	User Friendly	Attributes	Cardinalities	Cross tree constraints	Modularisation
Van Deursen et al.	✓				
GUIDSL (AHEAD)	✓				
SXFM	✓				
XML-based (FAMA etc.)		✓	✓	✓	



+ CML
+ VSL

Furthermore

- Intuitive C-like syntax
- Formal semantics
- Statically typed
- Editing facilities
 - Textual editor with auto-completion

TVL : allOf – and decomposition

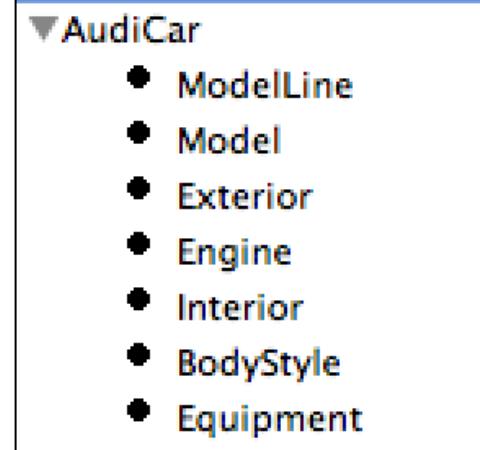
```
root AudiCar {  
    group allOf {  
        ModelLine,  
        BodyStyle,  
        Model,  
        Engine,  
        Exterior,  
        Interior,  
        Equipment  
    }  
}
```

root feature

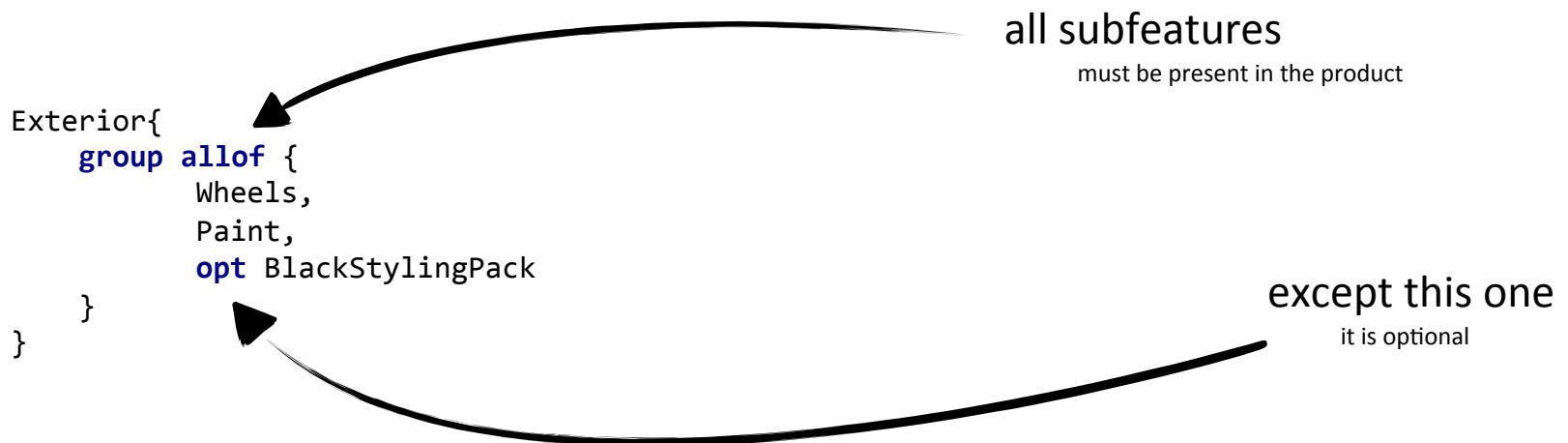
and decomposition

all subfeatures must be present in the product

sub features



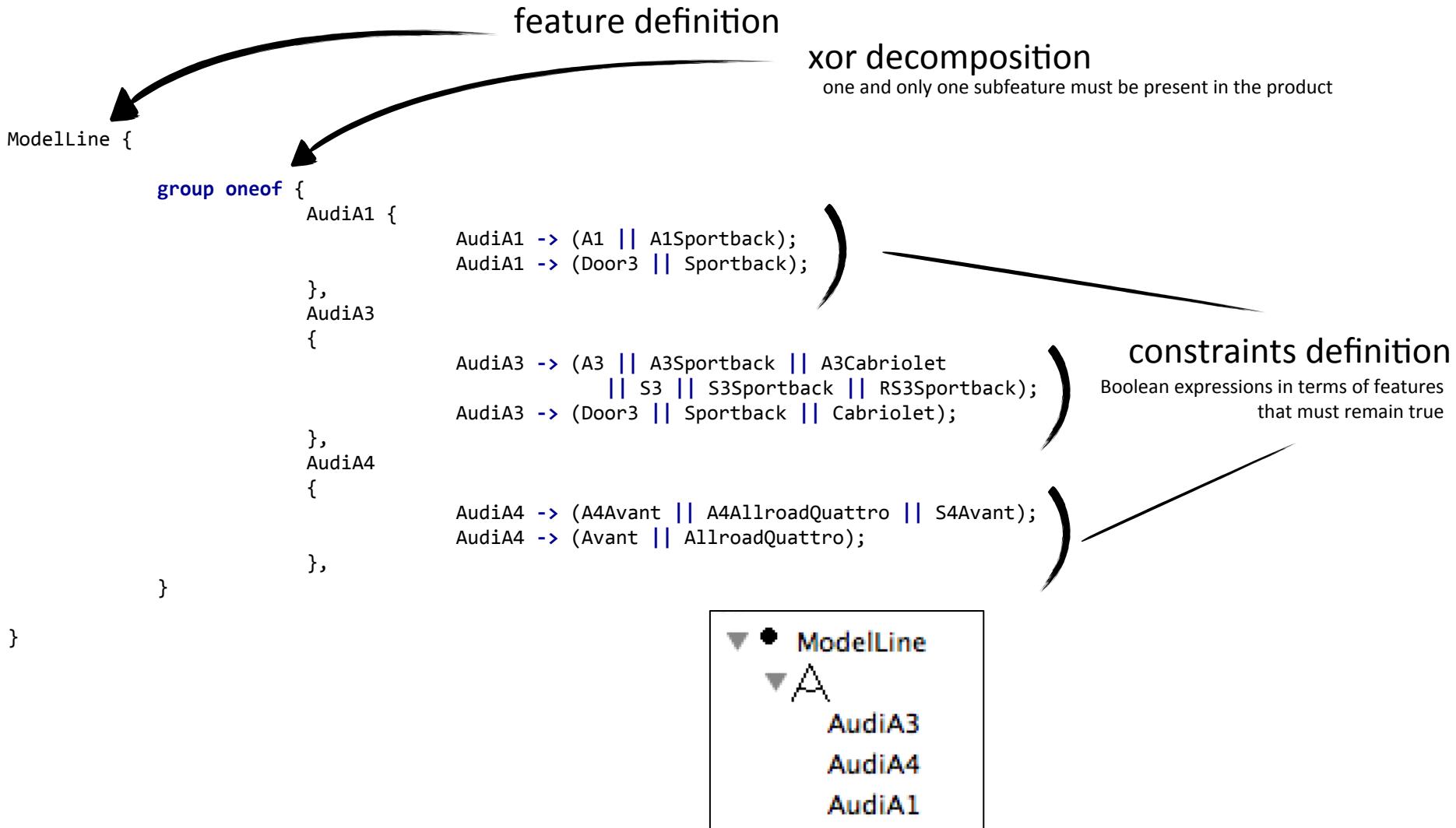
TVL : optional features



▼ Exterior

- BlackStylingPack
- Paint
- Wheels

TVL : one of – xor decomposition

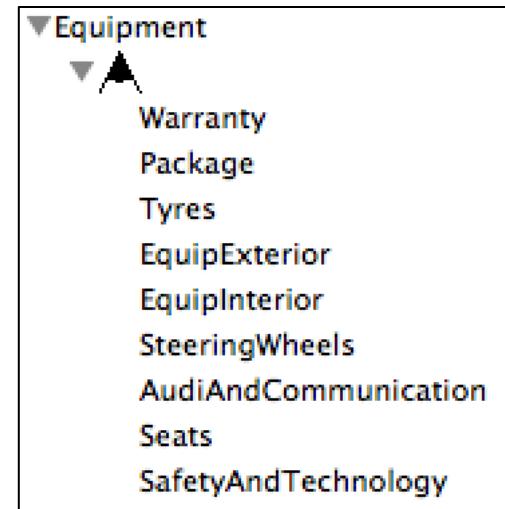


TVL : someOf – or decomposition

feature definition

```
Equipment{  
    group someof {  
        Package,  
        EquipExterior,  
        Tyres,  
        EquipInterior,  
        SteeringWheels,  
        Seats,  
        SafetyAndTechnology,  
        AudiAndCommunication,  
        Warranty  
    }  
}
```

or decomposition
one or more subfeatures must be present in the product



TVL : Modularity

```
root Car {  
    group allOf {  
        BodyStyle,  
        Model,  
        Engine,  
        Equipment }  
  
}  
  
include(BodyStyle.tvl);  
include(Model.tvl);  
include(Engine.tvl);  
include(Equipment.tvl);
```



Split model in reusable parts
using external files

TVL : Modularity

BodyStyle.tvl

```
BodyStyle {  
    group oneof {  
        Avant,  
        Door3,  
        Sportback,  
        Coupe,  
        Cabriolet,  
        Roadster,  
        Spyder,  
        SUV  
    }  
}
```

Model.tvl

```
Model {  
    group oneof {  
        A1,  
        A3,  
        S3,  
        A4,  
        S4,  
        A5,  
        S5,  
        RS5,  
        A6,  
        A7,  
        A8,  
        A8L,  
        R8  
        Q3,  
        Q5,  
        Q7,  
        TT,  
        TTS,  
        TT RS  
    }  
}
```

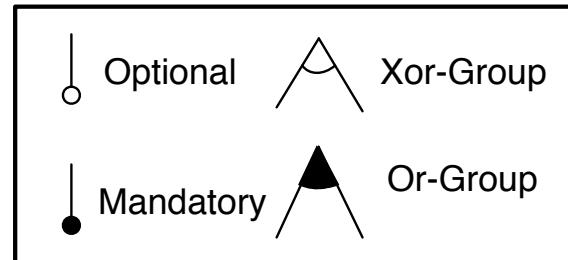
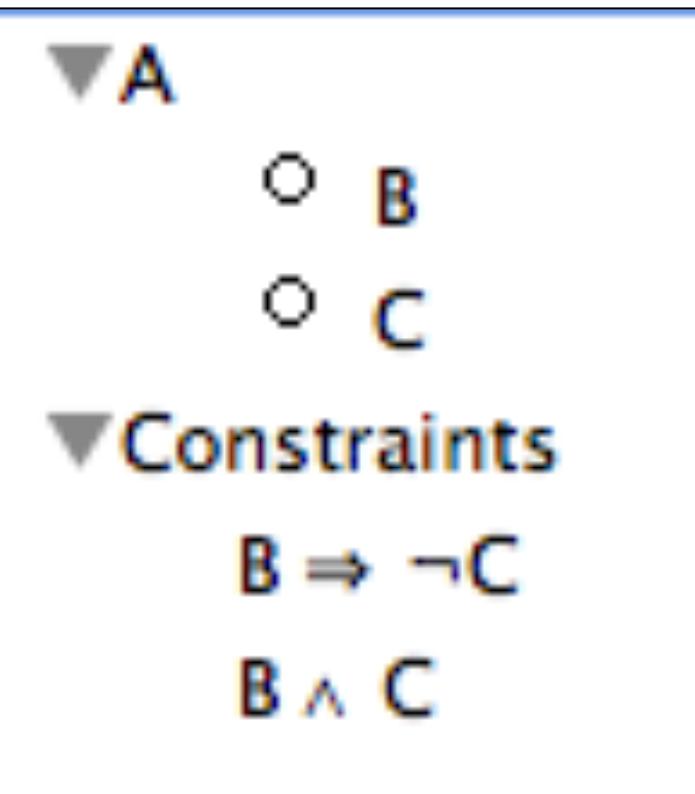
Engine.tvl

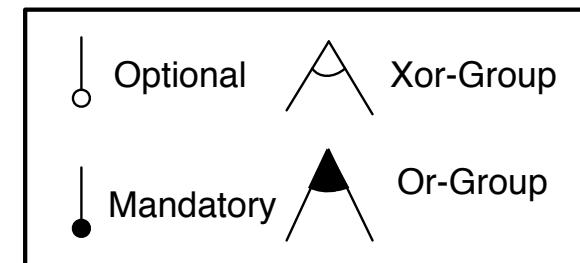
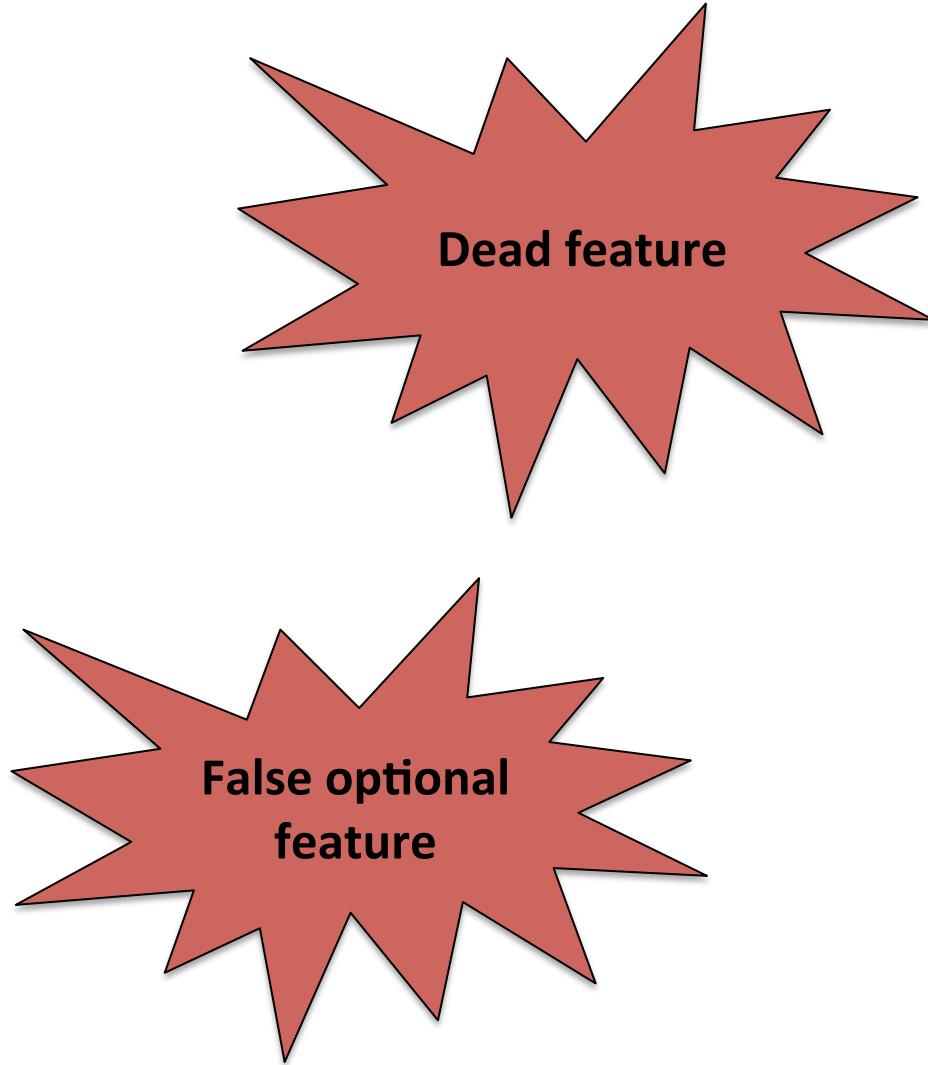
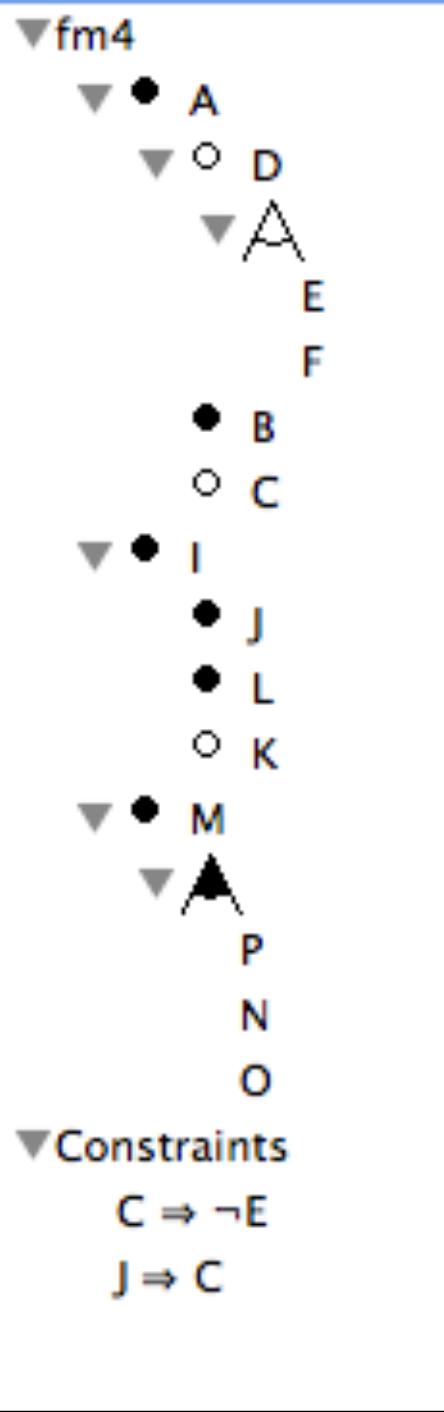
```
Engine {  
    group oneof {  
        SE12TFSI,  
        CE14TFSI,  
        CE14TFSI119,  
        Sport12TFSI,  
        Sport14TFSI,  
        Sport14TFSI119,  
        BlackEdition14TFSI,  
        SLine12TFSI,  
        SLine14TFSI,  
        SE16TDI,  
        CE16TDI,  
        CE20TDI,  
        ...  
    }  
}
```

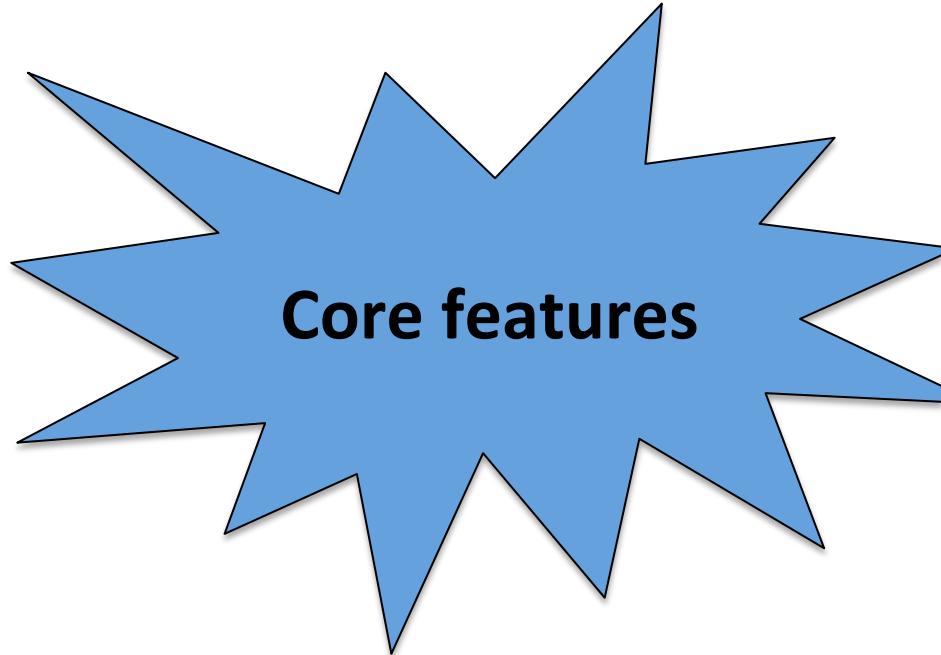
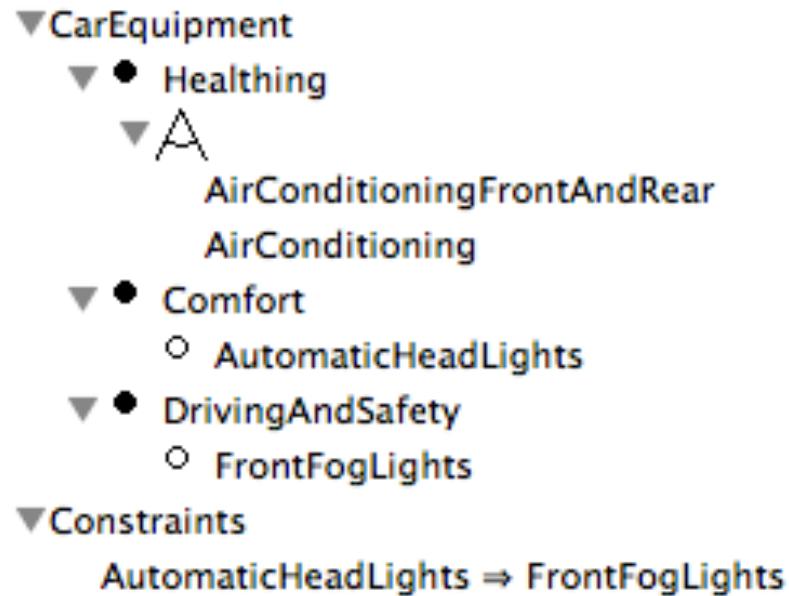
TVL : Tools

- Key references
 - Classen, A.; Boucher, Q. and Heymans, P. A Text-based Approach to Feature Modelling: Syntax and Semantics of TVL. In Science of Computer Programming (SCP), Special Issue on Software Evolution
 - Hubaux, A.; Boucher, Q.; Hartman, H.; Michel, R. and Heymans, P. **Evaluating a Text-based Feature Modelling Language: Four Industrial Case (SLE 2010)**
 - <http://info.fundp.ac.be/tvl/>
- Support
 - Parser / Syntactic and semantic checker
 - SAT-based checker for Boolean models
- Lessons learned
 - Textual vs graphical languages
 - Semantics of your language matter

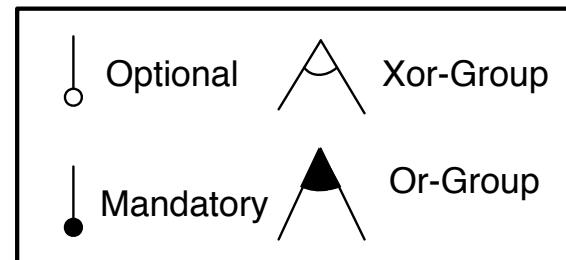
I want to analyze and
play with my specification!

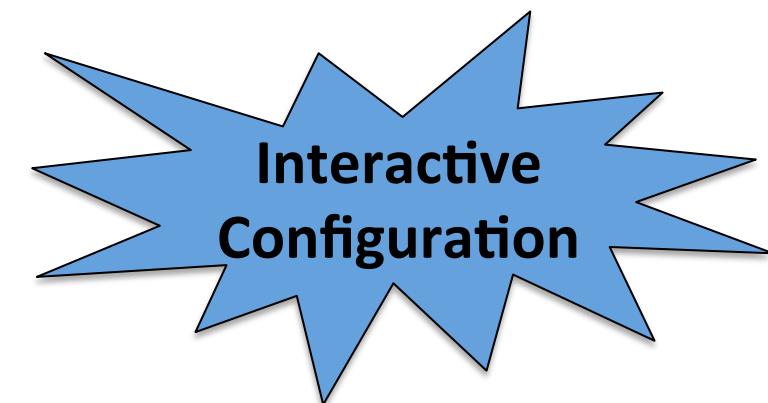
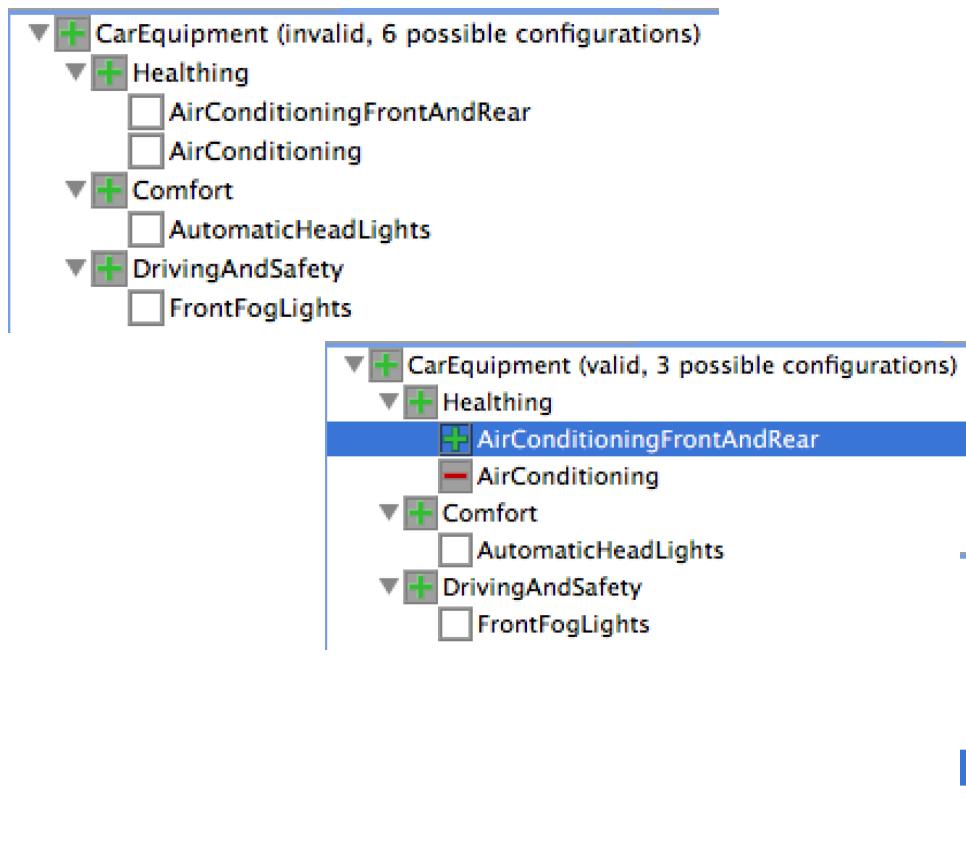
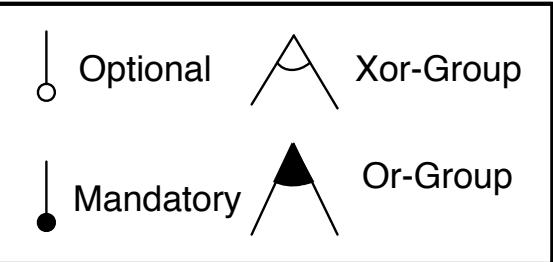






{CarEquipment, Comfort,
DrivingAndSafety, Healthing}





Feature Models and Automated Reasoning

Benavides et al. survey, 2010

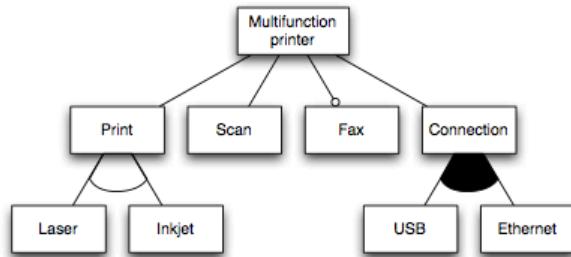
Decision problems and complexity

- Validity of a feature model
- Validity of a configuration
- Computation of dead and core features
- Counting of the number of valid configurations
- Equivalence between two feature models
- Satisfiability (SAT) problem
 - NP-complete

How to automate analysis of your feature models?

Binary Decision Diagram (BDD)
SAT solver

Typical implementations



Fontsource (Attributed - Free Processing 2012) (Attributed - Creative Commons)



result



logics



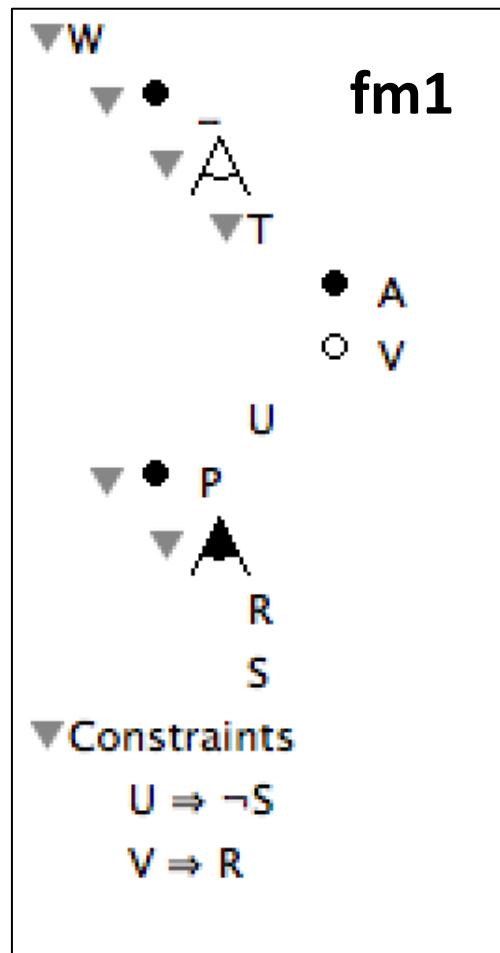
solvers



Z3

(Boolean) Feature Models

Hierarchy + Variability = set of valid configurations



$\llbracket fm1 \rrbracket = \{$

$\{W, P, R, S, T, A, V\},$

$\{W, P, S, T, A\},$

$\{W, P, R, T, A\},$

$\{W, P, R, U\},$

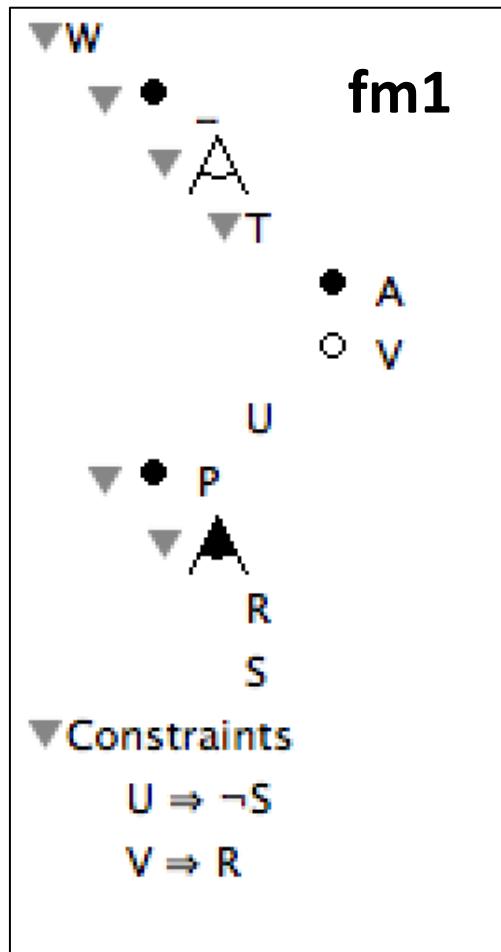
$\{W, P, R, T, V, A\},$

$\{W, P, R, S, T, A\},$

$\}$

(Boolean) Feature Models

~ Boolean formula

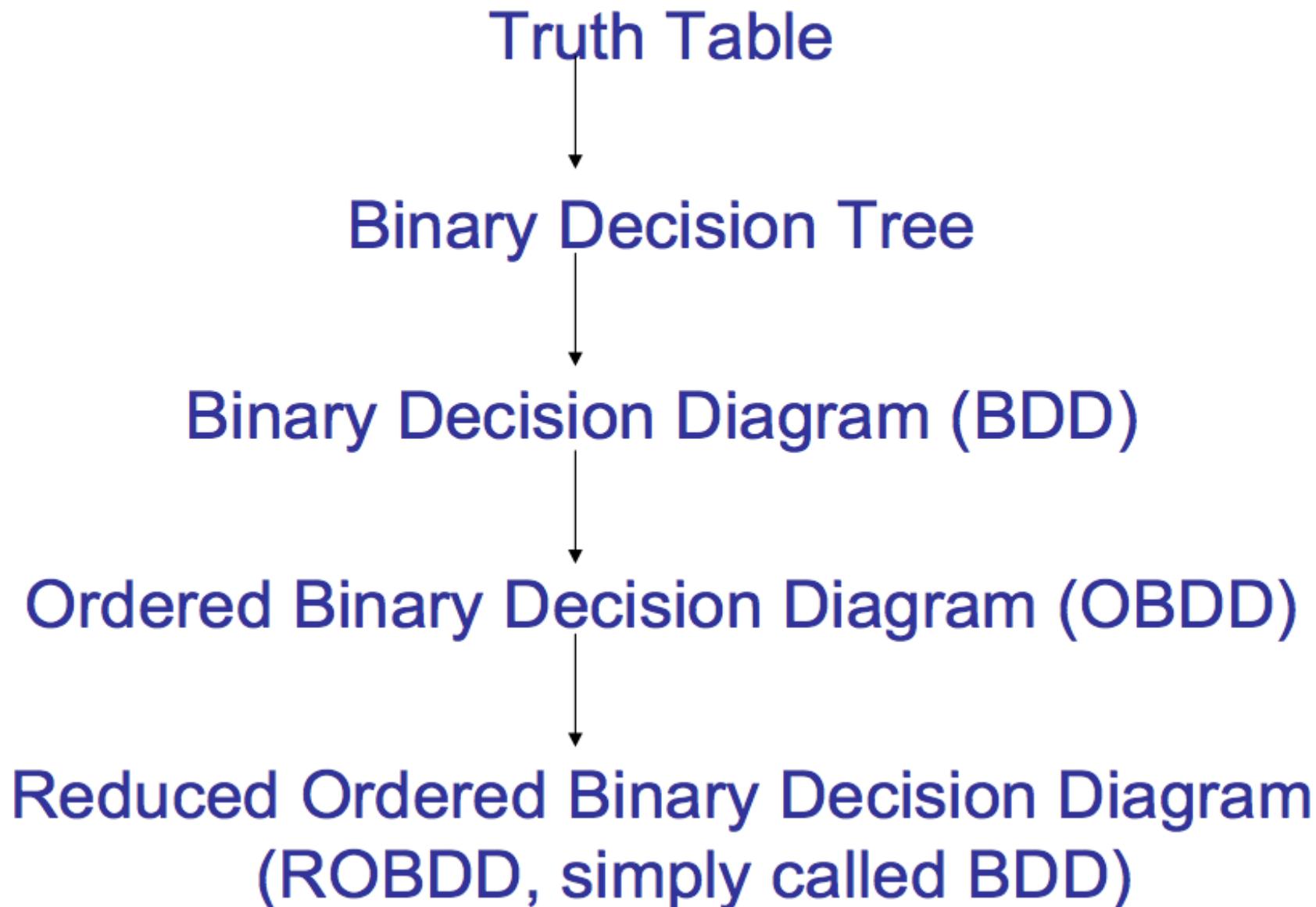


$\phi_{fm_1} = W // \text{root}$
 $\wedge W \Leftrightarrow P // \text{mandatory}$
 $// \text{Or-group}$
 $\wedge P \Rightarrow R \vee S$
 $\wedge R \Rightarrow P \wedge S \Rightarrow P$
 $\wedge V \Rightarrow T // \text{optional}$
 $\wedge A \Leftrightarrow T // \text{mandatory}$
 $// \text{Xor-group}$
 $\wedge T \Rightarrow W$
 $\wedge U \Rightarrow W$
 $\wedge \neg T \vee \neg U$
 $// \text{constraints}$
 $\wedge V \Rightarrow R // \text{implies}$
 $\wedge \neg U \Rightarrow \neg S // \text{excludes}$

Truth table, boolean function

from		to		
x_1	x_2	x_3	x_4	f
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

BDDs from Truth Tables

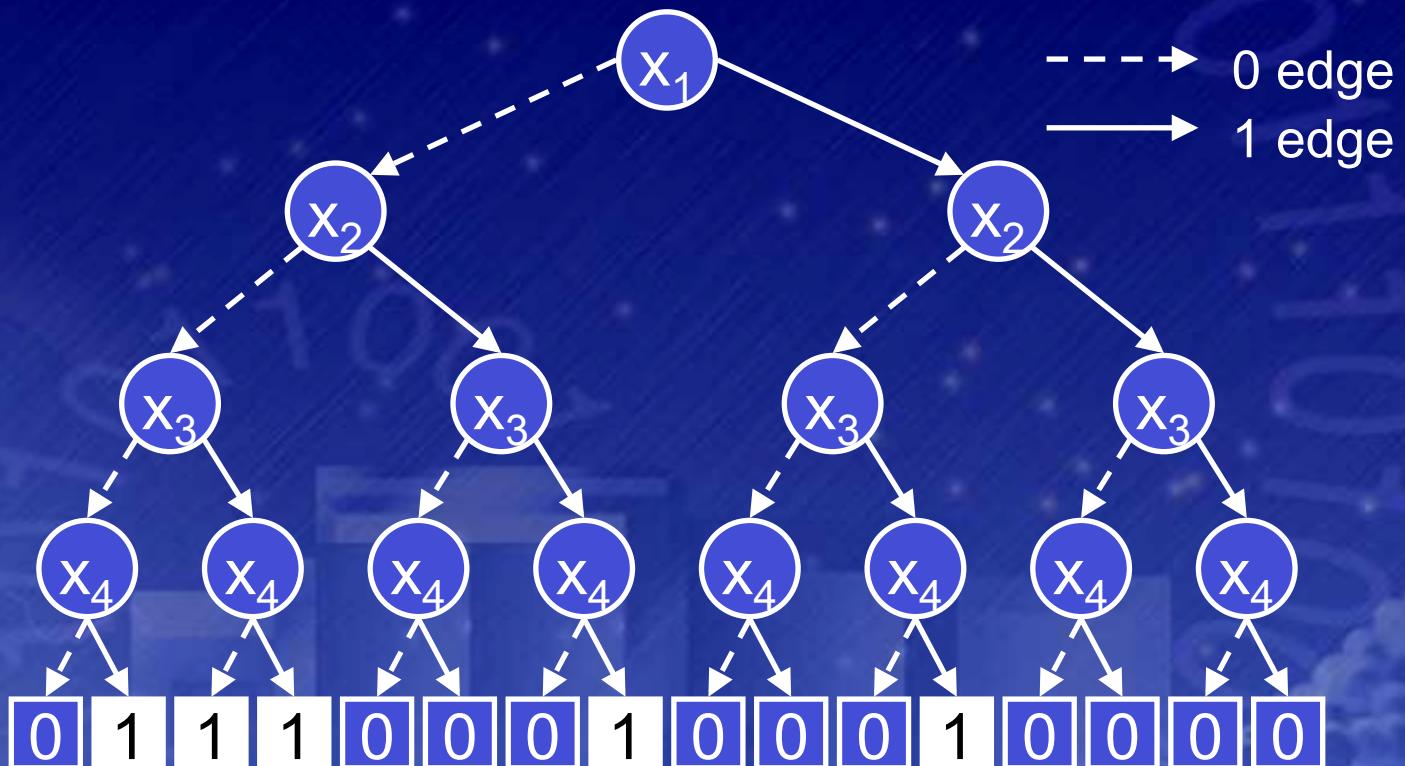


Binary Decision Diagrams

(Bryant 1986)

encoding of a truth table.

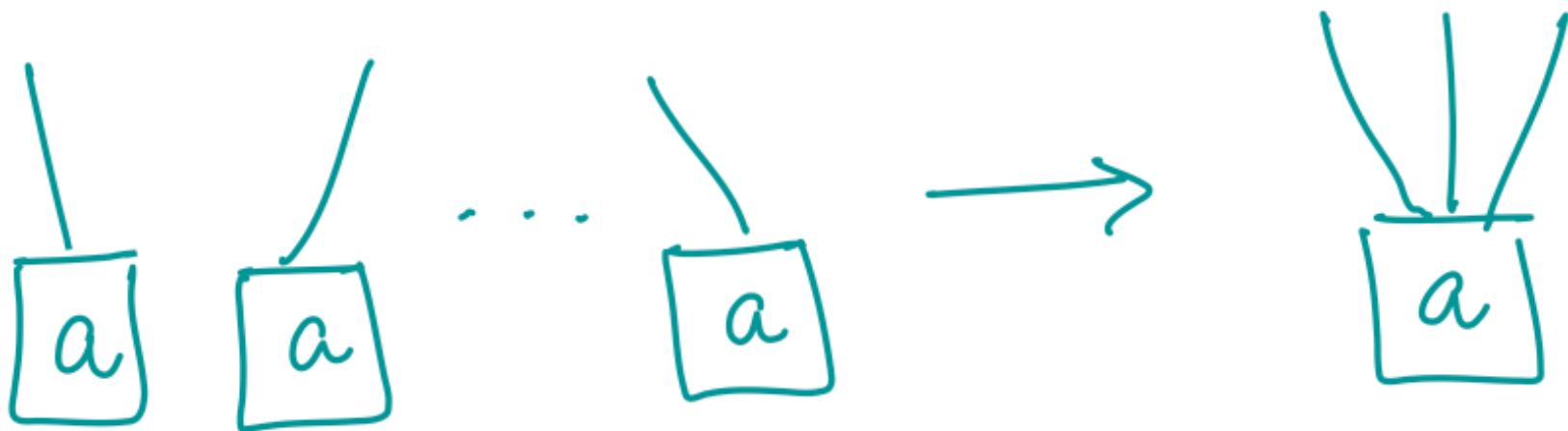
from		to		
x_1	x_2	x_3	x_4	f
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0



Reduction

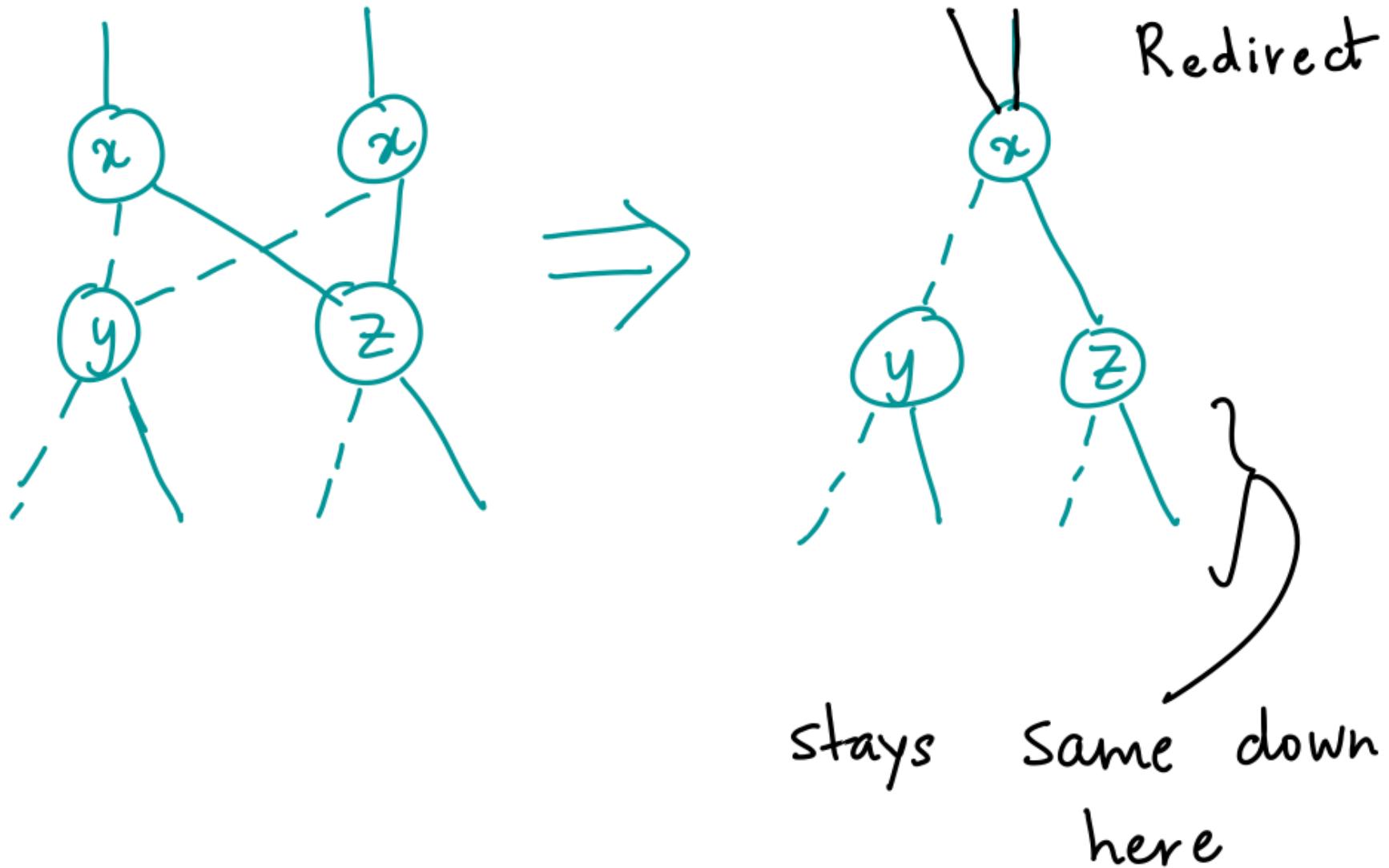
- Identify Redundancies
- 3 Rules
 - Merge equivalent leaves
 - Merge isomorphic nodes
 - Eliminate redundant tests

Merge equivalent leaves

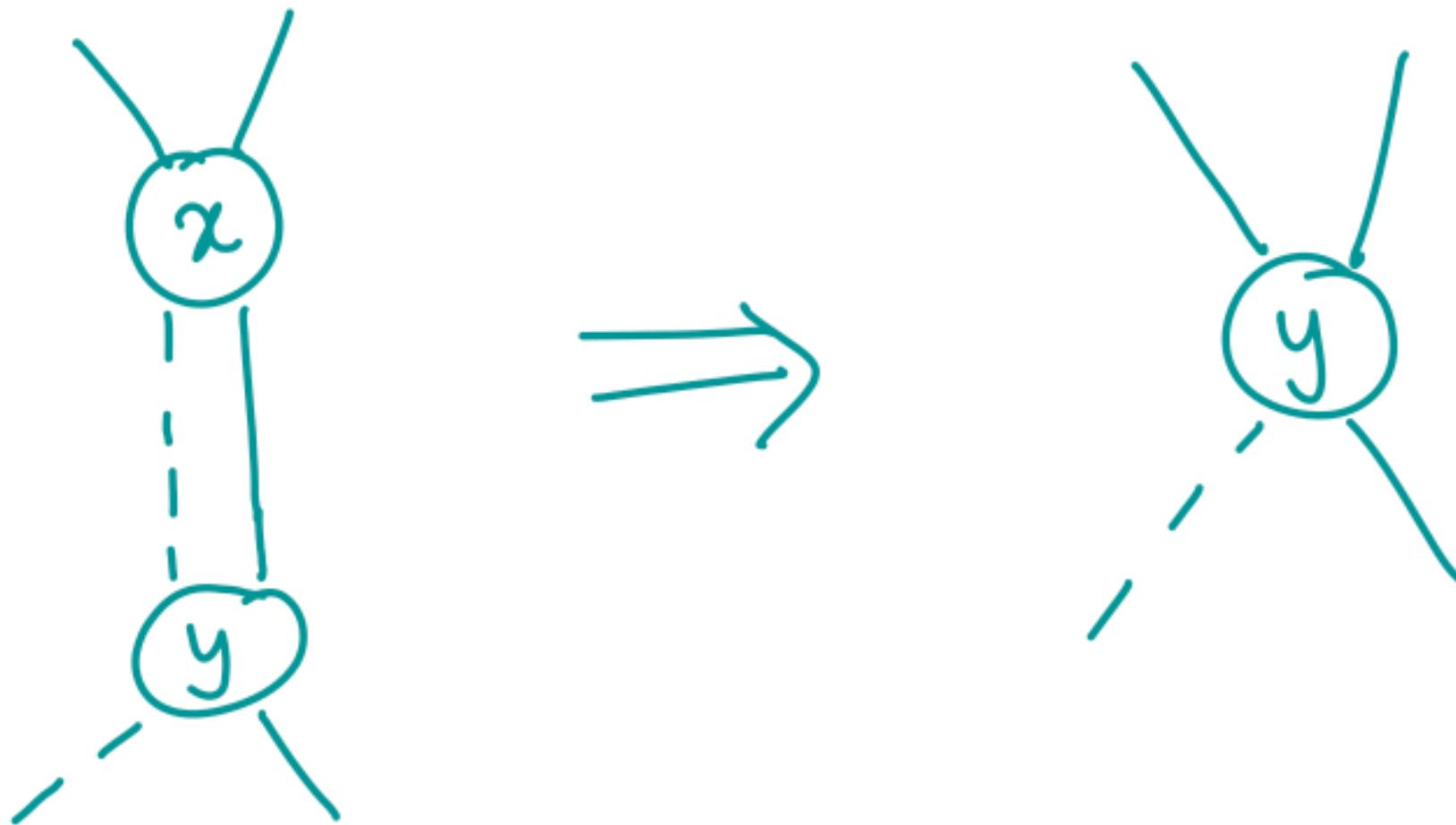


"a" is either 0 or 1

Merge isomorphic nodes

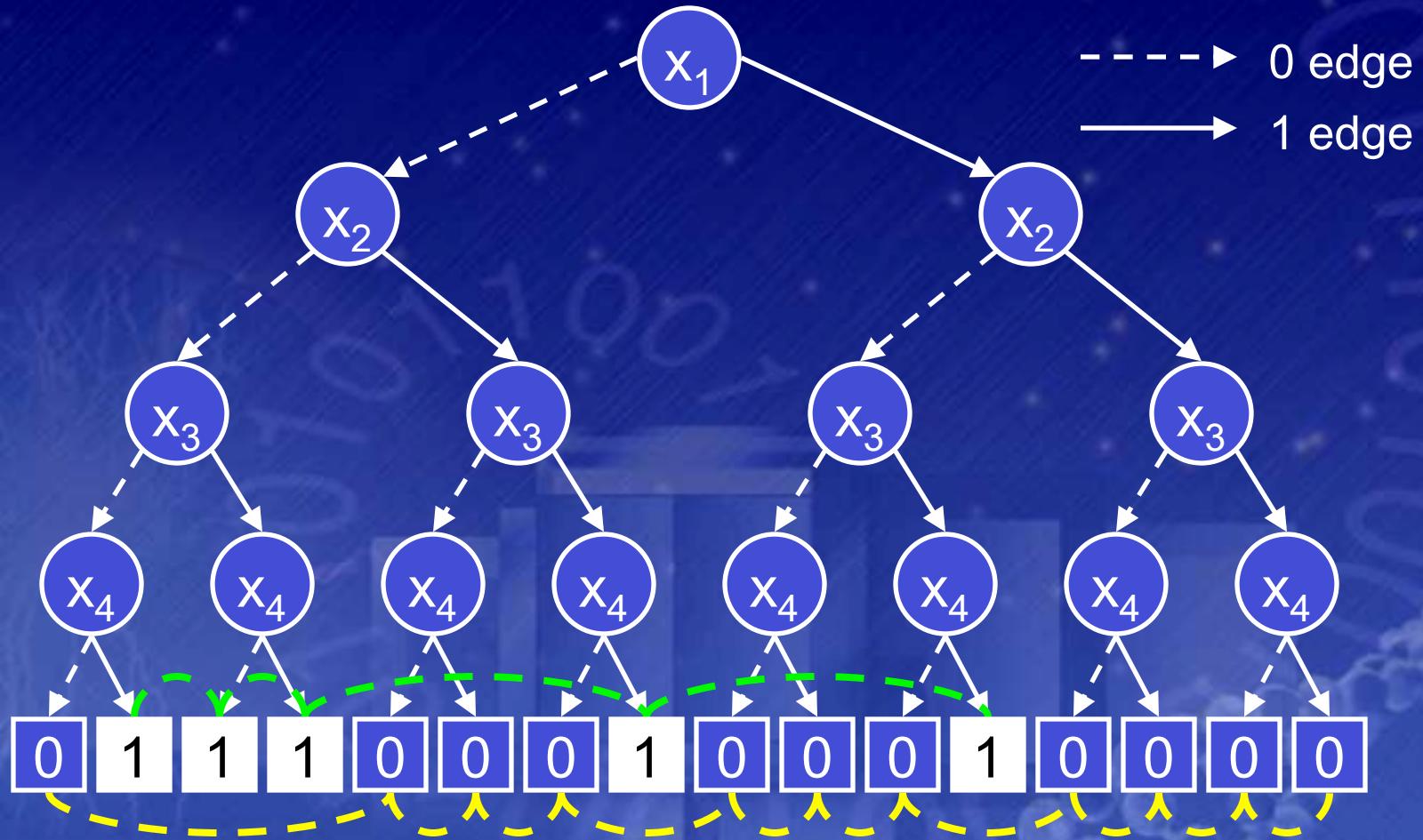


Eliminate redundant tests



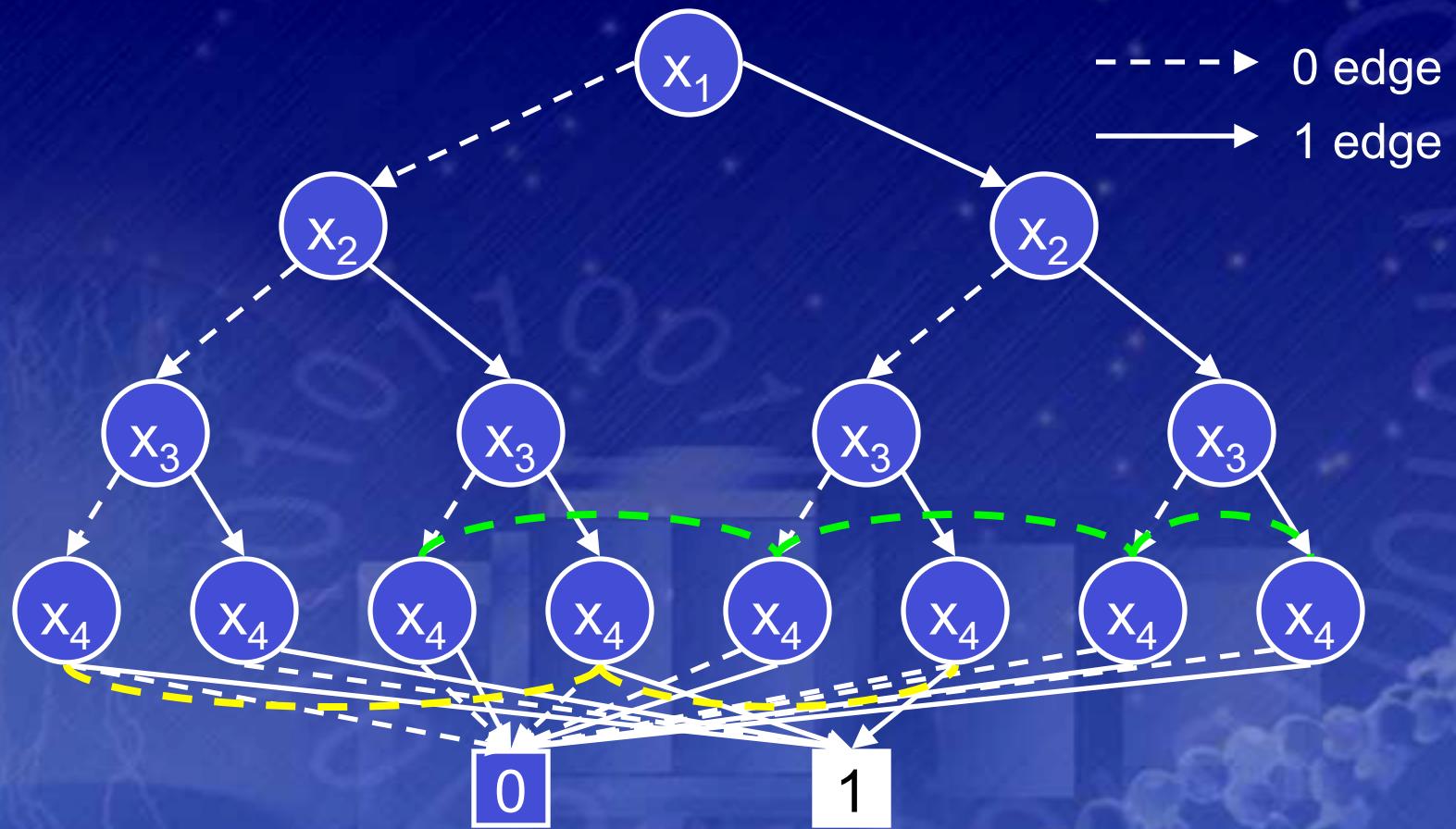
Binary Decision Diagrams

- Collapse redundant nodes.



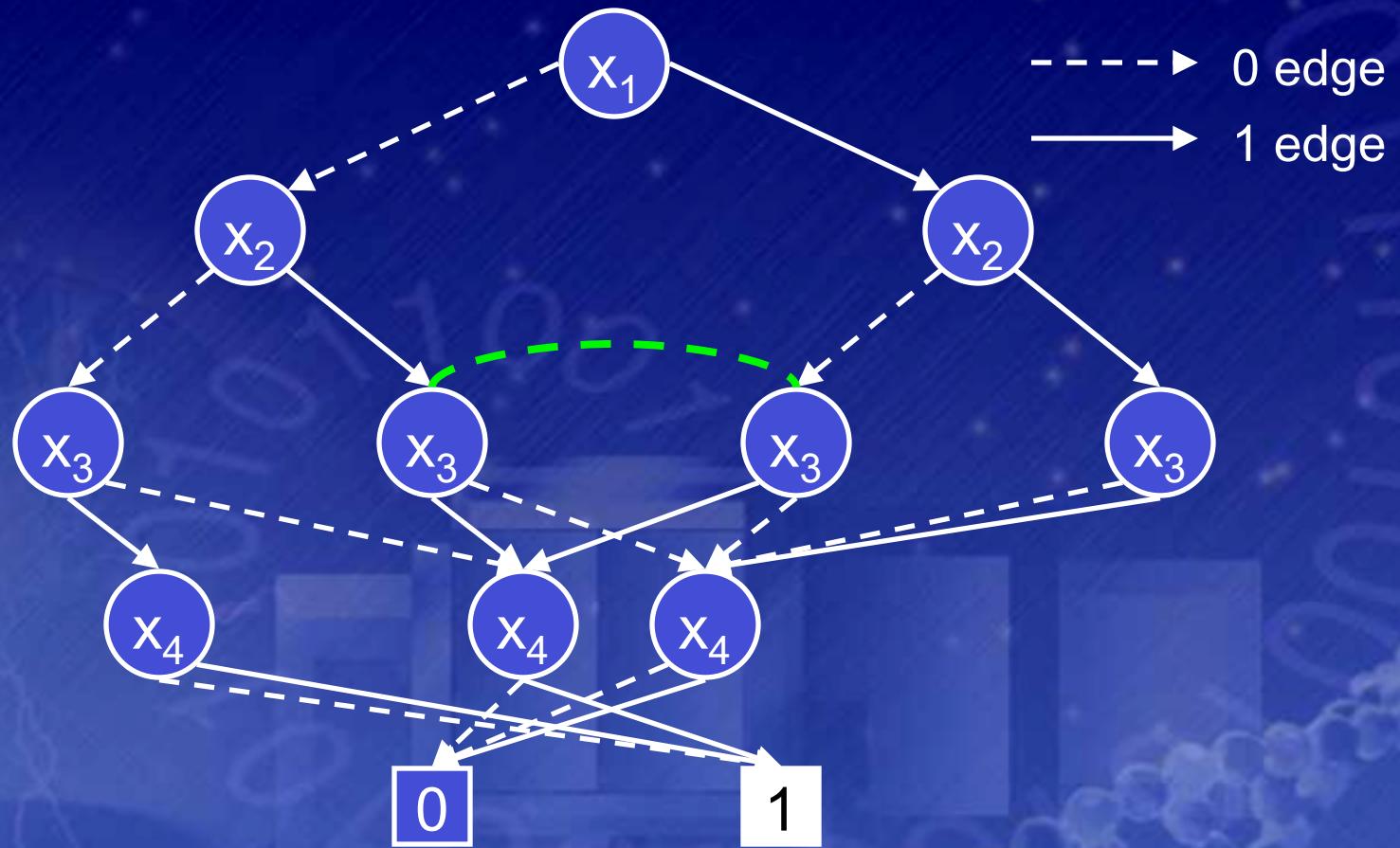
Binary Decision Diagrams

- Collapse redundant nodes.



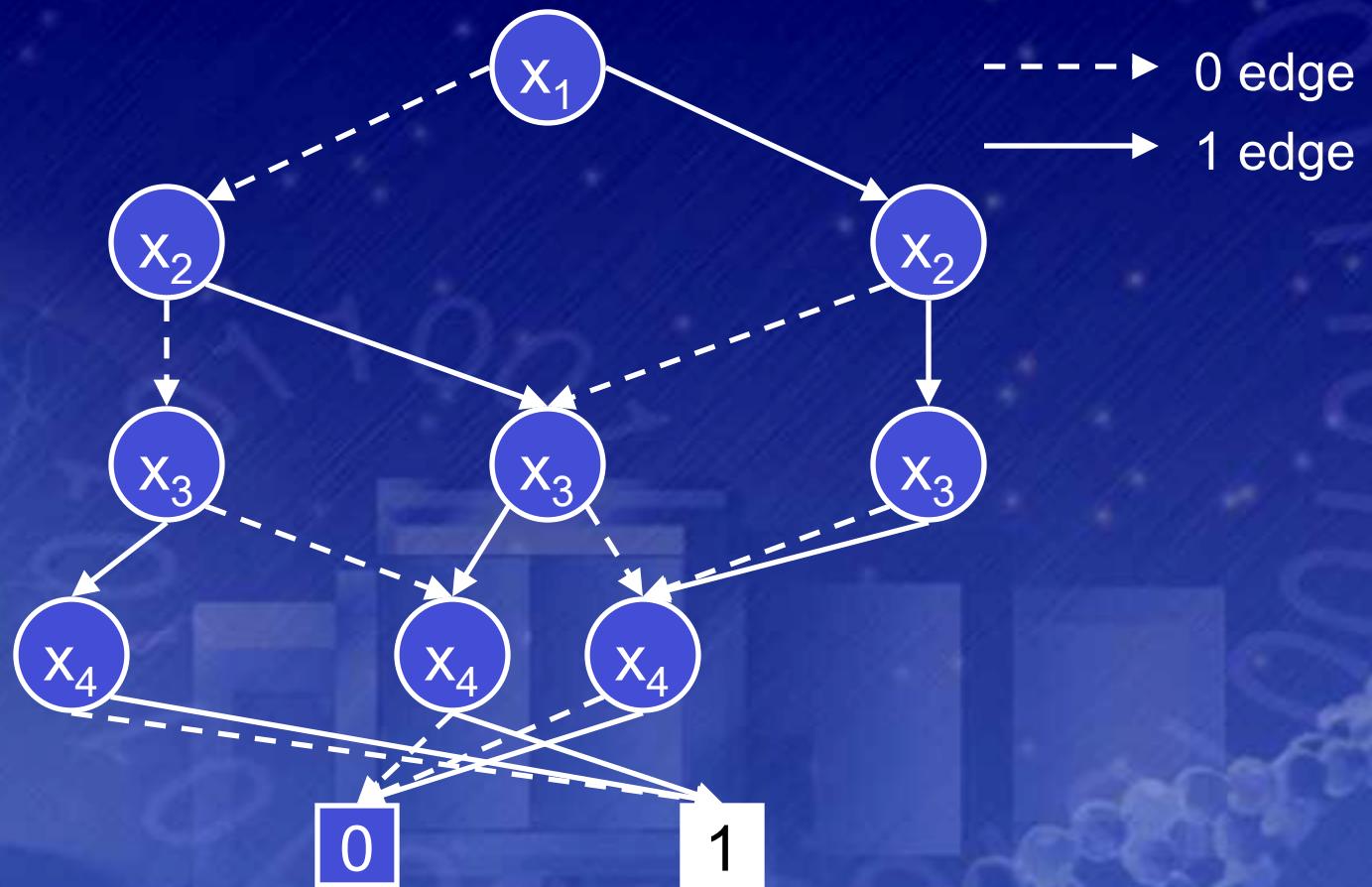
Binary Decision Diagrams

- Collapse redundant nodes.



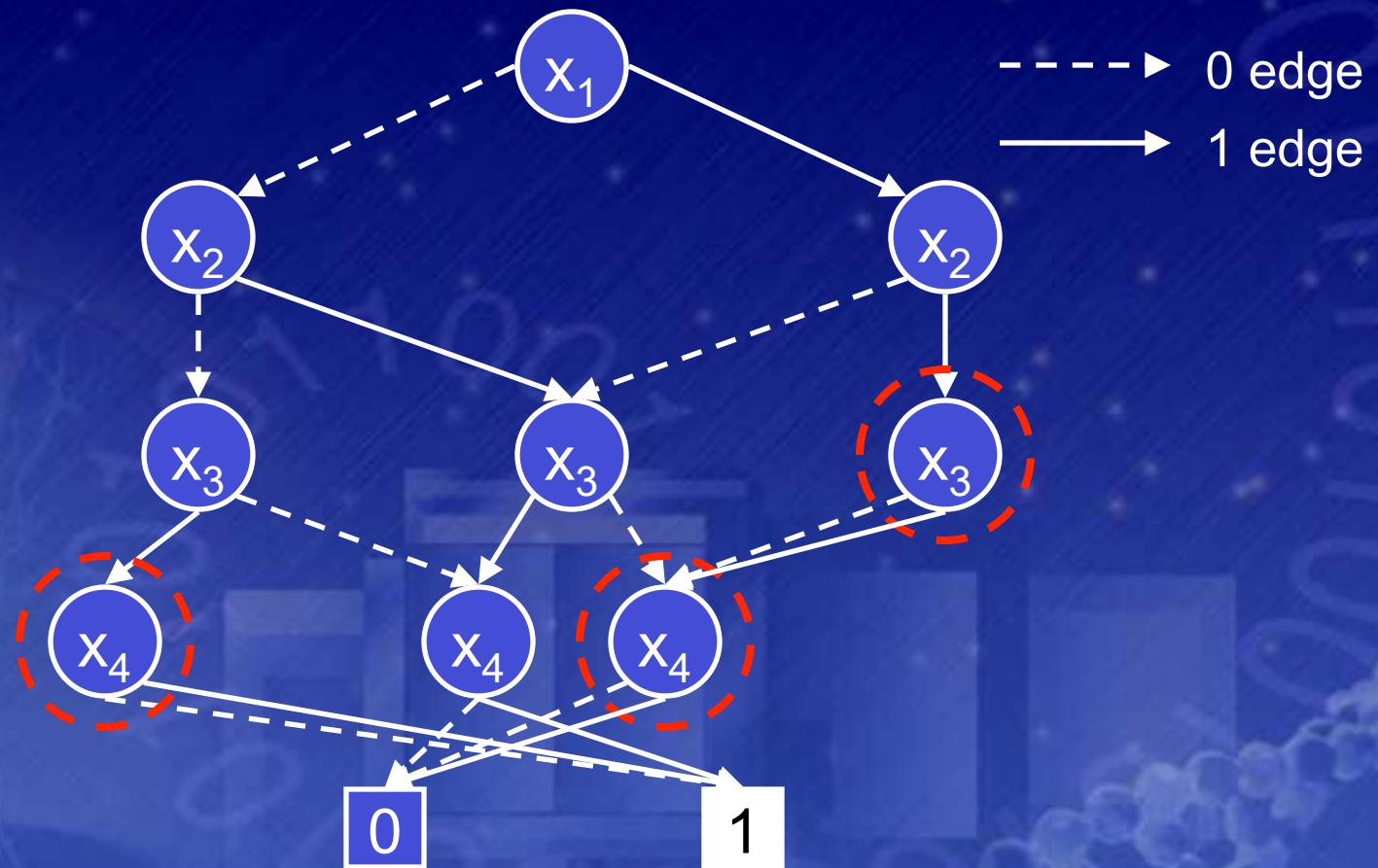
Binary Decision Diagrams

- Collapse redundant nodes.



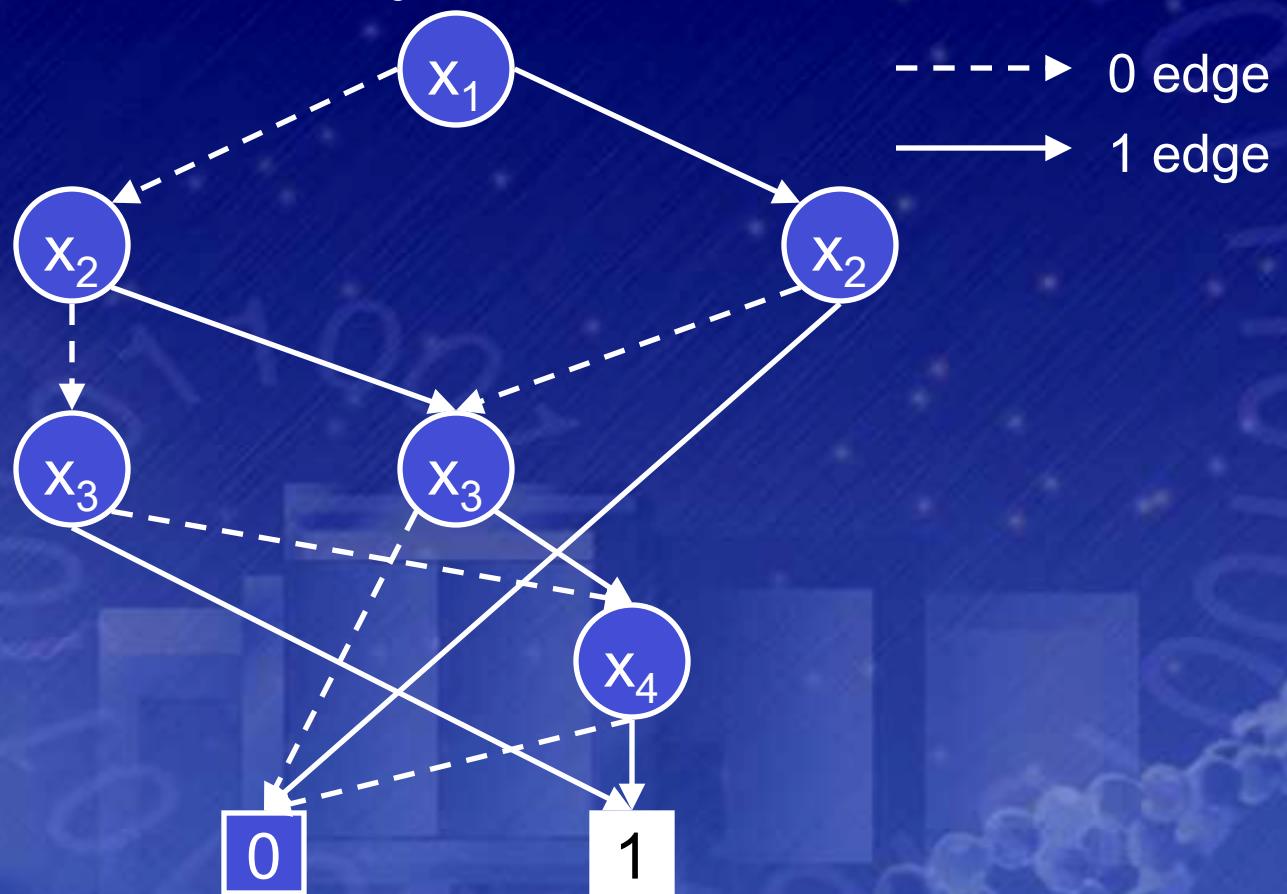
Binary Decision Diagrams

- Eliminate unnecessary nodes.



Binary Decision Diagrams

- Eliminate unnecessary nodes.

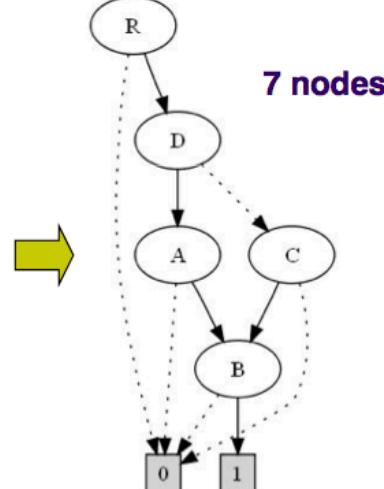
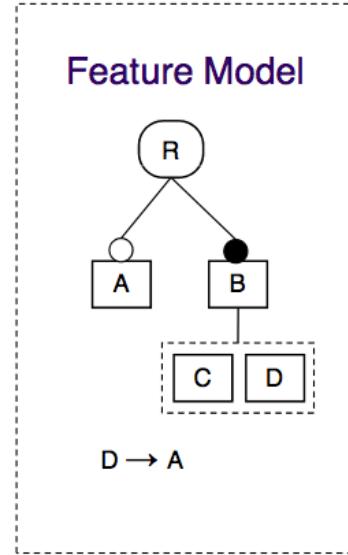
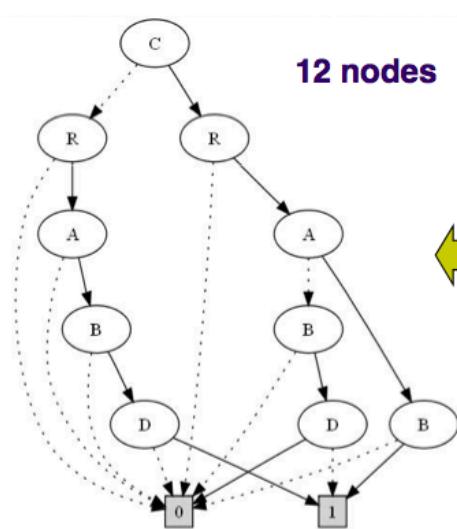


Binary Decision Diagrams (BDDs)

- Very efficient structure for most of the satisfiability operations
- Polynomial in time for checking satisfiability and determining equivalence between two BDDs
- Graph traversal
- So great?

Binary Decision Diagrams (BDDs): Theoretical Problem

- The size of the BDD is very sensitive to the order of the BDD variables
 - e.g. two equivalent BDDs for the same feature

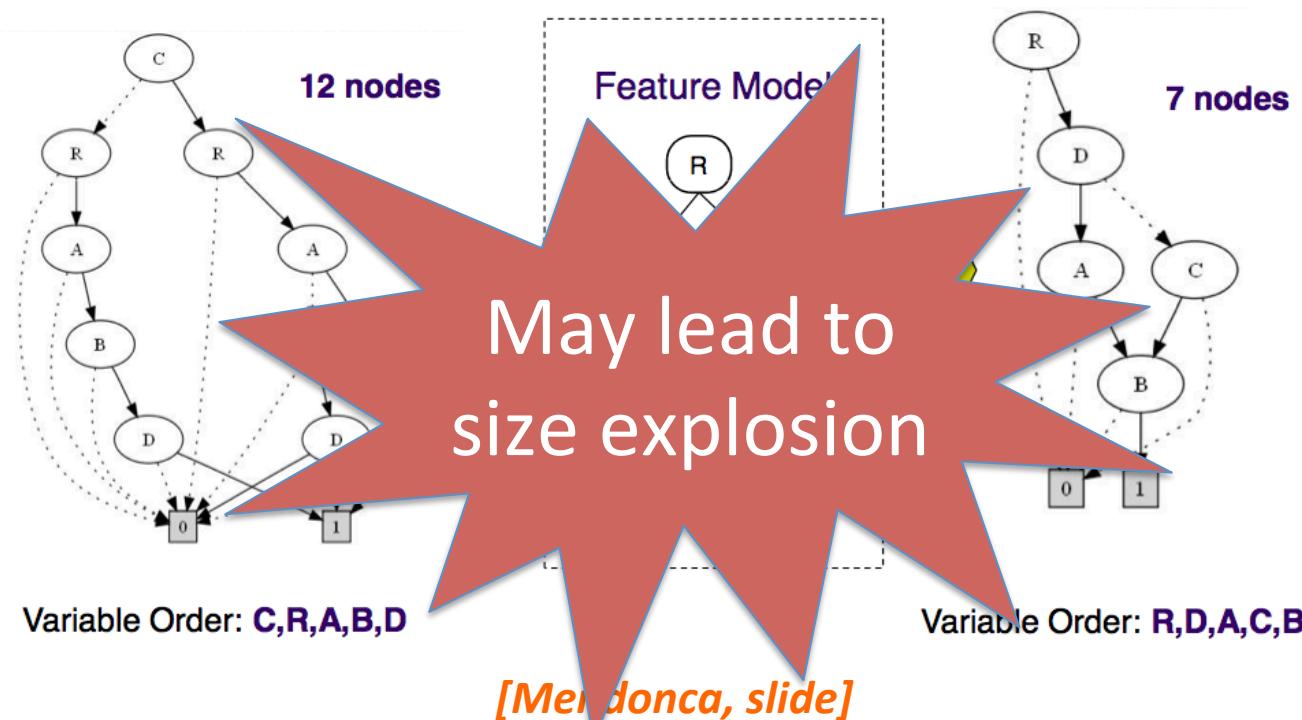


Variable Order: **C,R,A,B,D**

Variable Order: **R,D,A,C,B**

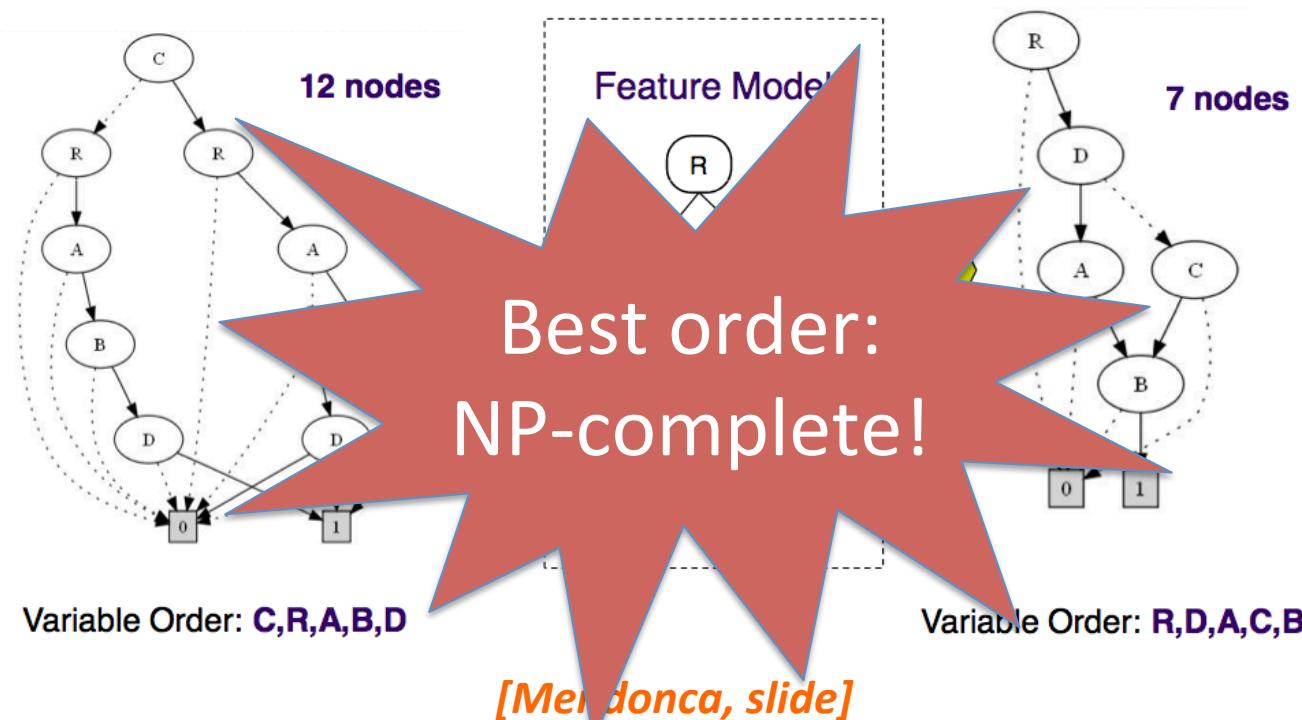
Binary Decision Diagrams (BDDs): Theoretical Problem

- The size of the BDD is very sensitive to the order of the BDD variables
 - e.g. two equivalent BDDs for the same feature



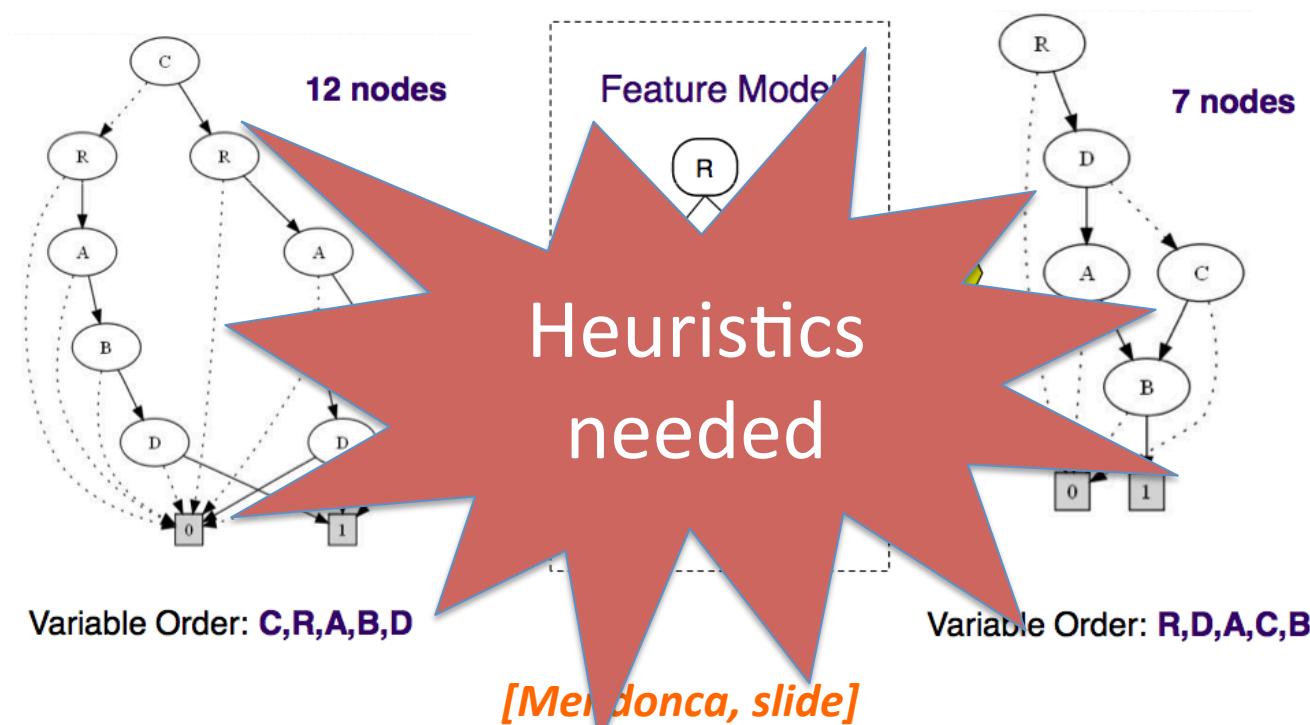
Binary Decision Diagrams (BDDs): Theoretical Problem

- The size of the BDD is very sensitive to the order of the BDD variables
 - e.g. two equivalent BDDs for the same feature



Binary Decision Diagrams (BDDs): Practical Problem

- The size of the BDD is very sensitive to the order of the BDD variables. In practice: **BDDs cannot be build for feature models with 2000+ features**

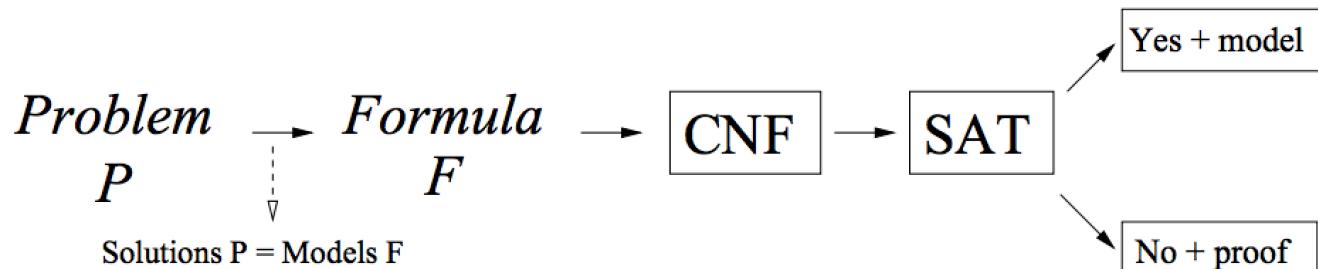


How to automate analysis of your feature models?

Let us try with SAT solvers

Satisfiability (SAT) solver

- A “SAT solver” is a program that automatically decides whether a propositional logic formula is satisfiable.
 - If it is satisfiable, a SAT solver will produce an example of a truth assignment that satisfies the formula.



- Basic idea: since all NP-complete problems are mutually reducible:
 - Write one really good solver for NP-complete problems (in fact, get lots of people to do it. Hold competitions.)
 - Translate your NP-complete problems to that problem.

SAT solver and CNF

- All current fast SAT solvers work on CNF
- Terminology:
 - A literal is a propositional variable or its negation (e.g., p or $\neg q$).
 - A clause is a disjunction of literals (e.g., $(p \vee \neg q \vee r)$). Since \vee is associative, we can represent clauses as lists of literals.
- A formula is in conjunctive normal form (CNF) if it is a conjunction of clauses
 - e.g., $(p \vee q \vee \neg r) \wedge (\neg p \vee s \vee t \vee \neg u)$

SAT solver and Unit Propagation

- Whenever all the literals in a clause are false except one, the remaining literal must be true in any satisfying assignment (such a clause is called a **unit clause**).
 - Therefore, the algorithm can assign it to true immediately. After choosing a variable there are often many unit clauses.
 - Setting a literal in a unit clause often creates other unit clauses, leading to a cascade.

$$\{\neg p \vee q, \neg p \vee \neg q \vee r, p, \neg r\}.$$

$$\begin{array}{c|c} \begin{array}{l} \neg p \vee q \\ \neg p \vee \neg q \vee r \\ p \\ \neg r \end{array} & \begin{array}{l} q \\ \neg q \vee r \\ \neg r \end{array} \end{array}$$

- A good SAT solver often spends 80-90% of its time in unit propagation.

$$\begin{aligned}
\mathcal{F}_{\text{unit}} := & (\neg x_1 \vee \neg x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_2 \vee x_3) \\
& (\neg x_1 \vee x_2) \wedge (x_1 \vee x_3 \vee x_6) \wedge (\neg x_1 \vee x_4 \vee \neg x_5) \\
& (x_1 \vee \neg x_6) \wedge (x_4 \vee x_5 \vee x_6) \wedge (x_5 \vee \neg x_6)
\end{aligned}$$

$$\begin{aligned}
\mathcal{F}_{\text{unit}} := & (\neg \textcolor{red}{x}_1 \vee \neg x_3 \vee x_4) \wedge (\neg \textcolor{red}{x}_1 \vee \neg x_2 \vee x_3) \\
& (\neg \textcolor{red}{x}_1 \vee x_2) \wedge (\textcolor{green}{x}_1 \vee x_3 \vee x_6) \wedge (\neg \textcolor{red}{x}_1 \vee x_4 \vee \neg x_5) \\
& (\textcolor{green}{x}_1 \vee \neg x_6) \wedge (x_4 \vee x_5 \vee x_6) \wedge (x_5 \vee \neg x_6)
\end{aligned}$$

$$\varphi = \{x_1=1\}$$

$$\begin{aligned}\mathcal{F}_{\text{unit}} := & (\neg x_1 \vee \neg x_3 \vee x_4) \wedge (\neg x_1 \vee \neg x_2 \vee x_3) \\ & (\neg x_1 \vee x_2) \wedge (\textcolor{green}{x_1} \vee x_3 \vee x_6) \wedge (\neg x_1 \vee x_4 \vee \neg x_5) \\ & (\textcolor{green}{x_1} \vee \neg x_6) \wedge (x_4 \vee x_5 \vee x_6) \wedge (x_5 \vee \neg x_6)\end{aligned}$$

$$\varphi = \{x_1=1, \textcolor{teal}{x_2=1}\}$$

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\end{aligned}$$

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\end{aligned}$$

$$\varphi = \{x_1=1, x_2=1, x_3=1, x_4=1\}$$

SAT solver and Unit Propagation

BCP():

Repeatedly search for unit clauses, and
set unassigned literal to required value.

If a literal is assigned conflicting values, return F
else return T;

satisfy(ϕ) {

if every clause of ϕ has a true literal, return T;

if BCP() == F, return F;

assign appropriate values to all pure literals;

choose an $x \in V$ that is unassigned in A ,

and choose $v \in \{T, F\}$.

$A(x) = v$;

if satisfy(ϕ) return T;

$A(x) = \neg v$;

if satisfy(ϕ) return T;

unassign $A(x)$; // undo assignment for backtracking.

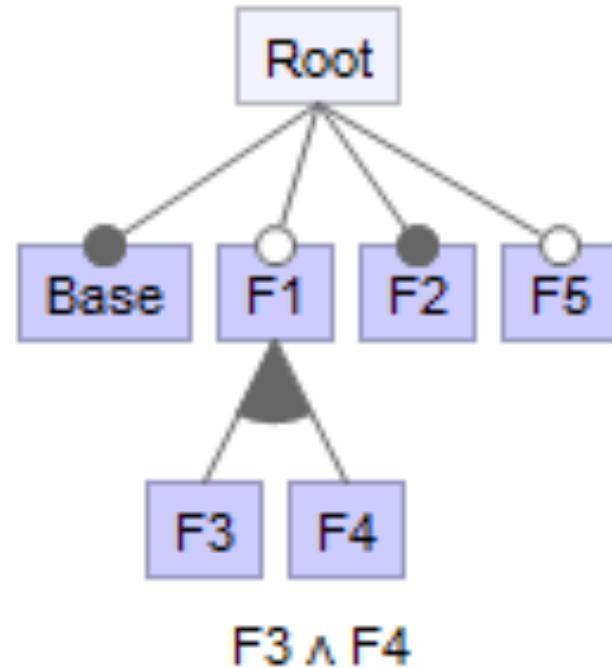
return F; }

How to automate analysis of your feature models?

Let us use BDDs and SAT solvers

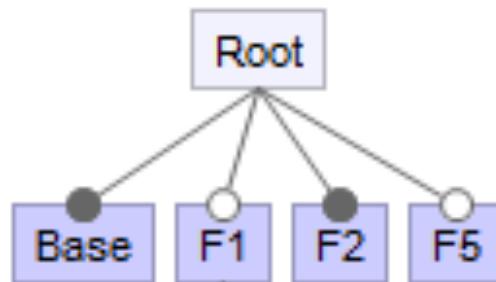
Consistency

- SAT-Solver
 - SAT(FM)



Core and dead features

- Dead : $\text{SAT}(\text{FM} \wedge F)$
- Core: $\text{SAT}(\text{FM} \wedge \text{not}(F))$



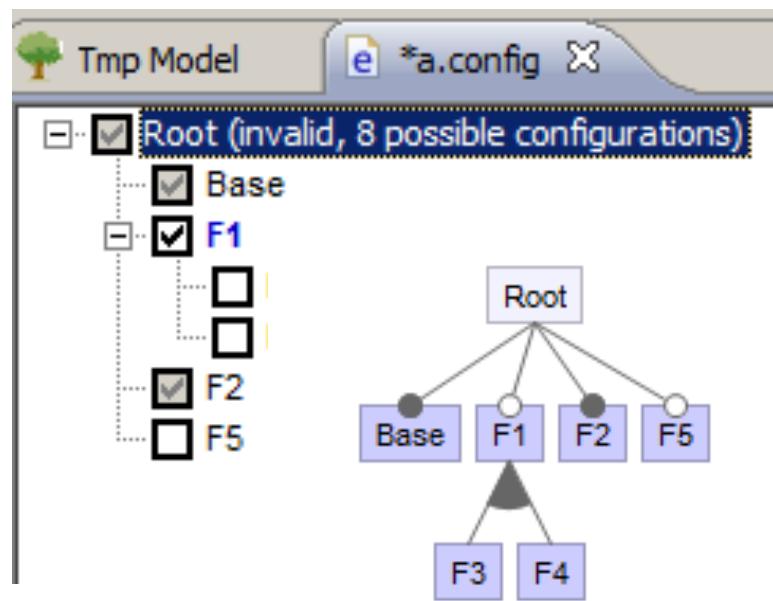
$$F5 \Rightarrow F4 \vee \text{Base}$$

$$F3 \Rightarrow F2 \wedge F5$$

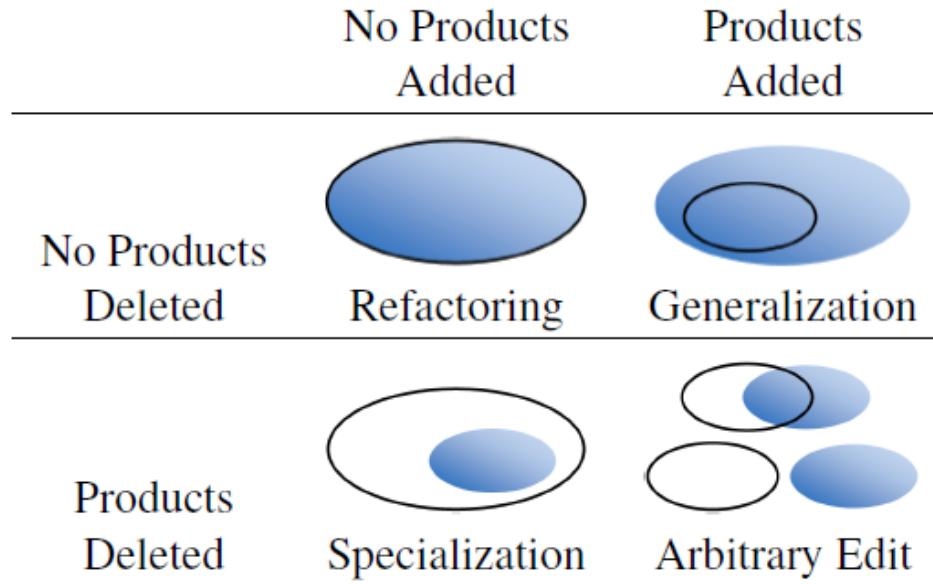
$$\neg(F4 \wedge F2)$$

Partial configuration

- $\text{SAT}(\text{FM} \wedge \text{PK} \wedge \text{F})$
- $\text{SAT}(\text{FM} \wedge \text{PK} \wedge \text{not(F)})$



Relationship between feature models

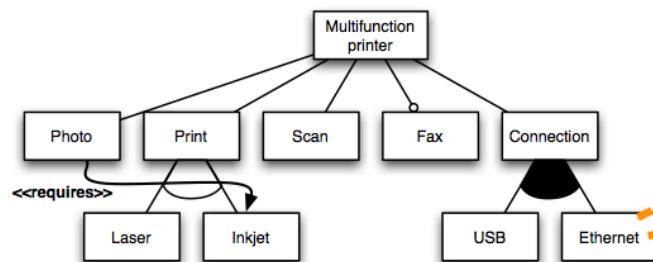


- Refactoring
 - Tautology: $(FM1 \Leftrightarrow FM2)$
 $= \text{not SAT}(\text{not } (FM1 \Leftrightarrow FM2))$

Recap

Product Derivation

feature model

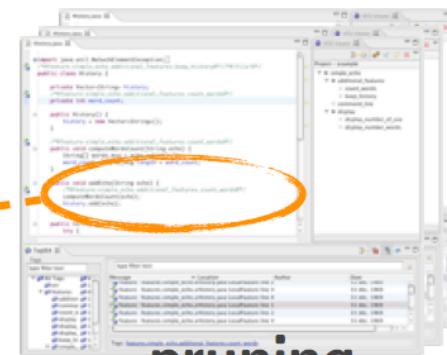
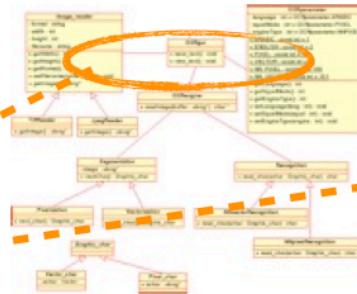


configuration

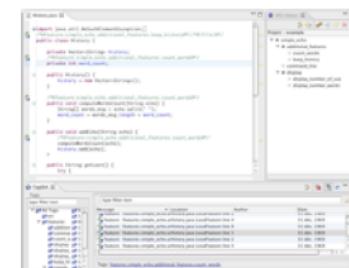
{ MP, Photo, Print, Inkjet, Scan,
Fax, Connection, USB, Ethernet }

product spec

variable model and
code assets



pruning,
composition,
weaving,
transformation



product

Feature model in the SPL framework

- Not restricted to document/communicate
- Product/model derivation
 - reminder of previous course
- Safe composition and automated reasoning
- Generation of Configurators

Feature Models

+

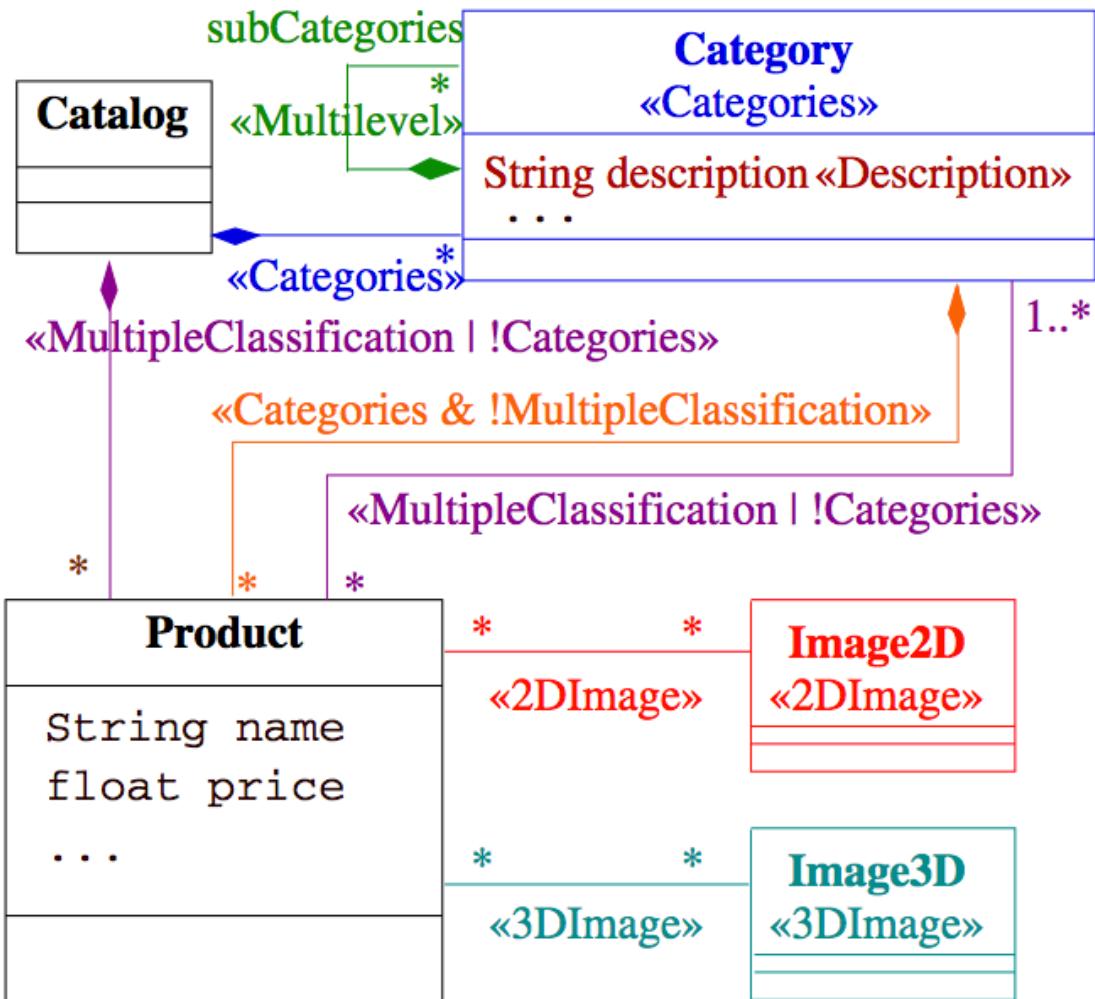
other artefacts

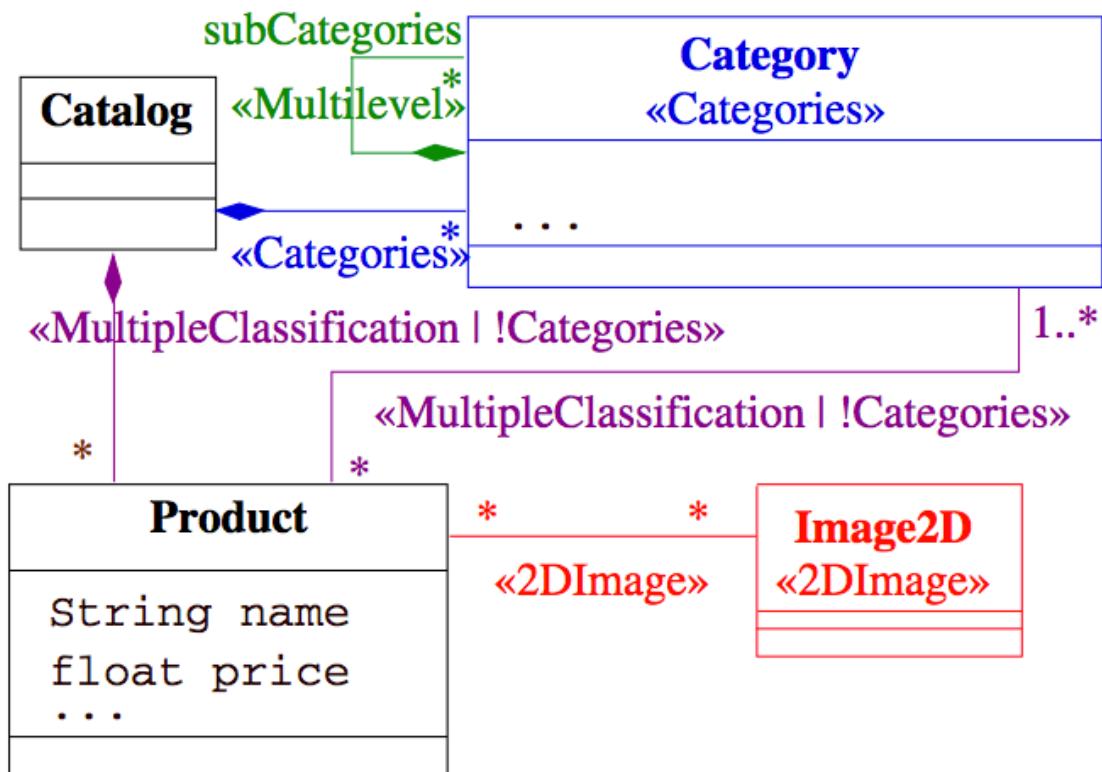
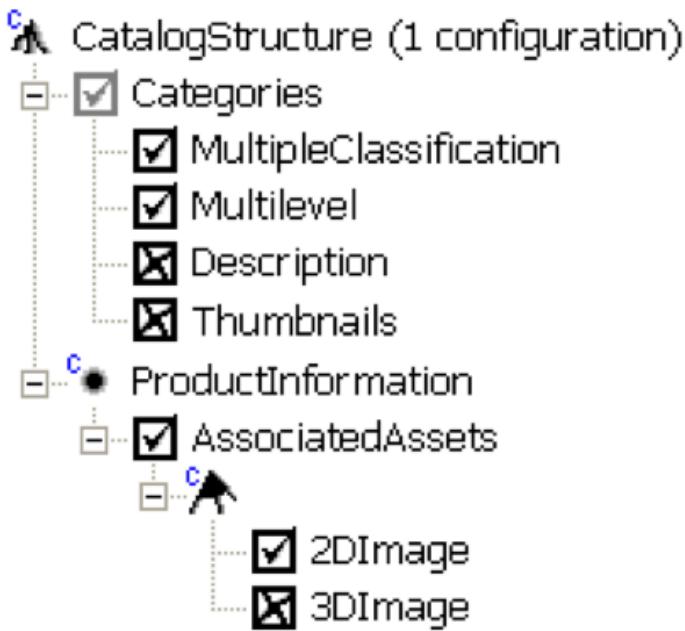
(what we can do with feature models)

**“features” in feature models:
a label**

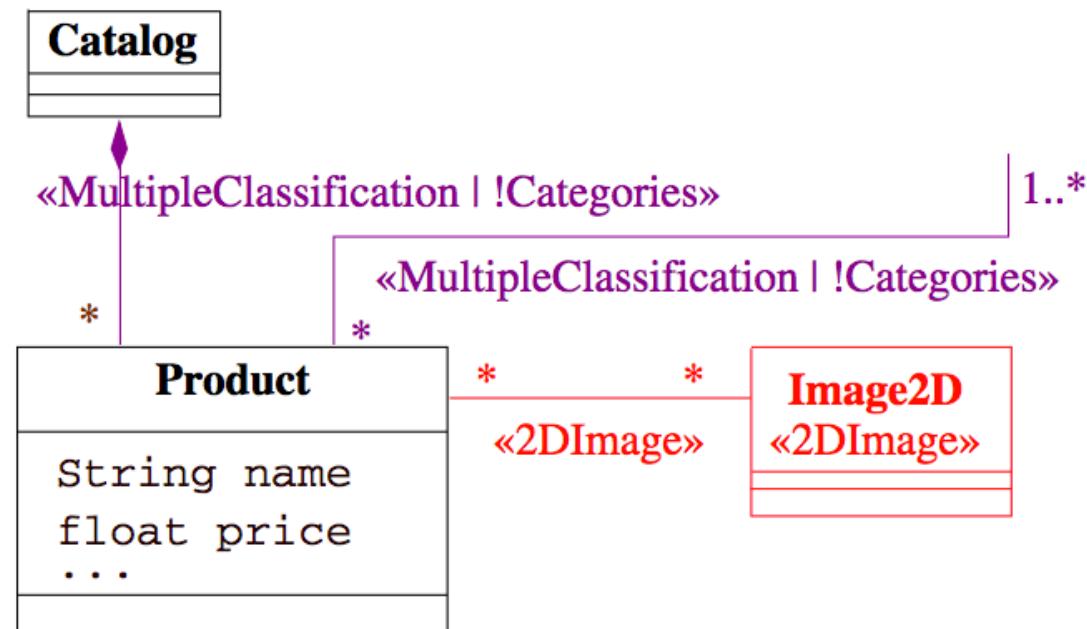
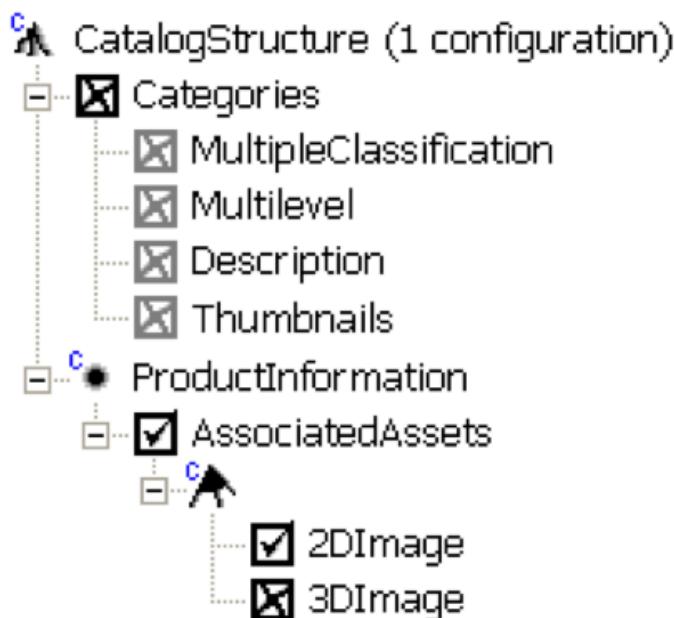
▲ CatalogStructure (52 configurations)

- Categories
 - MultipleClassification
 - Multilevel
 - Description
 - Thumbnails
- ProductInformation
 - AssociatedAssets
 - 2DImage
 - 3DImage





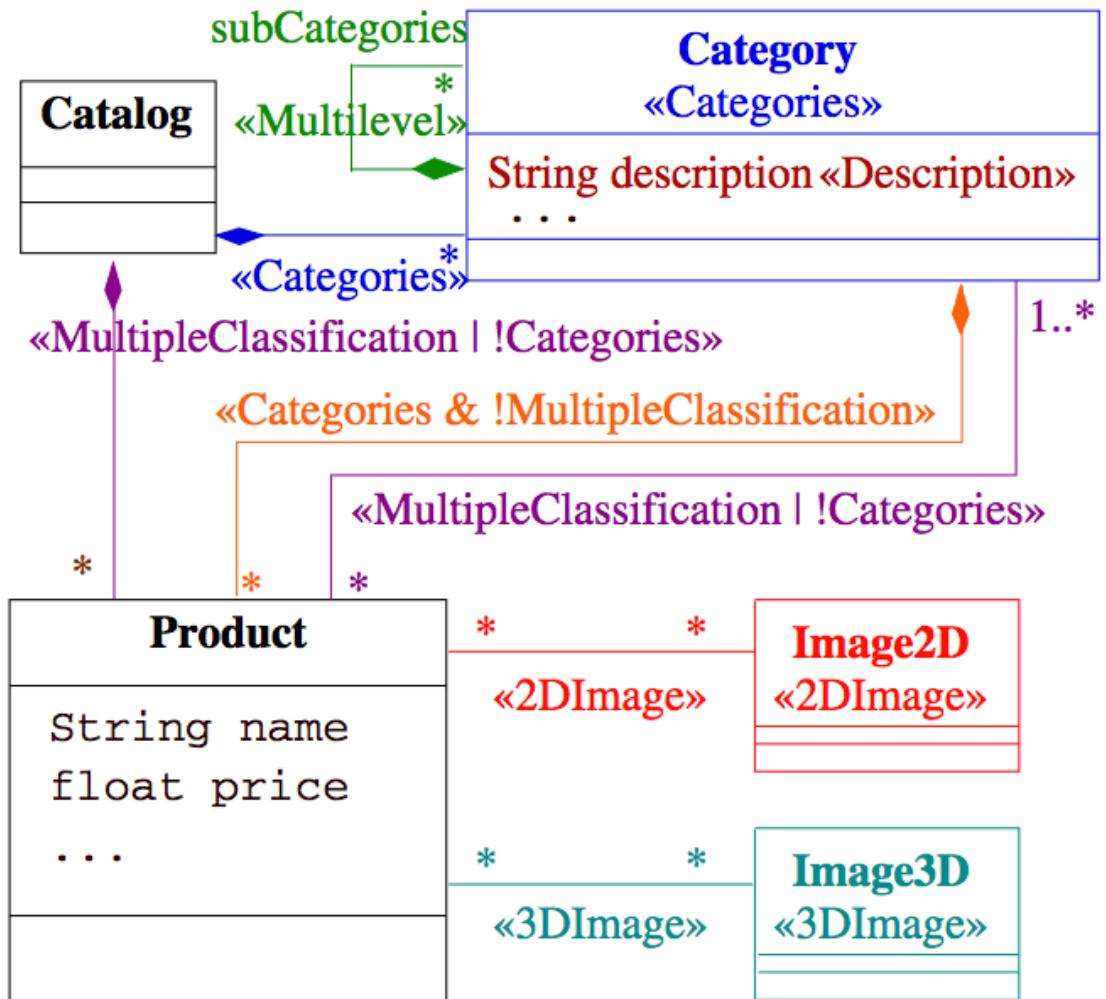
Ooops



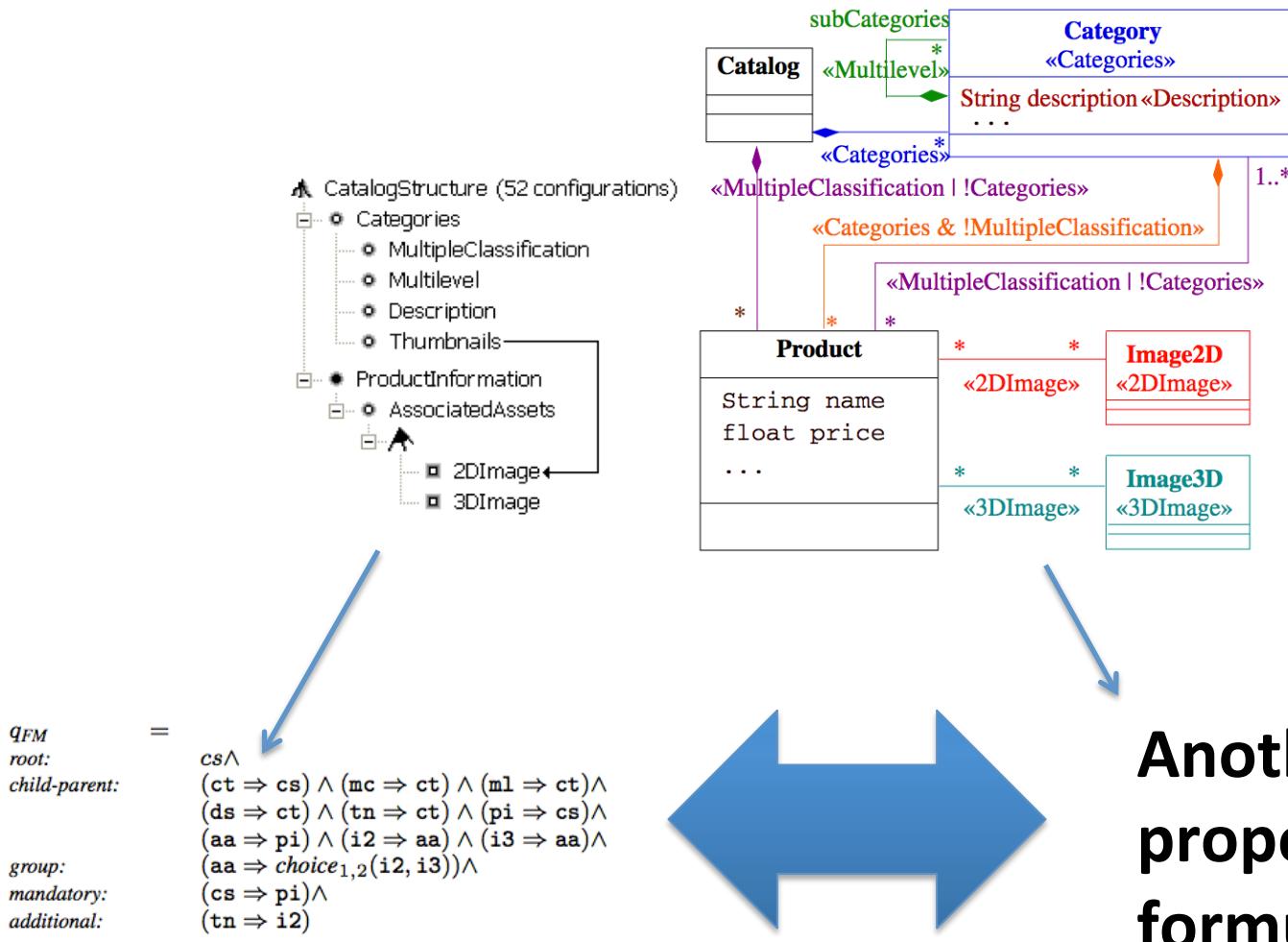
Safe composition? No!

▀ CatalogStructure (52 configurations)

- ▀ Categories
 - ▀ MultipleClassification
 - ▀ Multilevel
 - ▀ Description
 - ▀ Thumbnails
- ▀ ProductInformation
 - ▀ AssociatedAssets
 - ▀ 2DImage
 - ▀ 3DImage



Safe composition: how does it work?



**Another
propositional
formula**

Mapping: an example

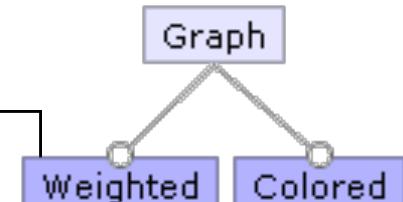
```
class Graph {  
    Vector nv = new Vector(); Vector ev = new Vector();  
    Edge add(Node n, Node m) {  
        Edge e = new Edge(n, m);  
        nv.add(n); nv.add(m); ev.add(e);  
        e.weight = new Weight();  
        return e;  
    }  
    Edge add(Node n, Node m, Weight w)  
    Edge e = new Edge(n, m);  
    nv.add(n); nv.add(m); ev.add(e);  
    e.weight = w; return e;  
}  
void print() {  
    for(int i = 0; i < ev.size(); i++) {  
        ((Edge)ev.get(i)).print();  
    }  
}
```

```
class Node {  
    int id = 0;  
    Color color = new Color();  
    void print() {  
        Color.setDisplayColor(color);  
        System.out.print(id);  
    }  
}
```

```
class Edge {  
    Node a, b;  
    Color color = new Color();  
    Weight weight = new Weight();  
    Edge(Node _a, Node _b) { a = _a; b = _b; }  
    void print() {  
        Color.setDisplayColor(color);  
        a.print(); b.print();  
        weight.print();  
    }  
}
```

```
class Color {  
    static void setDisplayColor(Color c) { ... }  
}
```

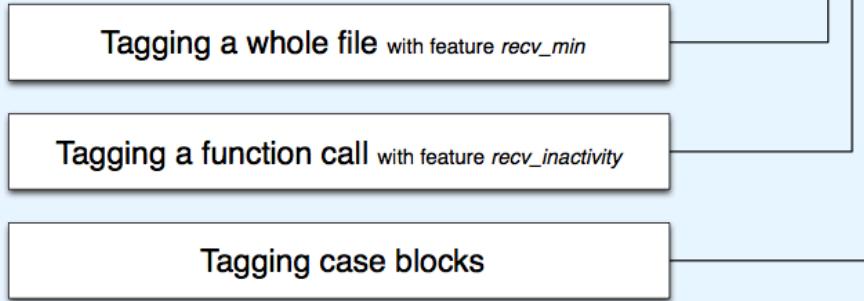
```
class Weight { void print() { ... } }
```



Tag and prune

Tag: annotate blocks of code with *features*

Prune: remove blocks tagged with non-selected features



```

/*@feature:recv_min@*/ /*@file_feature@*/
void cfdp_receiver_handle_PDU(cfdp_receiver* const me, struct cfdp_buffer* PDU_buffer,
CFDP_PDU_type_t PDU_type) {
    /*@feature:recv_inactivity@*/
    /* Restart inactivity timer */
    cfdp_timer_start(&(me->timer_inactivity), me->config.timeout_inactivity);
}

/* Handle PDU and dispatch it depending on its type */
switch (PDU_type)
{
    /*@feature:recv_min_ack@*/
    case CFDP_PDU_ACK_FINISHED:
    {
        cfdp_receiver_handle_PDU_ack_no_error(me, PDU_buffer);
    }
    break;

    case CFDP_PDU_EOF_NO_ERROR:
    {
        cfdp_receiver_handle_PDU_eof_no_error(me, PDU_buffer);
    }
    break;

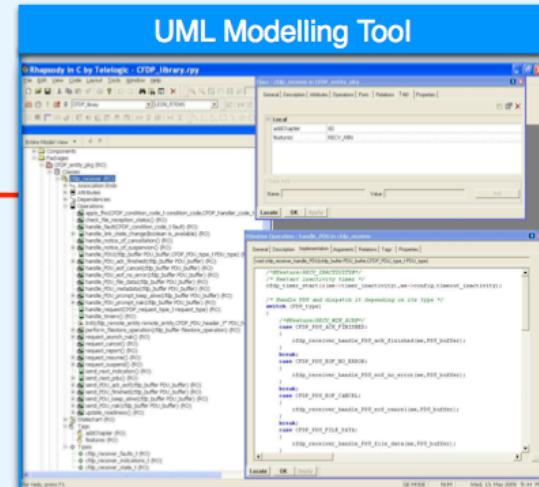
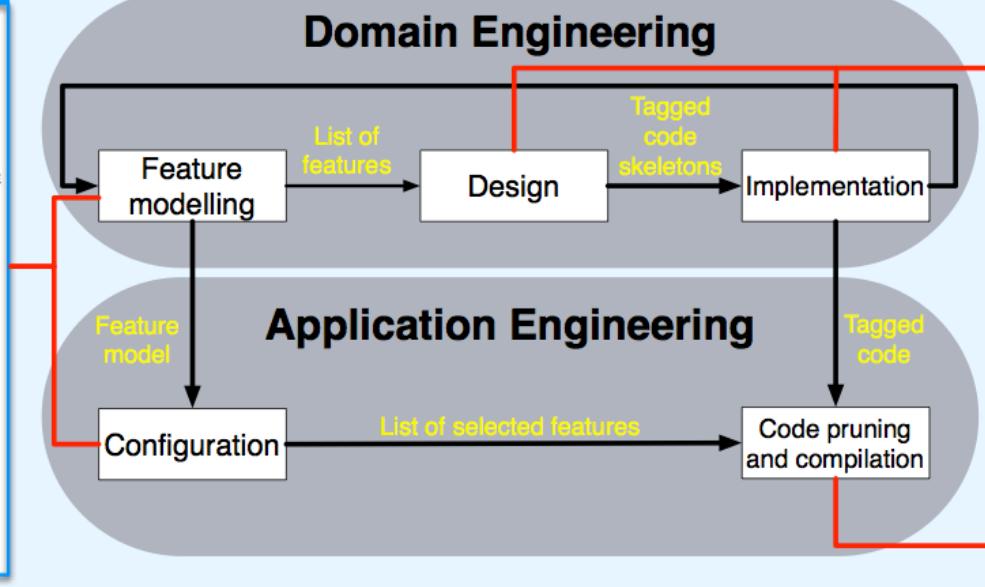
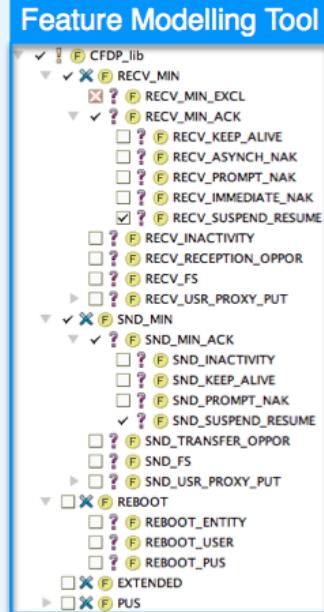
    /*@feature:recv_keep_alive@*/
    case CFDP_PDU_PROMPT_KEEP_ALIVE:
    {
        cfdp_receiver_handle_PDU_prompt_keep_alive(me, PDU_buffer);
    }
    break;

    /*@feature:recv_prompt_nak@*/
    case CFDP_PDU_PROMPT_NAK:
    {
        cfdp_receiver_handle_PDU_prompt_nak(me, PDU_buffer);
    }
    break;

    default:
    {
        /* Unexpected PDU -> discard */
    }
    break;
}
}

```

AST (Abstract Syntax Tree) diagram showing the structure of the annotated code.

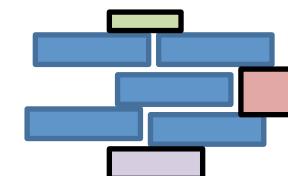
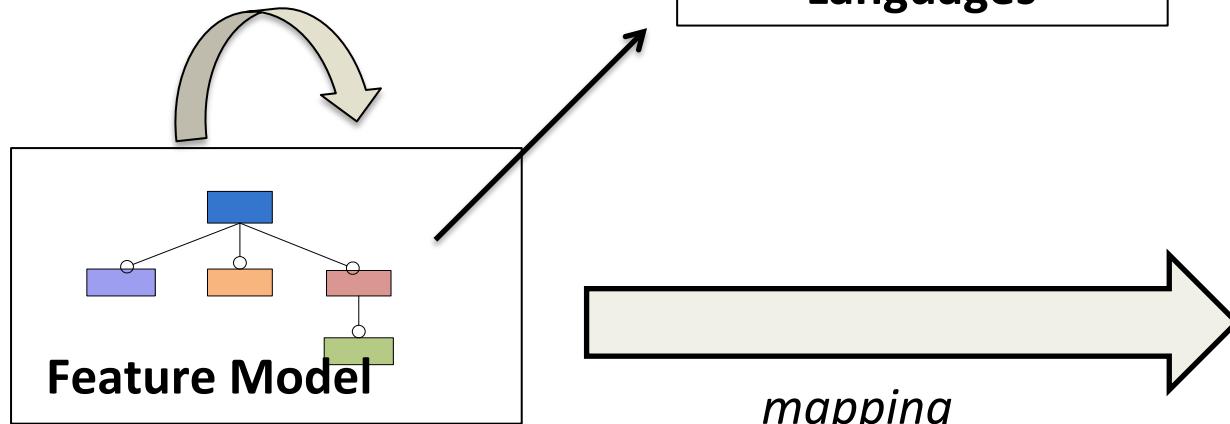


Summary

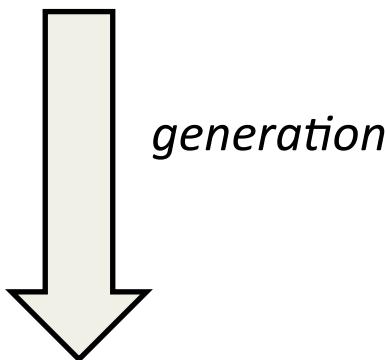
- Variability models for mastering complexity: variability is everywhere, precise specification, automated reasoning and management
- Feature Models
 - **Formalism and theory**: modeling principles, semantics, logics
 - **Language** to elaborate/build variability models
 - **Reasoning techniques** to analyze properties of an SPL (scalable and automated way)
 - **Applications**: variability models and so what?

reasoning and management

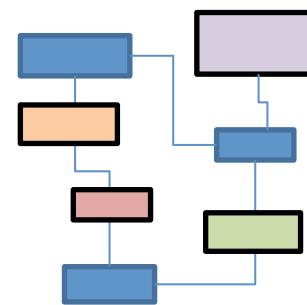
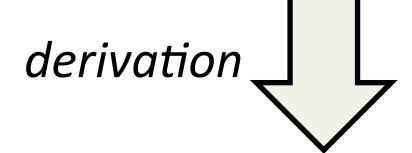
Textual or Graphical Languages



Reusable Assets
(e.g., models or source code)



Configurators



product_n

How can I use automated
analysis in practice?

Like this?

```
// Feature model path
String featureModelPath = "/Users/mathieuacher/Documents/workspaceScala/SPLAR/resources/models/simple_bike_fm.xml";

// Create feature model object from an XML file (SXF format - see www.splot-research.org for details)
// If an identifier is not provided for a feature use the feature name as id
FeatureModel featureModel = new XMLFeatureModel(featureModelPath, XMLFeatureModel.USE_VARIABLE_NAME_AS_ID);
// load feature model from
featureModel.loadModel();

// create BDD variable order heuristic
new FTPreOrderSortedECTraversalHeuristic("Pre-CL-MinSpan", featureModel, FTPreOrderSortedECTraversalHeuristic.FORCE_SORT);
VariableOrderingHeuristic heuristic = VariableOrderingHeuristicsManager.createHeuristicsManager().getHeuristic("Pre-CL-MinSpan");

// BDD construction parameters
// - Tuning this parameters can be tricky at times and may require playing a bit
// - For the purpose of this example let's assume "large enough" values
int bddNodeNum = 10000;      // sets the initial size of the BDD table
int bddCacheSize = 10000;    // sets the size of the BDD cache table

// Creates the BDD reasoner
ReasoningWithBDD reasoner = new FMReasoningWithBDD(featureModel, heuristic, 50000, 50000, 60000, "pre-order");

// Initialize the reasoner (BDD is created at this moment)
reasoner.init();

// Use the reasoner
System.out.println("BDD has " + reasoner.getBDD().nodeCount() + " nodes and was built in " + reasoner.getBDDBuildingTime() + " ms");

// Check if feature model is consistent, i.e., has at least one valid configuration
System.out.println("Feature model is " + (reasoner.isConsistent()? "" : " NOT ") + "consistent!");

// Count feature model solutions
System.out.println("Feature model has " + reasoner.countValidConfigurations() + " possible configurations");
```

No. Like this!

```
fml = FM ("fml.tvl") // many other formats
b1 = isValid fml // check consistency
n1 = counting fml // count the number of valid configurations
```

```
// Feature model path
String featureModelPath = "/Users/mathieuacher/Documents/workspaceScala/SPLAR/resources/models/simple_bike_fm.xml";

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featureModel.loadModel();

// create BDD variable order heuristic
new FTPreOrderSortedECTraversalHeuristic("Pre-CL-MinSpan", featureModel, FTPreOrderSortedECTraversalHeuristic.FORCE_SORT);
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```

FAMILIAR: domain-specific language

```
fml = FM ("fml.tvl") // many other formats
b1 = isValid fml // check consistency
n1 = counting fml // count the number of valid configurations
```

more concise and readable
no need to learn a (set of) API
interoperability facilities

```
// Feature model path
String featureModelPath = "/Users/mathieuacher/Documents/workspaceScala/SPLAR/resources/models/simple_bike_fm.xml";
// Create feature model object from XML file (SXF format - see www.splot-research.org for details)
// If feature identifiers in the feature model do not use the feature name as id
FeatureModel featureModel = new XMLFeatureModel(featureModelPath, XMLFeatureModel.USE_VARIABLE_NAME_AS_ID);
// load feature model from
featureModel.loadModel();

// create BDD variable order heuristic
new FPTreePreOrderSortedECTraversalHeuristic("Pre-CL-MinSpan", featureModel, FPTreePreOrderSortedECTraversalHeuristic.FORCE_SORT);
VariableOrderingHeuristic heuristic = VariableOrderingHeuristicsManager.createHeuristicsManager().getHeuristic("Pre-CL-MinSpan");

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System.out.println("Feature model has " + reasoner.countValidConfigurations() + " possible configurations");
```

See FAMILIAR