



Software Product Line Engineering

**modeling and managing variability
of software intensive systems**

Dr. Mathieu Acher
email: macher@fundp.ac.be

Prof. Patrick Heymans

University of Namur
PReCISE Research Centre

Material

- http://www.fundp.ac.be/etudes/cours/page_view/INFOM435/
 - Folder: “Documents_sur_VariabilityAndSPL”
 - Slides, exercises, evaluation

The screenshot shows a web-based course management system interface. At the top, there is a navigation bar with links like "Mon bureau", "Liste de mes cours", "Mon compte utilisateur", and "Quitter". Below the navigation bar, the page title is "Questions spéciales d'ingénierie du logiciel et de l'information INFOM435 - Vincent ENGLEBERT, Naji HABRA, Jean-Luc HAIAUT, Patrick HEYMANS". The main content area displays a table of files under the heading "Documents et liens". The table has columns for "Nom", "Taille", "Date", "Modifier", "Supprimer", "Déplacer", and "Visibilité". The files listed are:

Nom	Taille	Date	Modifier	Supprimer	Déplacer	Visibilité
CoursAgile			X	X	X	X
CoursQualité			X	X	X	X
documentation_MoCQA			X	X	X	X
documents_sur_VariabilityAndSPL			X	X	X	X
Software product line engineering (variability modeling and management)			X	X	X	X
Syllabus						

- Email: macher@fundp.ac.be

I reuse some material from
other colleagues (Czarnecki,
Kästner, etc.)

I try to mention references as
much as possible

(you can find/read them on
the web or simply ask me!)

[Czarnecki et al. (GPCE'05)]

Previously

- **Domain engineering pays off**
 - software product line engineering changes the way software should be developed
 - reuse-in-the-large works best in families of related systems, and thus is domain dependent



- **Application engineering**
- **Generative, model-based approaches**
 - customized products automatically generated from specifications (models)

Today: Feature Models

defacto standard for
modeling variability

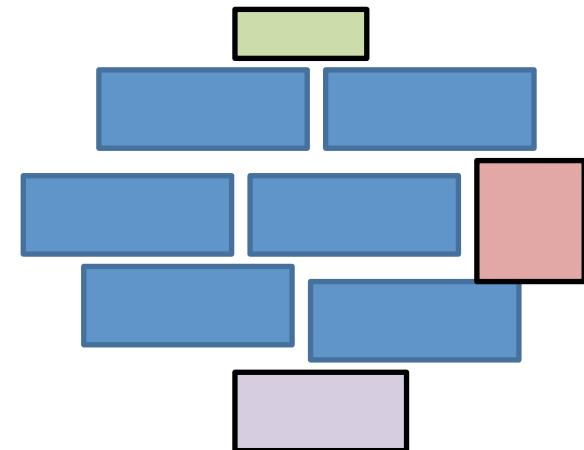
Today

- **Variability modeling**
- **Feature models**
 - What's in a feature?
 - Rationale
 - Syntax and semantics
 - Overview of automated techniques
- **Feature model in the SPL framework**
 - not restricted to document/communicate
 - safe composition
 - configurators
 - product/model derivation

Software Product Line Engineering

Factoring out **commonalities**

for **Reuse** [Krueger et al., 1992] [Jacobson et al., 1997]



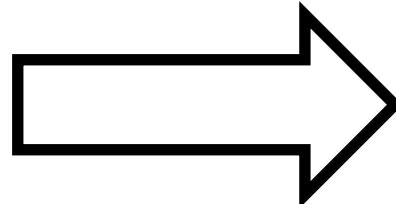
Managing **variabilities**

for Software **Mass Customization** [Bass et al., 1998] [Krueger et al., 2001], [Pohl et al., 2005]



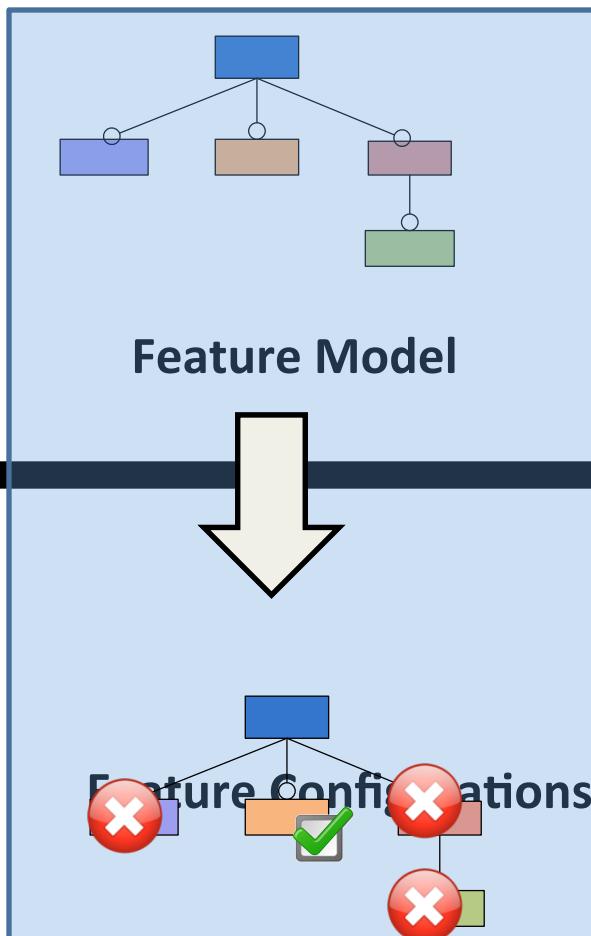
Software product line engineering

= modeling and managing variability



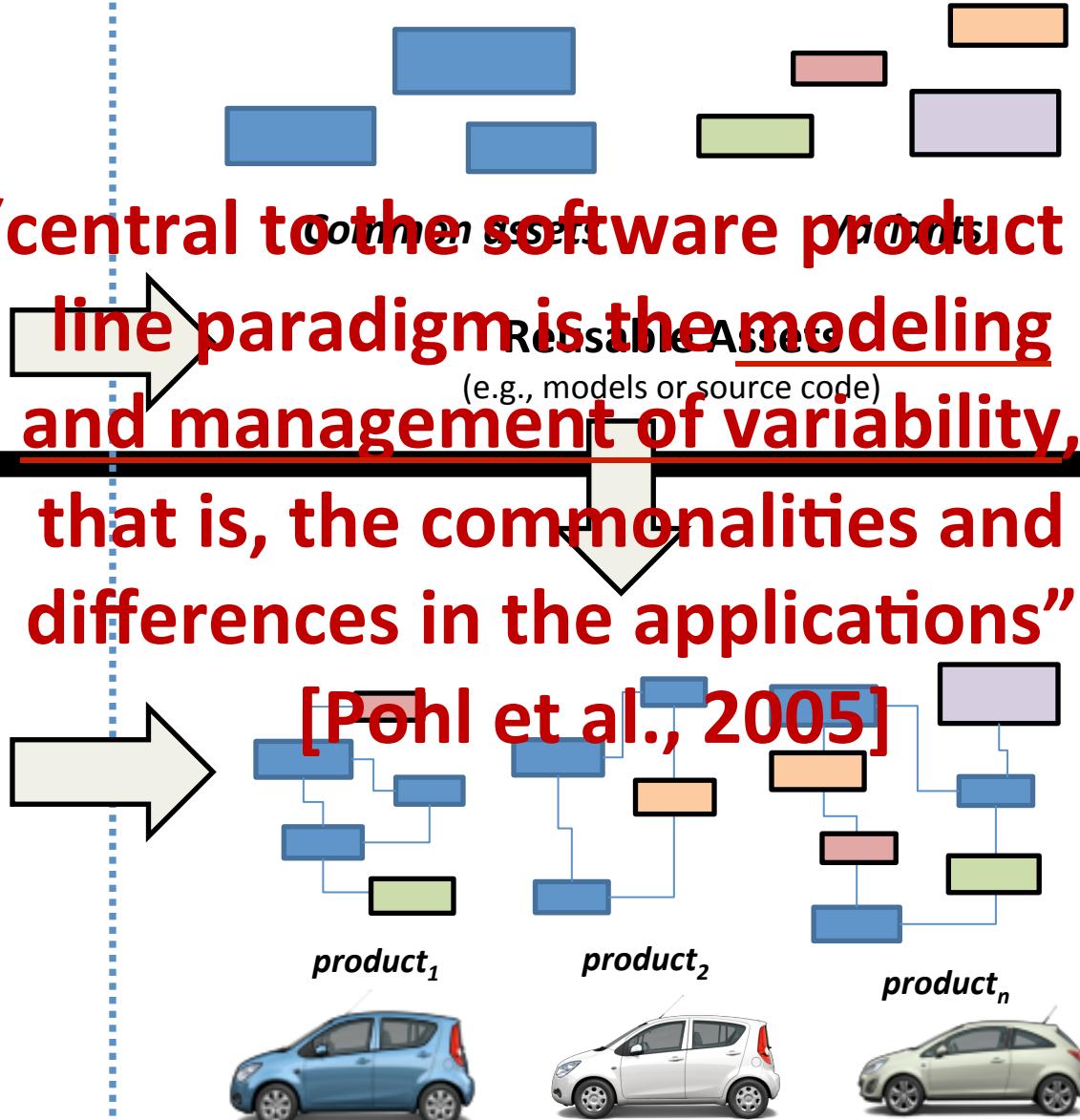
Variability Models: Why?

Domain engineering (development for reuse)



“central to the software product line paradigm is the modeling and management of variability, that is, the commonalities and differences in the applications”

[Pohl et al., 2005]



Application engineering (development with reuse)

#1 precise specification is needed

A photograph of an old, green-painted pickup truck that has been left to decay in a field. The truck is heavily rusted, particularly on the body and the front fenders. The driver's side door is open, revealing the interior frame and some remaining mechanical components. The truck is positioned in front of a dense thicket of green bushes and tall grass. In the bottom left corner, there are some wooden planks and metal debris, suggesting a construction or demolition site nearby.

Unused flexibility

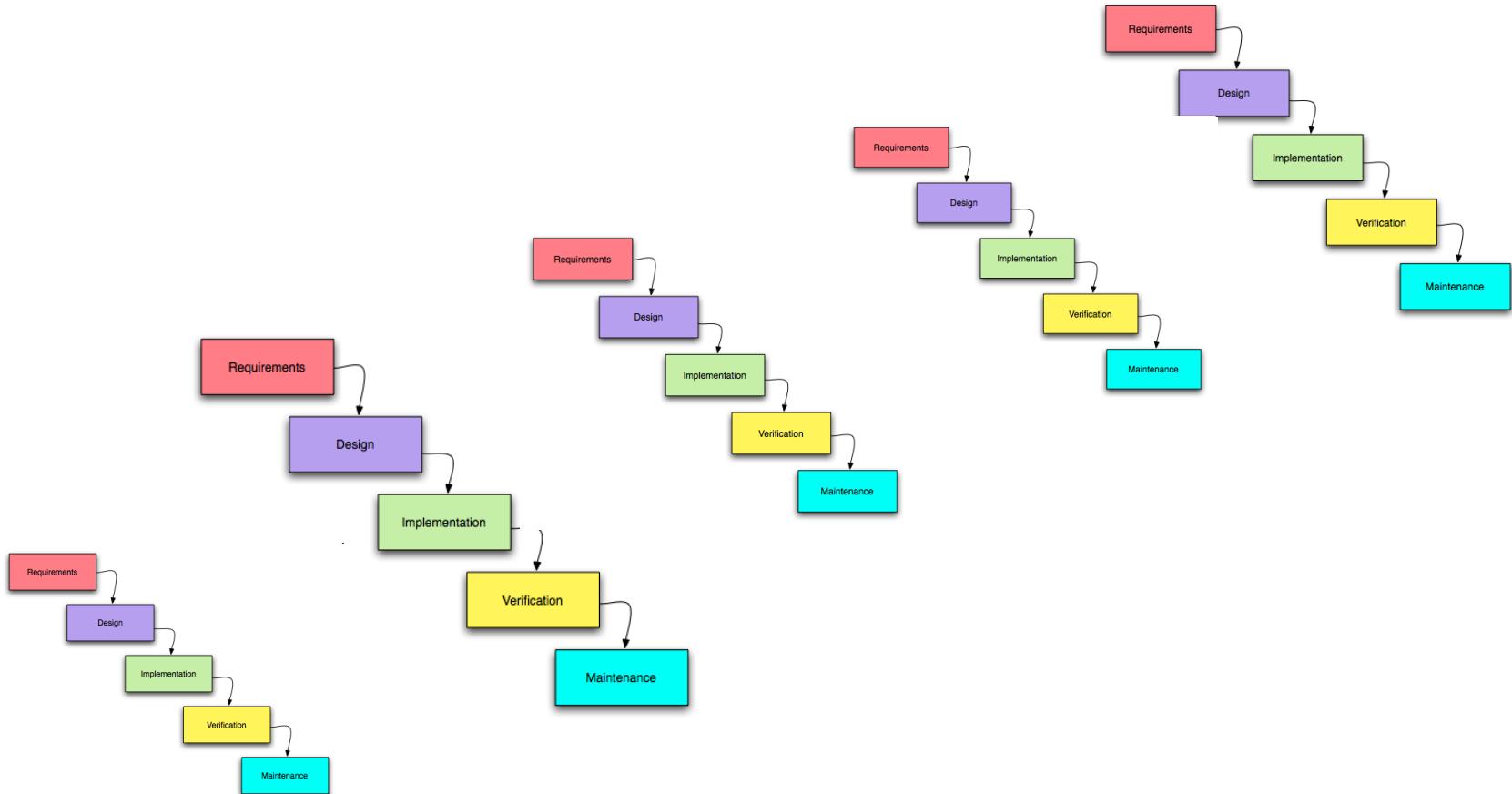
A police car is engulfed in intense orange and yellow flames, with thick black smoke billowing upwards. The car is a white sedan with "766" on the side and "POLICE" and "To Serve & Protect" on the front. It is parked on a city street at night. In the background, there are other buildings, including one with "GUESS" and "DRUM SHOP" signs. Several people are standing around the burning vehicle, some appearing to take pictures or videos. The scene is chaotic and suggests a protest or riot.

DRUM SHOP

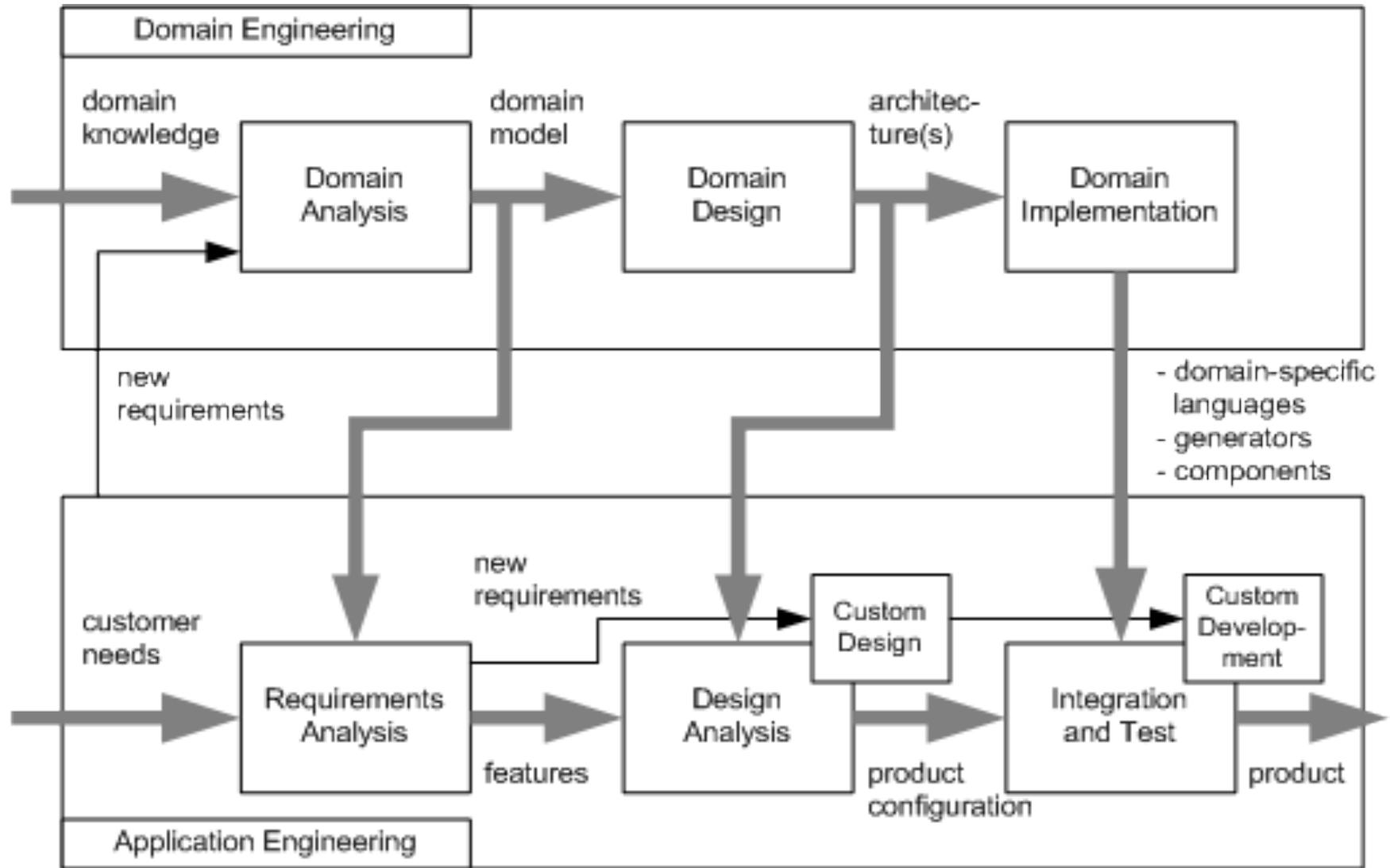
Illegal Variant

**#2 variability cross cuts all
artefacts (from requirements to
code)**

Software Product Line Development?



Time and Effort: not scalable!



#3 automated reasoning (exponential number of products)

Variability = Complexity

33 features



a unique variant for every
person on this planet

320^{optional, independent}
features

more variants than estimated
atoms in the universe

Variability Models: Why?

Variability Model

- **Abstraction** of some aspect of a system under study (SUS)
 - Some details are hidden or removed to **simplify** and focus attention
 - **general** concepts can be formulated by abstracting common properties of instances
- In a form that can be **mechanically analyzed**
 - properties of a SUS
 - model transformation

Variability Models: Why?

(3 arguments, summary)

precise specification

**variability is orthogonal / cross cuts different
artefacts**

automated reasoning/analysis

Variability models are needed. Now we are convinced...

#1 Formalism? Theory?
modeling principles, semantics, logics

#2 Language?
to elaborate/build variability models

#3 Reasoning techniques?
to analyze properties of an SPL (scalable and automated way)

#4 Applications of variability models?
variability model and so what?

**Feature models, the defacto
standard for modeling variability**

Feature Model: de facto standard for modeling variability

- Central to many software product line approaches
 - More than 1000 citations of [Kang et al., 1990] per year
 - Generative programming [Czarnecki et al., 2000]
- Research effort
 - Formal Semantics [Schobbens et al., 2007]
 - Automated Reasoning Techniques [Batory et al., 2005], [Czarnecki et al., 2007], [Mendonca et al., 2009], [Thuem et al., 2009], [Janota et al., 2010], [Benavides et al., 2010]
- Tools
 - Commercial: pure::variants, Gears
 - Academic: fmp, FeatureIDE, FaMa, SPLOT, TVL, et al.



Feature Models

#1 Formalism? Theory?

modeling principles, semantics, logics

#2 Language?

to elaborate/build variability models

#3 Reasoning techniques?

to analyze properties of an SPL (scalable and automated way)

#4 Applications of variability models?

variability model and so what?

Feature Models

#1 Formalism? Theory?
modeling principles, semantics, logics

Feature modeling is...

- ... about identifying
 - common features of concepts
 - variable features of concepts
 - and their dependencies
- and documenting them in a coherent model, called a feature model
- Feature modeling is a core activity of important domain-engineering methods

Feature and Psychology

- The Human mind processes lots of information
- Important function of human information processing: reducing the amount of information
- • Describing concepts by features, e.g. a bird has wings, a bill, and can fly
- • Classifying a perceptual unit as an instance of a concept, e.g. Aunt Martha's bird
- Acquisition and evolution of concepts will not be pursued further in this course

Modeling variability (and commonality)

- **Features** are a convenient way to think about commonalities and differences between family members
 - e.g. "MP3 player" – some phones have it, some don't
- Features are usually **coarse-grained** abstractions
 - Abstract from detailed requirements
 - e.g. "when in shuffle mode, the player selects the next track using a random function"
 - Abstract from design/implementation details
 - e.g. "MP3_Player is a concrete subclass of Media_Player"
- An **old but still vague** notion

What's a **feature**? (survey excerpt)

- *[Kang et al.]* “a prominent or distinctive **user-visible aspect, quality or characteristic** of a software system or systems”
- *[Kang et al.]* “distinctively identifiable **functional abstractions** that must be implemented, tested, delivered, and maintained”
- *[Eisenecker and Czarnecki]*. “**anything users** or client programs might want to **control about a concept**”
- *[Bosch et al.]* “A **logical unit of behaviour** specified by a set of **functional and non-functional requirements.**”
- *[Chen et al.]* “a **product characteristic** from user or customer views, which essentially consists of a cohesive **set of individual requirements**”
- *[Batory]* “an elaboration or augmentation of an entity(s) that introduces a **new service, capability** or relationship”

What's in a feature?

- Confusion as to what a feature generally represents (i.e., blackbox view)
- Redefine “Feature” as a 3-tuple of requirements, domain assumptions and specifications
 - Requirements (R)
 - The purpose of the system (optative): ‘*Notify the police of the presence of burglars*’
 - Domain assumptions (W)
 - The given behaviour of the environment (indicative): ‘*Burglars cause movement when they break in*’ and ‘*There is a movement sensor monitoring the house, connected to the system*’
 - Specifications (S)
 - What the system should do to satisfy R given W holds (optative): ‘*Alert the police if the sensor captures movement*’

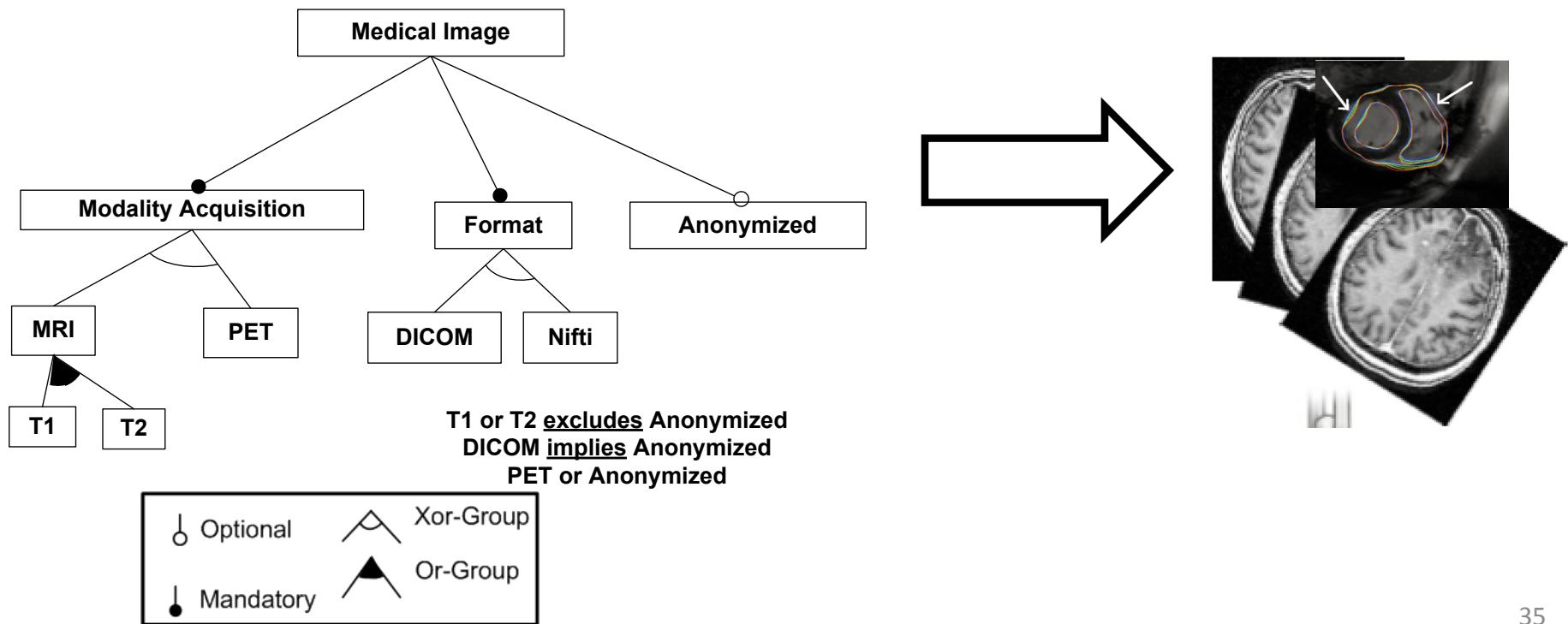
$$\frac{\text{Specification} \quad S, W \models R}{\text{Requirement} \quad \text{Domain Assumption}}$$

Feature Models

Modeling principles, syntax

Feature Models

describe the common and variable features (characteristics) of a system under study (SUS)

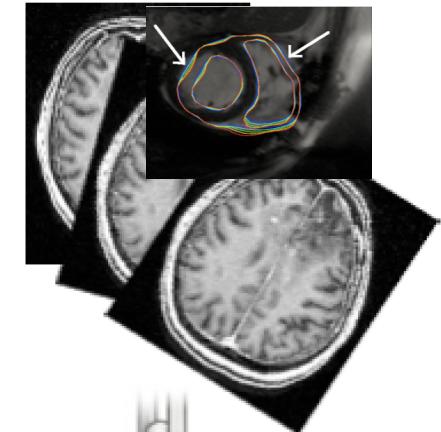
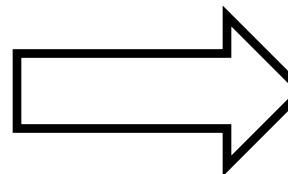
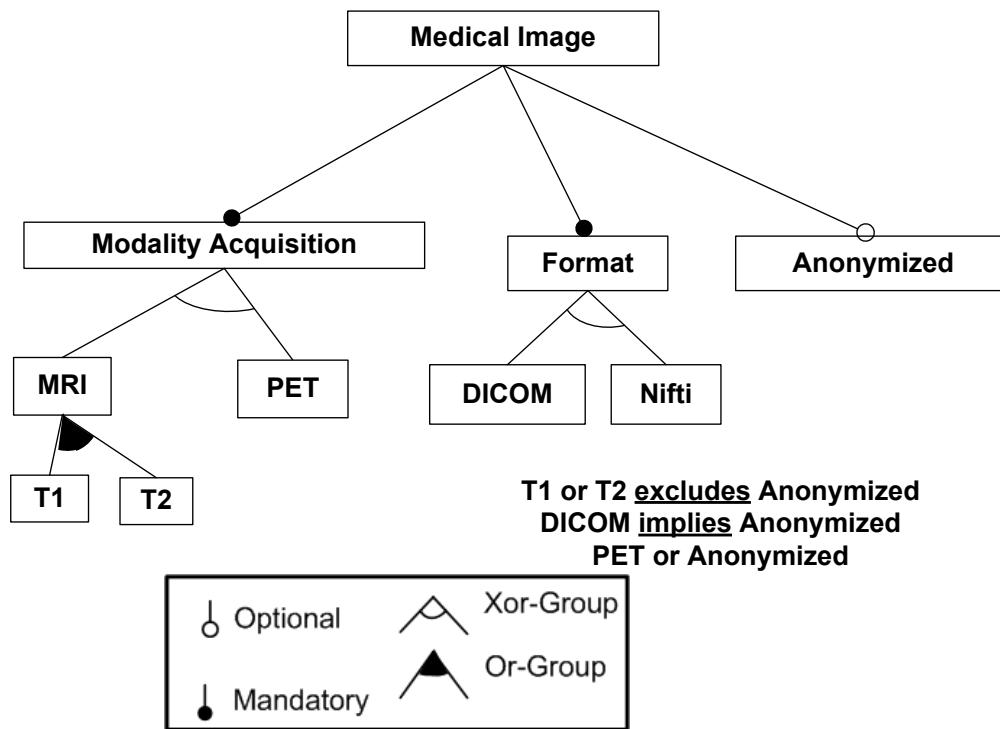


Feature Models

Hierarchy: rooted tree

Variability:

- mandatory,
- optional,
- Groups: exclusive or inclusive features
- Cross-tree constraints

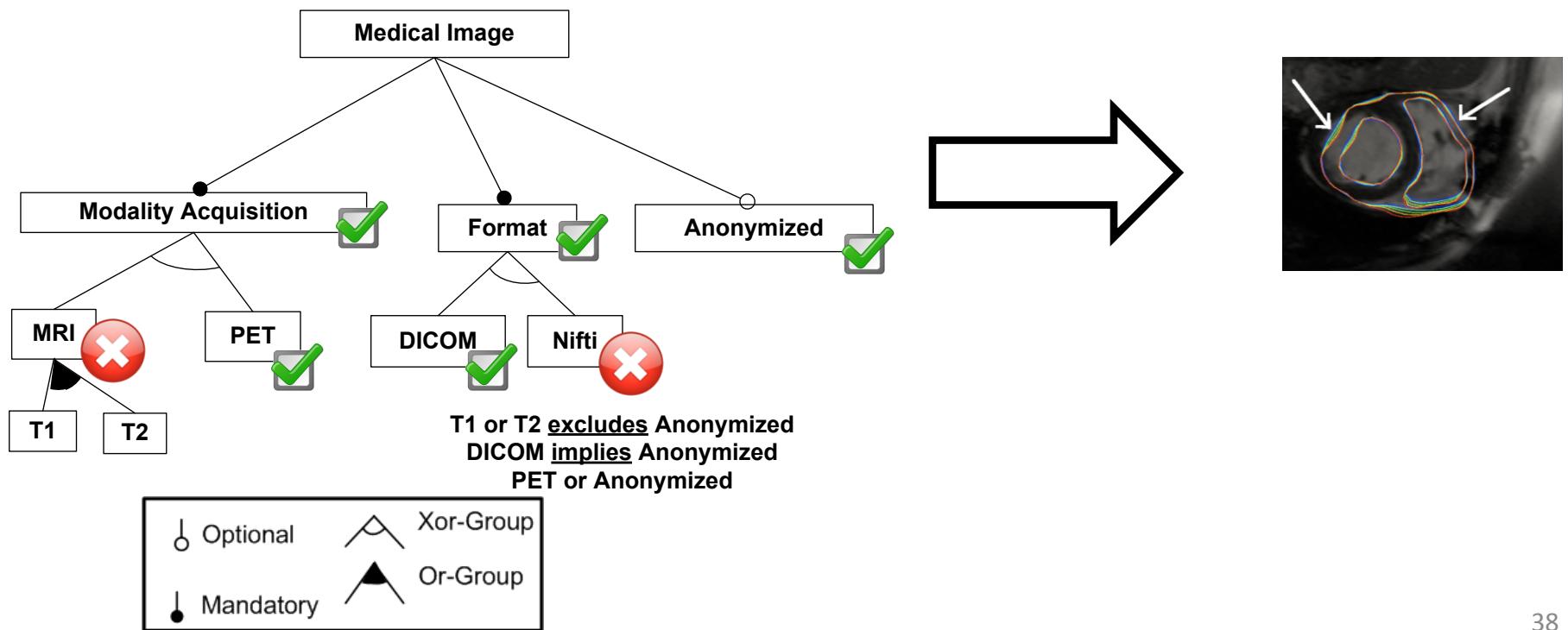


Feature Models

Semantics

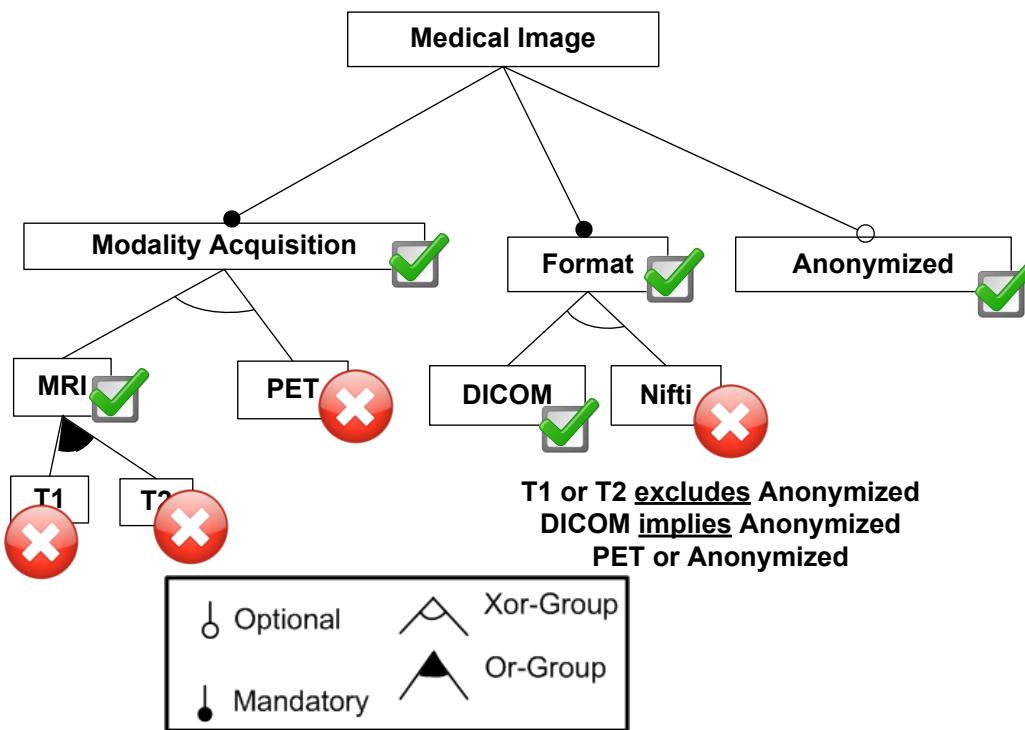
Feature Models

Hierarchy + Variability = set of valid configurations



Feature Models

Hierarchy + Variability = set of valid configurations

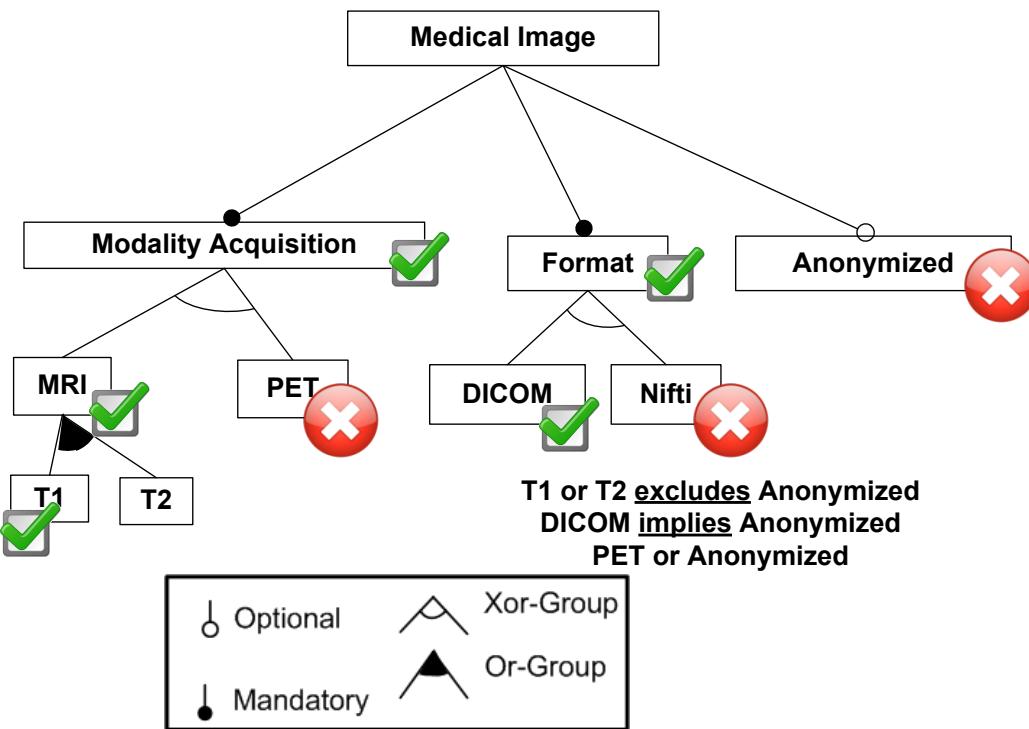


Illegal configuration

T1 or T2 excludes Anonymized
DICOM implies Anonymized
PET or Anonymized
see Or-group:
at least one feature should be selected

Feature Models

Hierarchy + Variability = set of valid configurations

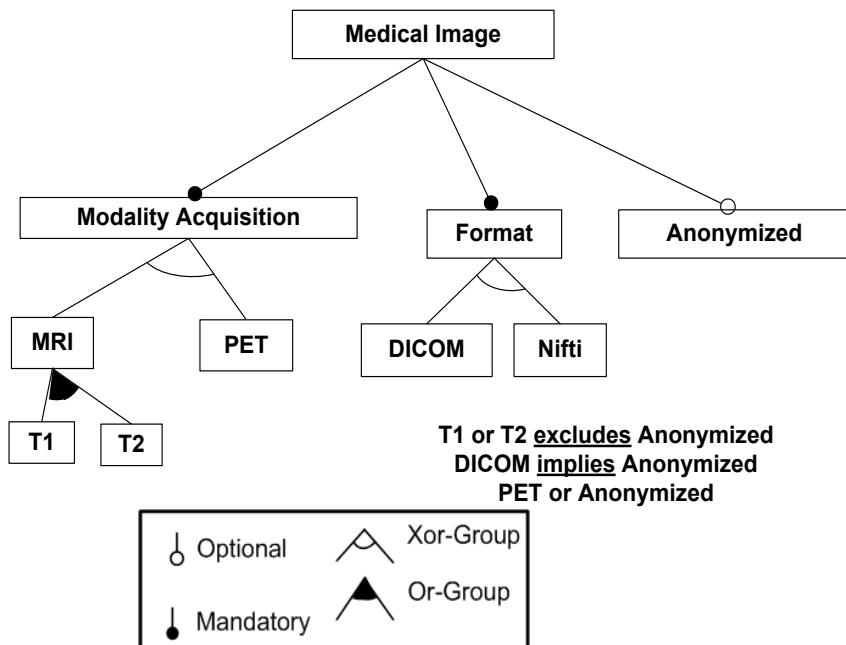


Illegal configuration

see constraint “PET or Anonymized”

Feature Models

Hierarchy + Variability = set of valid configurations



Satisfiability (NP-complete)

Configuration Checking

Dead features detection

...

Around 30 reasoning operations

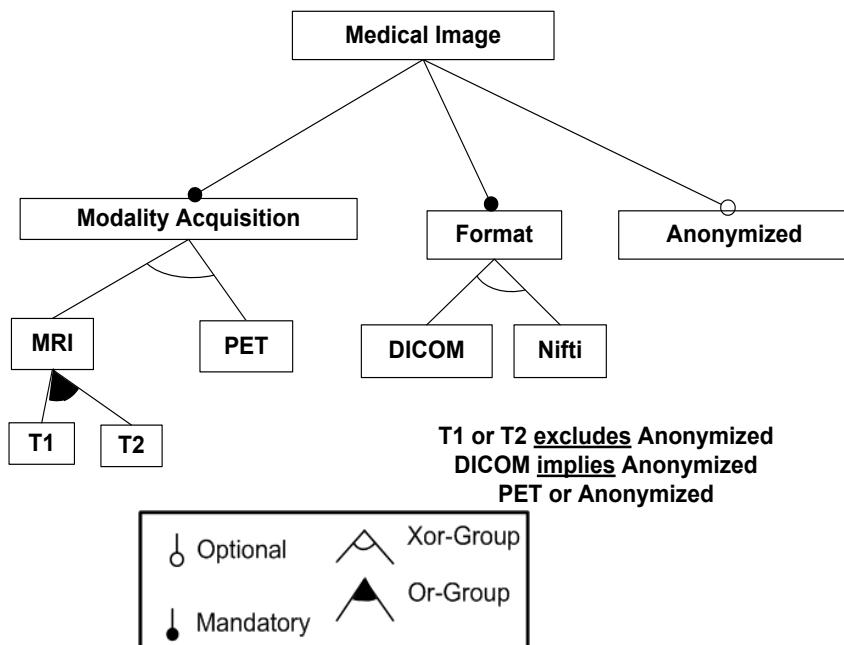
Propositional Feature Models

Hierarchy + Variability = set of valid configurations



Propositional formula ($\wedge, \vee, \sim, \Leftrightarrow, \Rightarrow$)

Boolean variables
set of valid assignments



$$\varphi = \text{Medical Image} \wedge$$

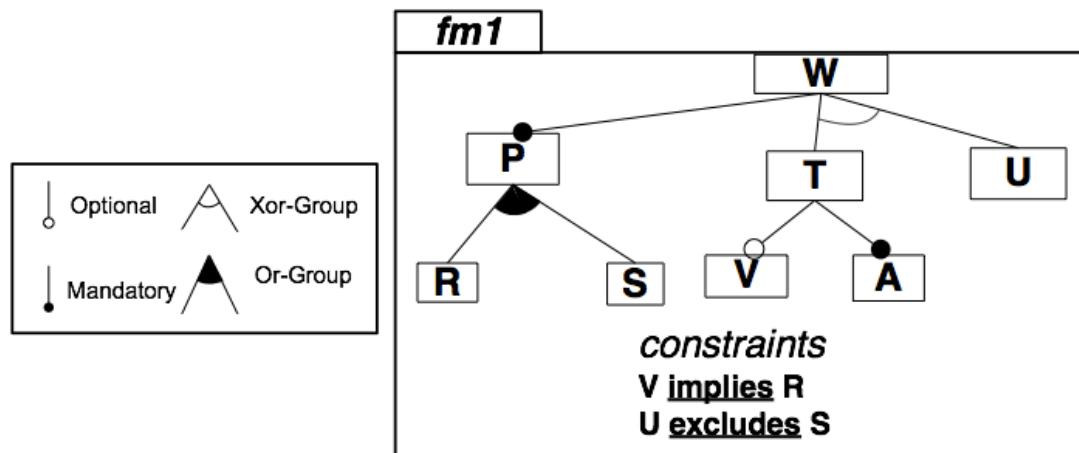
$$\text{Format} \Leftrightarrow \text{Medical Image} \wedge$$

$$\text{Anonymized} \Rightarrow \text{Medical Image} \wedge$$

...

Propositional Feature Models

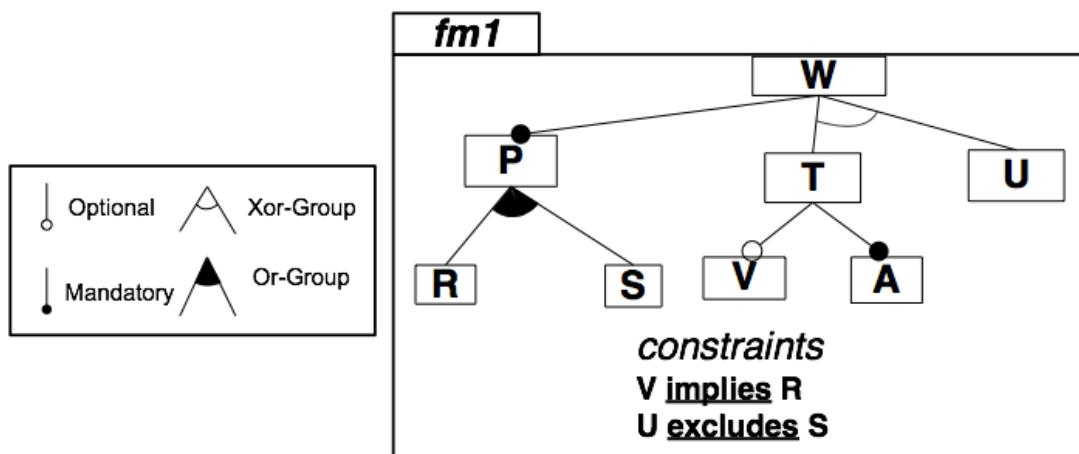
Hierarchy + Variability = set of valid configurations



$\llbracket fm1 \rrbracket = \{$
 $\{W, P, R, S, T, A, V\},$
 $\{W, P, S, T, A\},$
 $\{W, P, R, T, A\},$
 $\{W, P, R, U\},$
 $\{W, P, R, T, V, A\},$
 $\{W, P, R, S, T, A\},$
 $\}$

Propositional Feature Models

~ propositional formula



$\phi_{fm_1} = W // \text{root}$
 $\wedge W \Leftrightarrow P // \text{mandatory}$
 $// \text{Or-group}$
 $\wedge P \Rightarrow R \vee S$
 $\wedge R \Rightarrow P \wedge S \Rightarrow P$
 $\wedge V \Rightarrow T // \text{optional}$
 $\wedge A \Leftrightarrow T // \text{mandatory}$
 $// \text{Xor-group}$
 $\wedge T \Rightarrow W$
 $\wedge U \Rightarrow W$
 $\wedge \neg T \vee \neg U$
 $// \text{constraints}$
 $\wedge V \Rightarrow R // \text{implies}$
 $\wedge \neg U \Rightarrow \neg S // \text{excludes}$

★ Formal semantics which consists of

- a **formal syntax** (L) – clearcut syntactic rules defining all legal diagrams, a.k.a. syntactic domain
- a **semantic domain** (S) – a mathematical abstraction of the real-world concepts to be modelled
- a **semantic function** ($M: L \rightarrow S$) – clearcut semantic rules defining the meaning of all legal diagrams

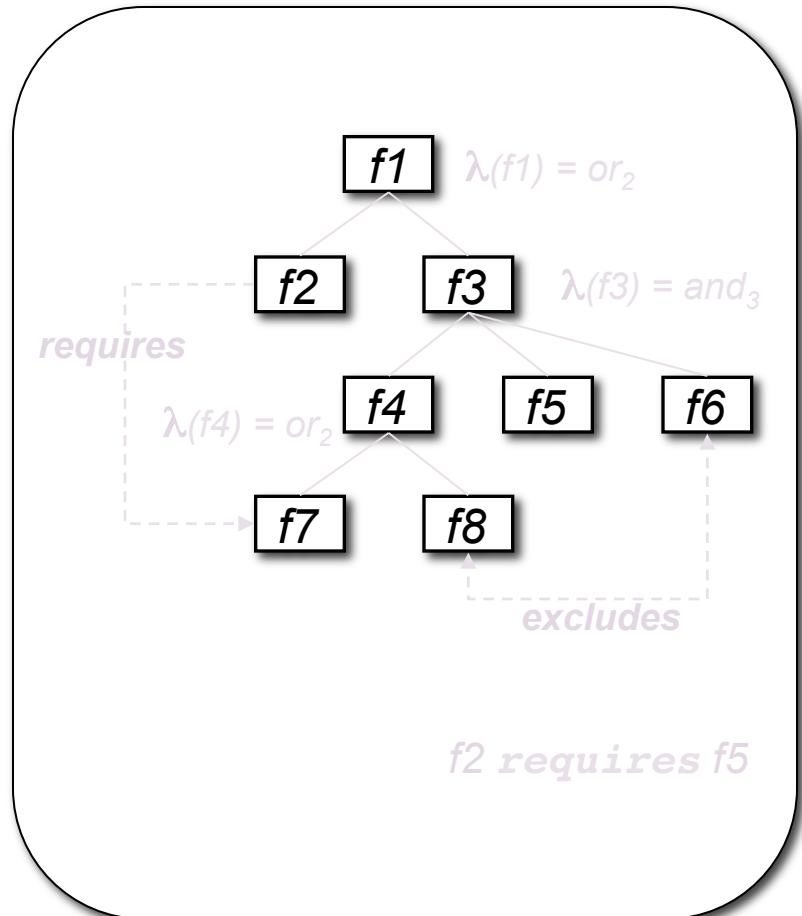
[Harel & Rumpe, IEEE Computer, 2004]



The L_{FFD} parametric abstract syntax

Every FD $d \in L_{FFD}(GT, NT, GCT, TCL)$ is a tuple $(N, r, \lambda, DE, CE, \Phi)$ such that

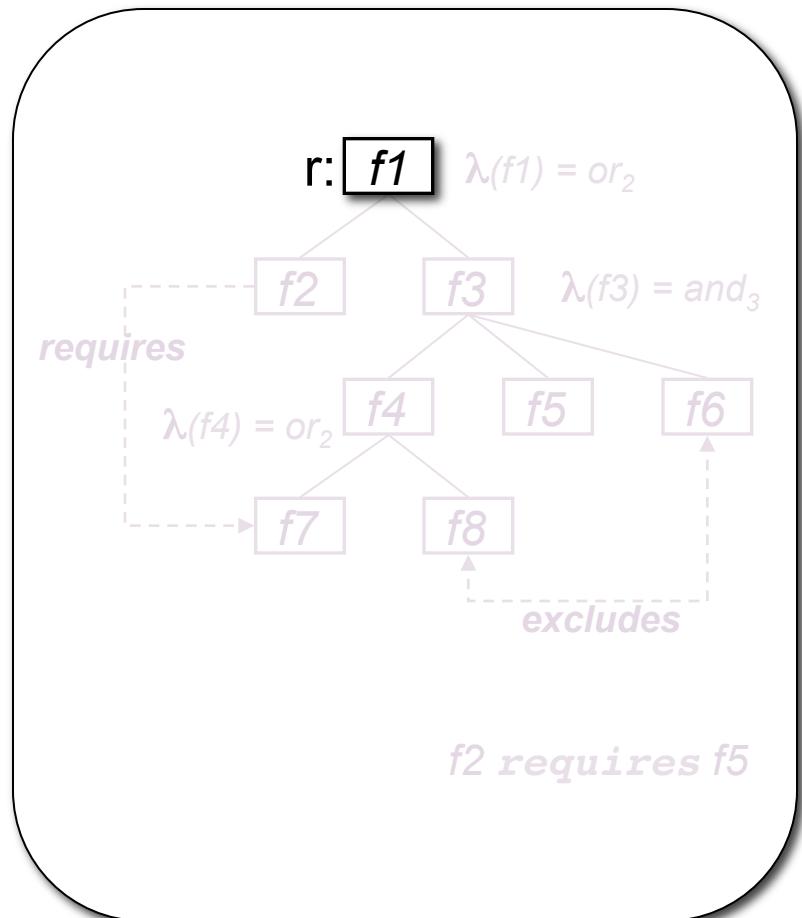
- ★ N the set of nodes
- ★ $r \in N$ the root
- ★ $DE \subseteq N \times N$ the decomposition edges (obeying GT)
- ★ $\lambda : N \rightarrow NT$ the function labelling nodes with boolean operators
- ★ $CE \subseteq N \times GCT \times N$ the constraint edges
- ★ $\Phi \subseteq TCL$ the textual constraints
- ★ + 4 well-formedness constraints
e.g. only r has no parent



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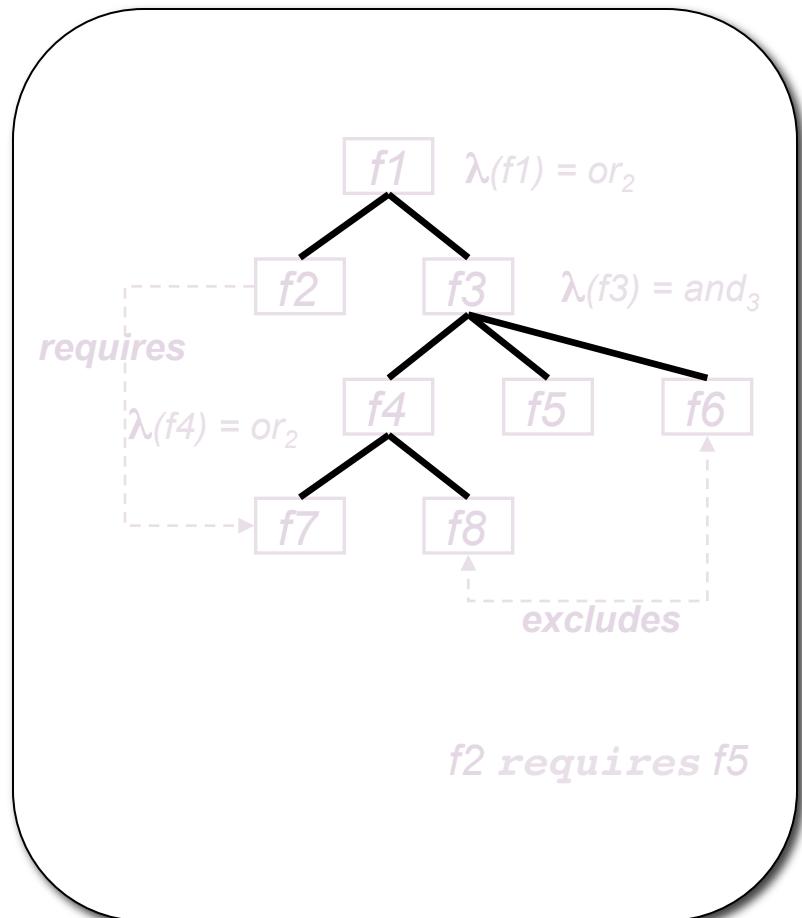


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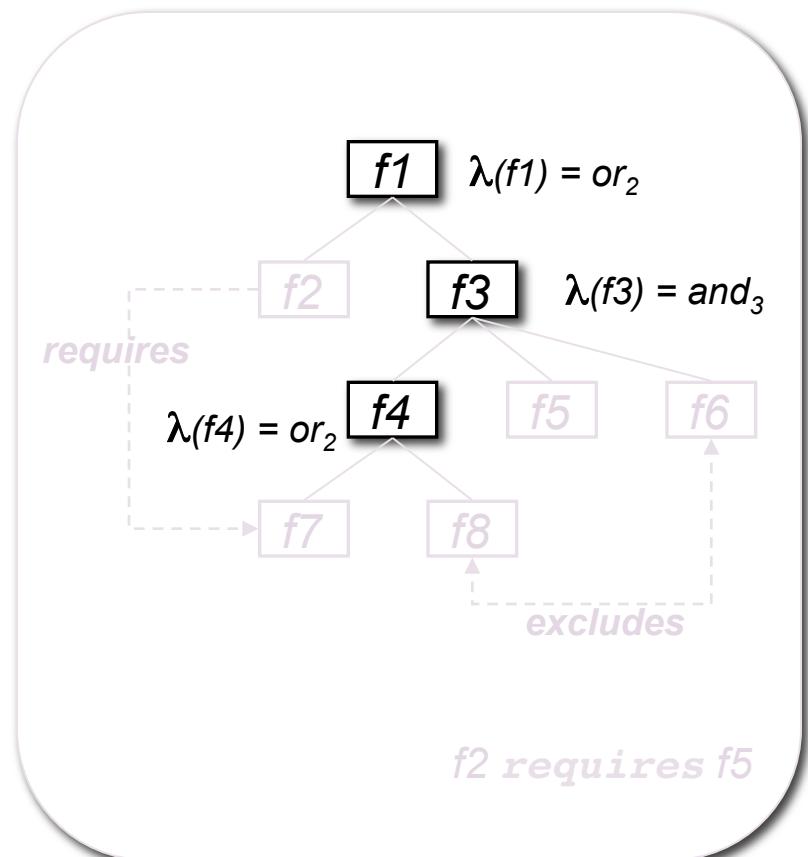
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Conventionally, for a leaf node n , $\lambda(n) = and_0$

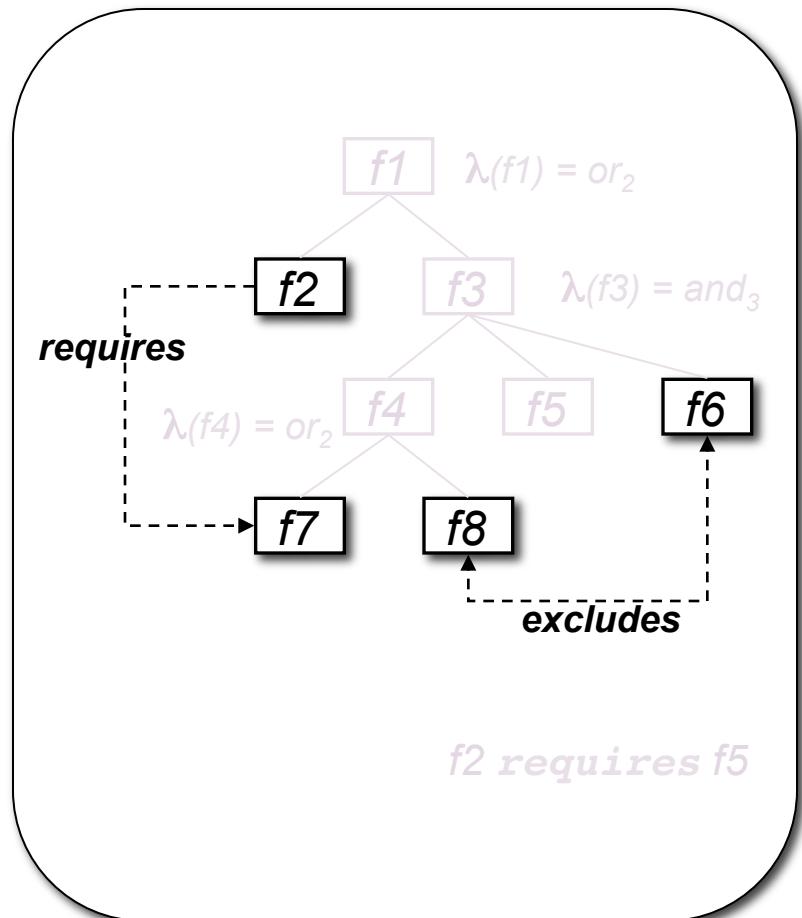


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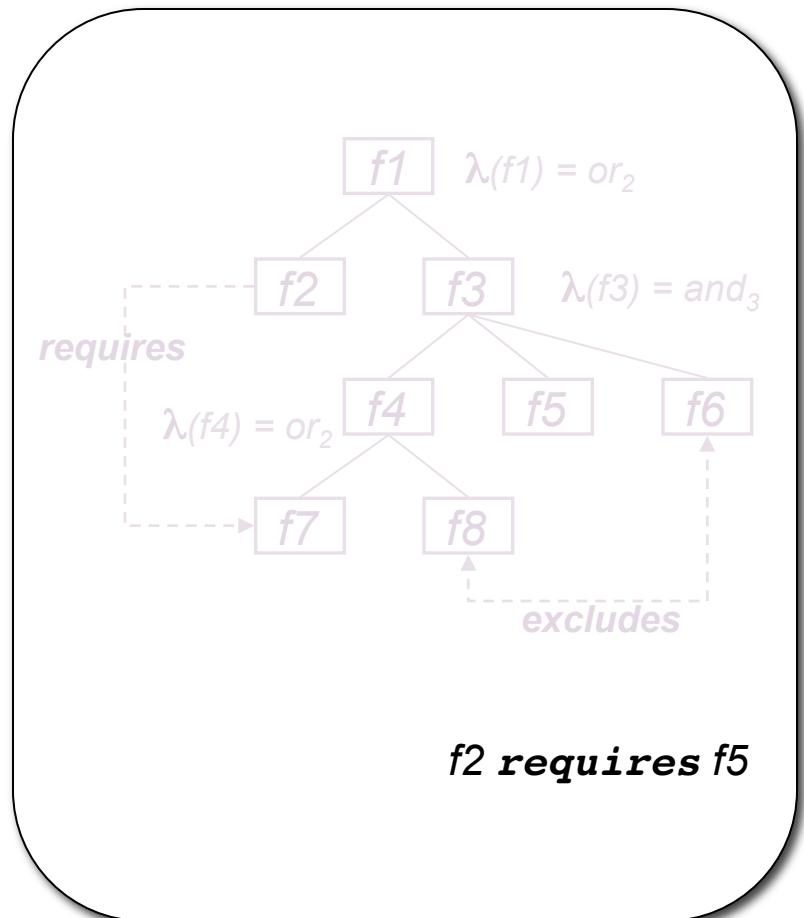
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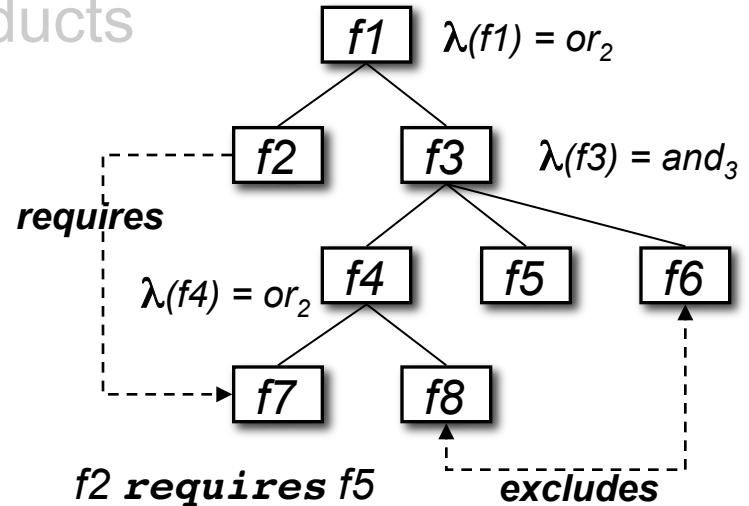
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Semantic domain

- ★ A **configuration** is a set of nodes/features
- ★ A **product** is a set of primitive nodes/features
- ★ A **product line (PL)** is a set of products



$f_1 \ f_3 \ f_4 \ f_5 \ f_6 \ f_7$

$\in \wp^N$



Semantic domain

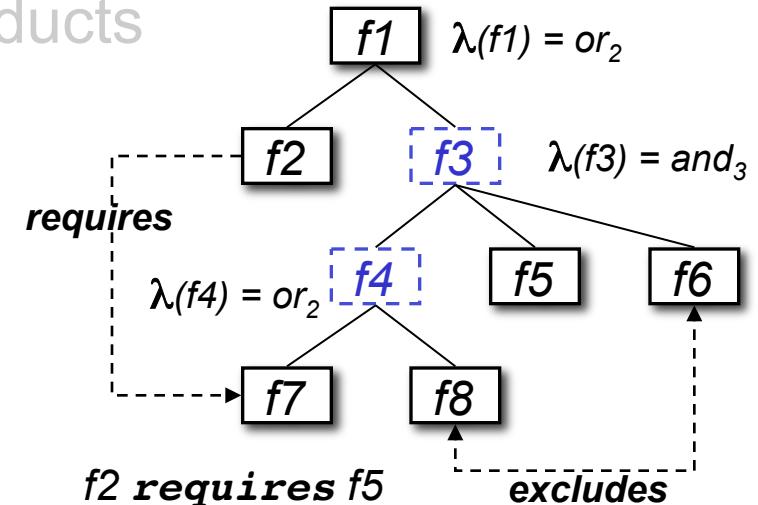
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e.g., if $f3$ and $f4$ are “irrelevant”
 (non-primitive) for the stakeholders

*remove
 non-primitive features*

$f1 \ f3 \ f4 \ f5 \ f6 \ f7$

$\in \wp N$



$f1 \ f5 \ f6 \ f7$

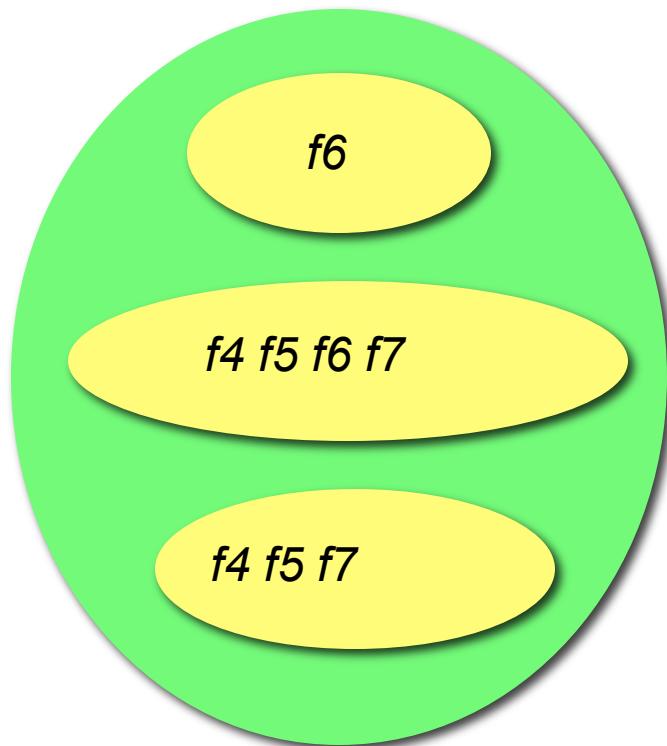
$\in \wp P$

where $P \subseteq N$



Semantic domain

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$$\in S_{FFD} = PL = \wp \wp P$$



Feature Models

#2 Language?
to elaborate/build variability models

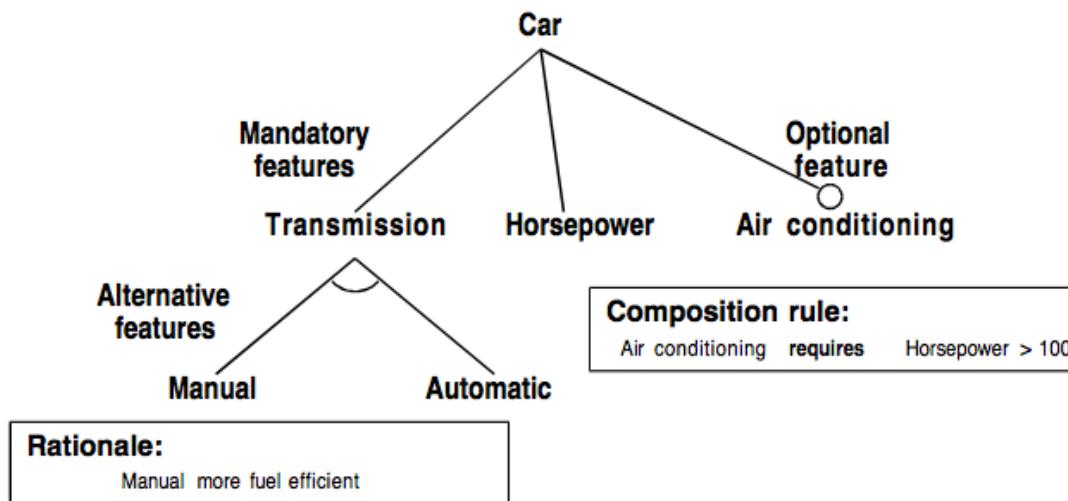
Feature Models
existing languages
vs
TVL language

Feature Models

~ Feature Diagrams (suggest a visual representation)

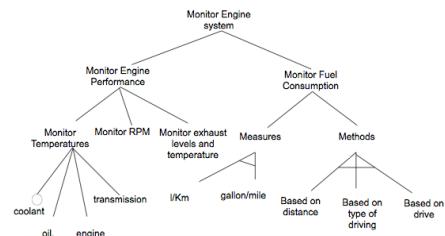
graphical languages

Feature Models

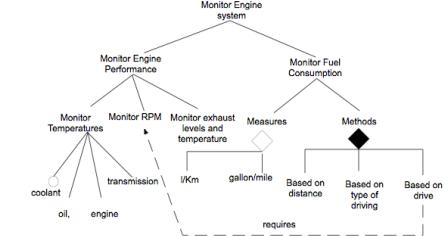


Kang et al. (1990)

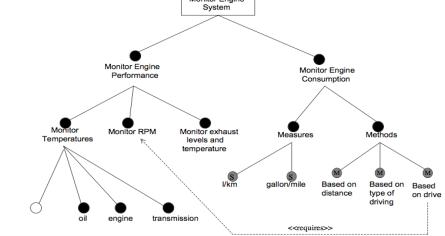
FD dialects



FODA (OFT)
[Kang et al., 1990]



FeatuRSEB (RFD)
[Griss et al., 1998]



PLUSS (PFT)
[Eriksson et al., 2005]



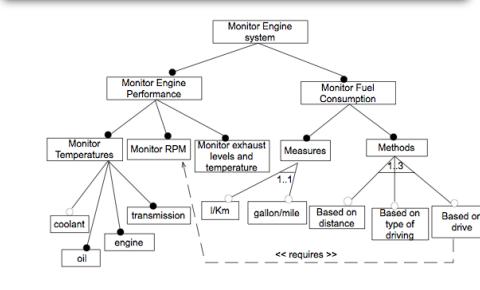
FORM (OFD)
[Kang et al., 1998]



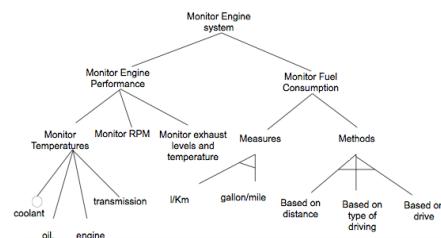
Gen. Prog. (GPFT)
[Czarnecki et al., 2000]



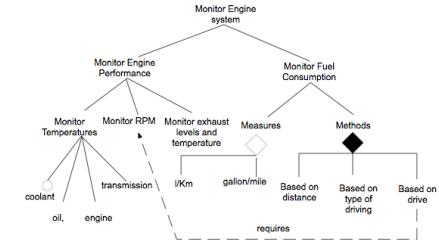
VBFD
[van Gurp et al., 2001]



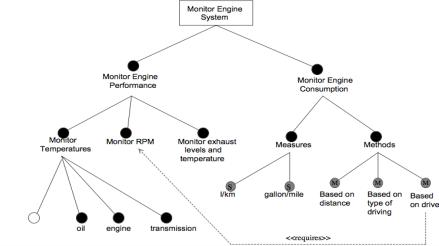
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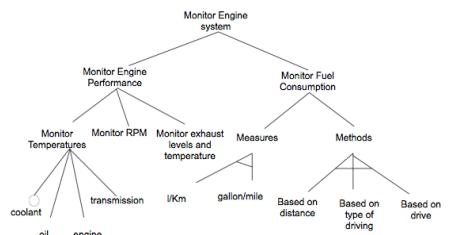
FeaturSEB (RFD)
[Griss et al., 1998]



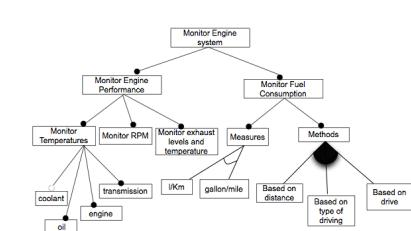
PLUSS (PFT)
[Eriksson et al., 2005]

Aesthetic differences

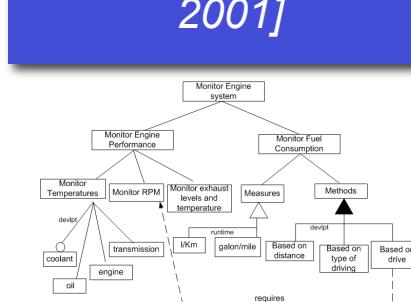
FORM (OFD)
[Kang et al., 1998]



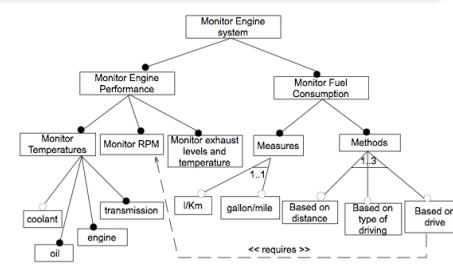
Gen. Prog. (GPFT)
[Czarnecki et al., 2000]



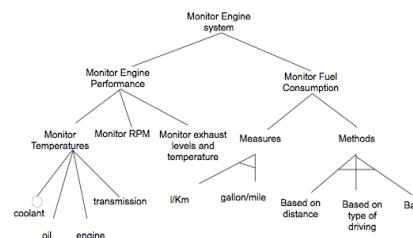
VBFD
[van Gurp et al., 2001]



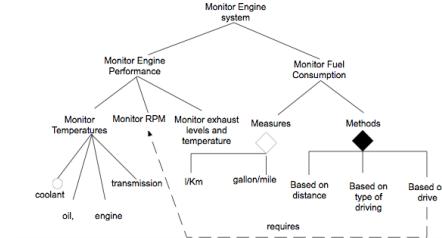
EFD
[Riebisch et al., 2002]



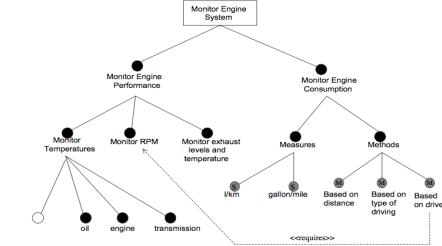
FD dialects



FODA (OFT)
[Kang et al., 1990]



FeatuRSEB (RFD)
[Griss et al., 1998]



PLUSS (PFT)
[Eriksson et al., 2005]

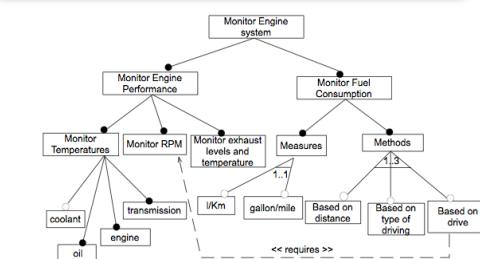
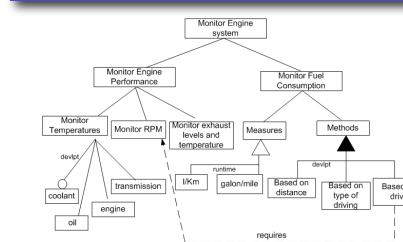
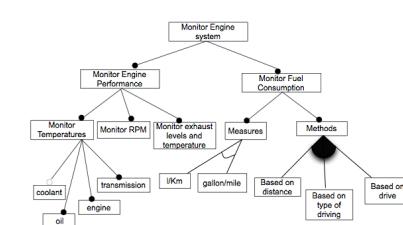
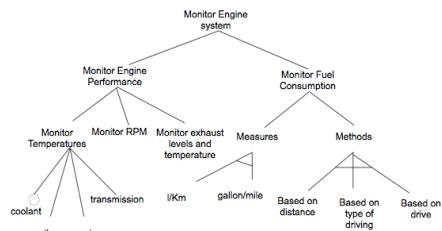
Aesthetic differences

FORM (OFD)
[Kang et al., 1998]

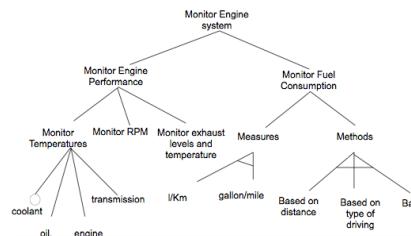
Gen. Prog. (GPFT)
[Czarnecki et al., 2000]

VBFD
[van Gurp et al., 2001]

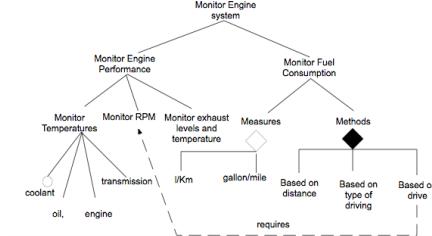
EFD
[Riebisch et al., 2002]



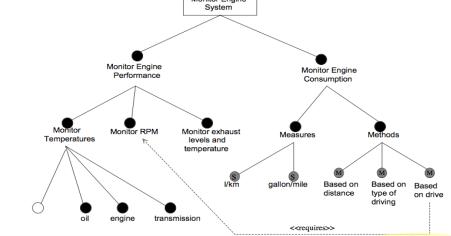
FD dialects



FODA (OFT)
[Kang et al., 1990]



FeatuRSEB (RFD)
[Griss et al., 1998]



PLUSS (PFT)
[Eriksson et al., 2001]

Aesthetic differences

Stronger claims

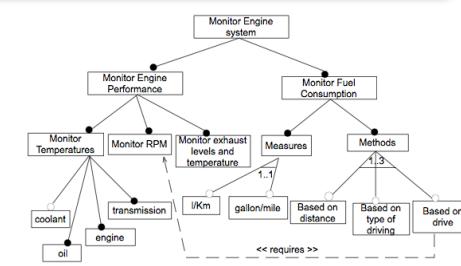
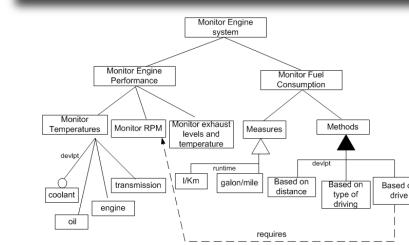
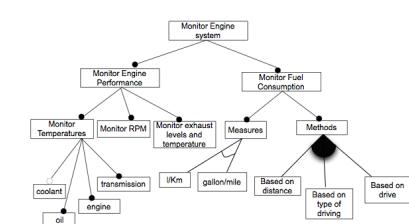
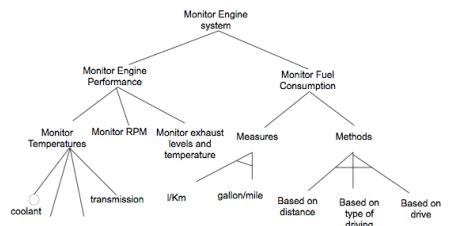
Vague use of terms
syntax, semantics,
expressiveness, ambiguity ...

FORM (OFD)
[Kang et al., 1998]

Gen. Prog. (GPFT)
[Czarnecki et al., 2000]

VBFD
[van Gurp et al., 2001]

EFD
[Riebisch et al., 2002]



★ Formal definition of FODA

[Bontemps et al., ICFI, 2004]

★ General formal definition of FD dialects

[Schobbens et al., RE, 2006]

★ Survey according to formal criteria

- Ambiguity – expressiveness – embeddability – succinctness
- Computational complexity of decision problems

[Schobbens et al., J. Computer Networks, 2007]



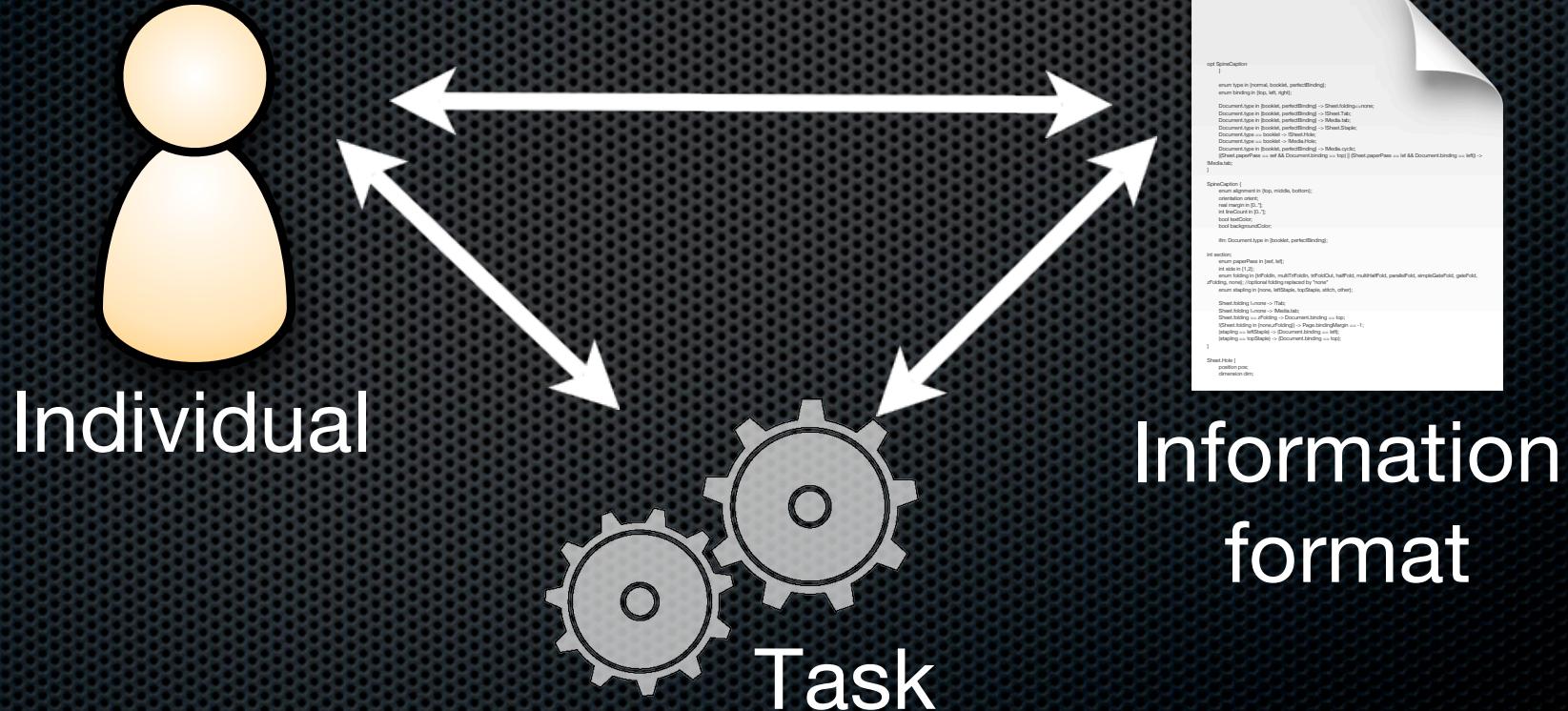
Feature Models

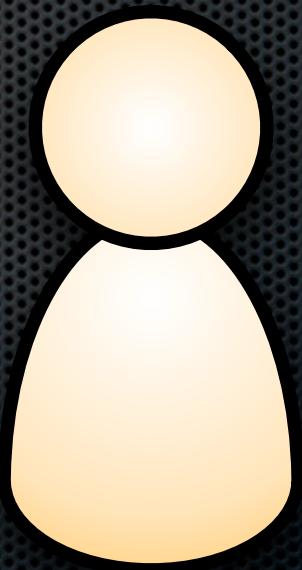
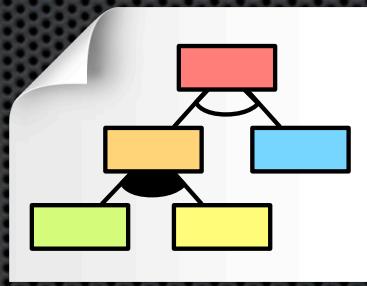
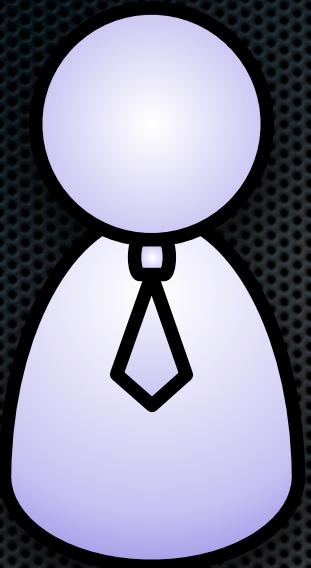
~ Feature Diagrams (suggest a visual representation)

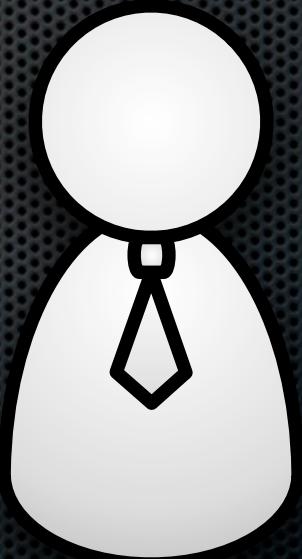
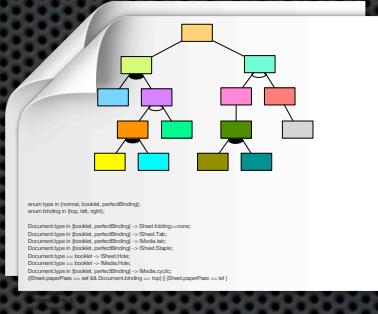
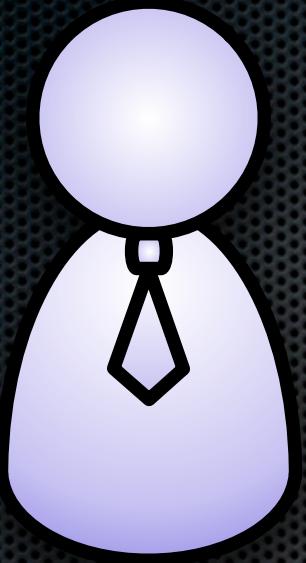
but that's not mandatory!

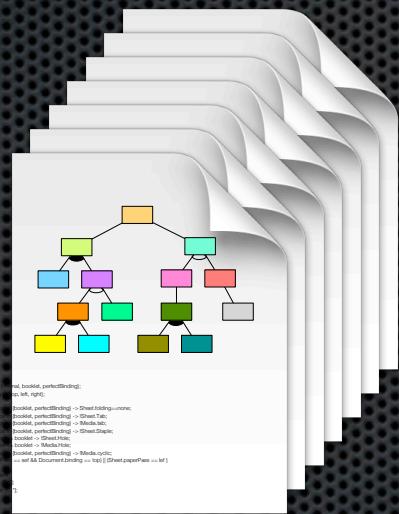
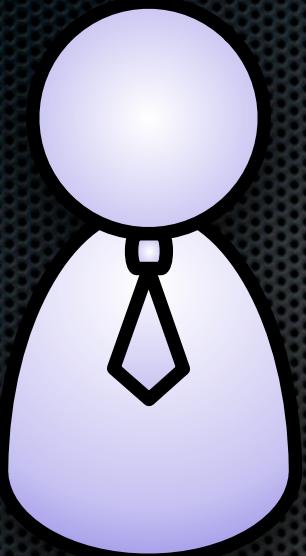
Cognitive fit theory

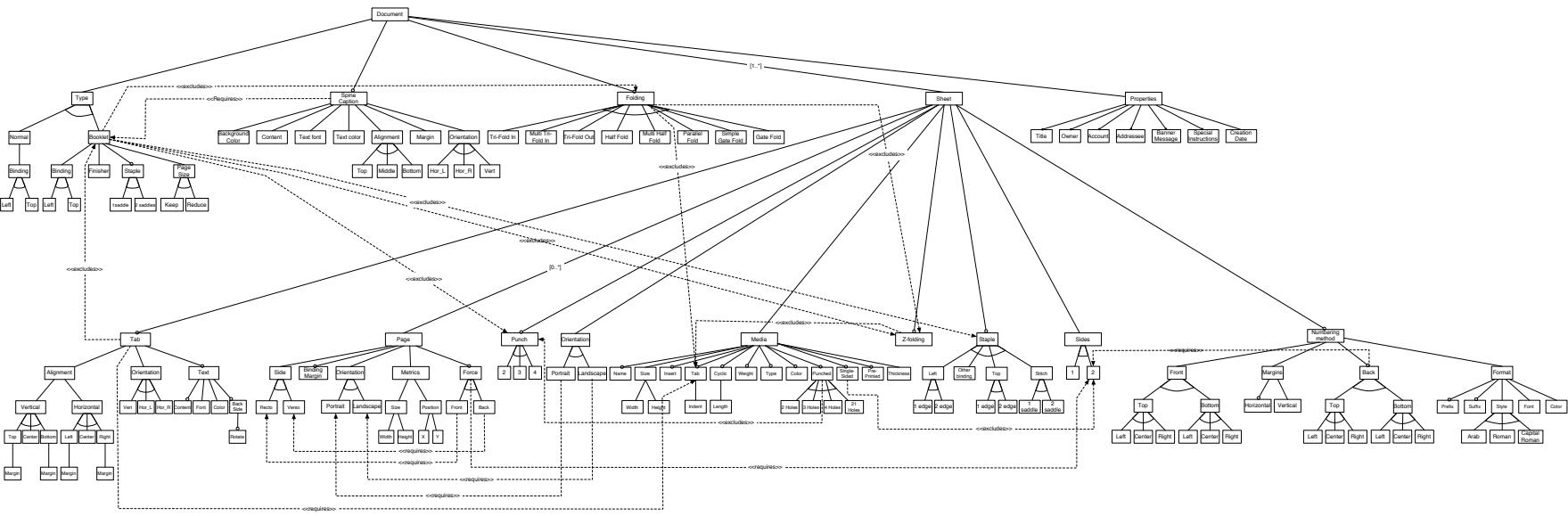
[Vessey1991]











Visualisation will help, right?

- ★ Tools create their own problems
- ★ Lock-in, dependence
- ★ Some information is inevitably textual
 - ▶ OCL constraints in UML
 - ▶ Minispecs in StateCharts
 - ▶ Attributes and constraints in FDs

Why not use text altogether?

- ★ There are powerful tools for that
- ★ Helps with
 - ▶ editing,
 - ▶ transformation,
 - ▶ versioning,
 - ▶ information exchange,
 - ▶ ...
- ★ Graphical views can be generated anyway

+
CML,
+ VSL

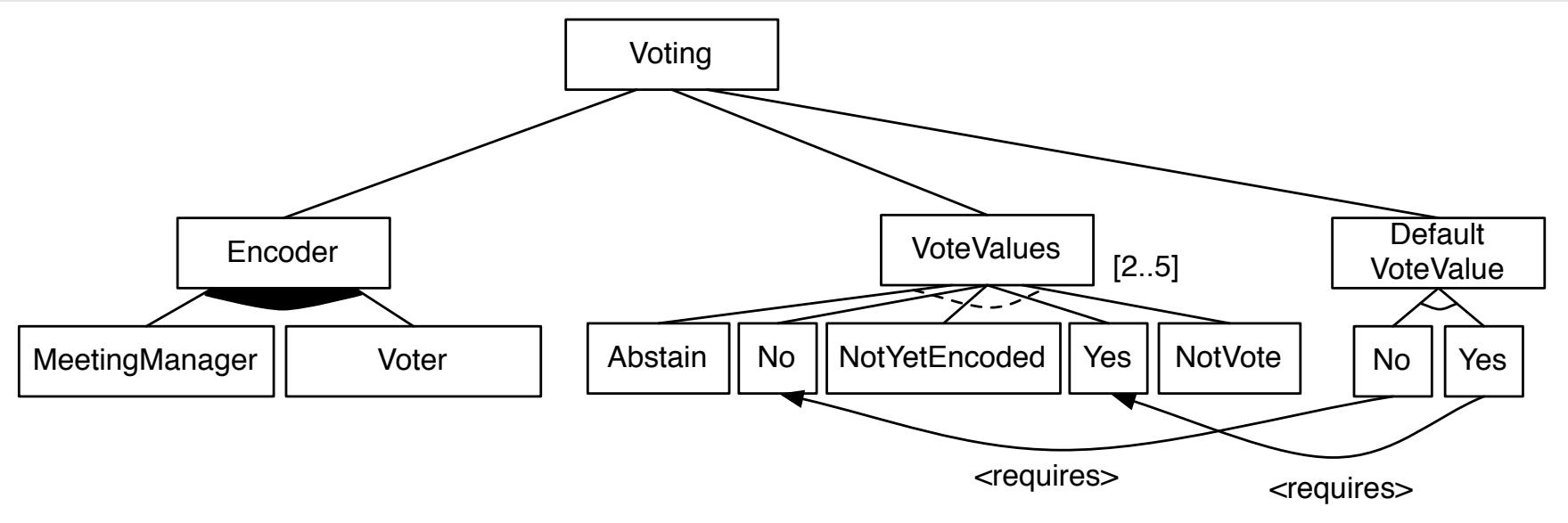
Not a new idea

Language	Userfriendly	Attributes	Cardinalities	Cross-tree constraints	Modularisation
Van Deursen et al.					
GUIDSL (AHEAD)					
SXFM					
XML-based (EΔΜΔ etc.)					

Furthermore

- ★ Intuitive C-like syntax
- ★ Formal semantics
- ★ Statically typed
- ★ Implemented

Graphical feature models



Legend



And-decomposition



Or-decomposition

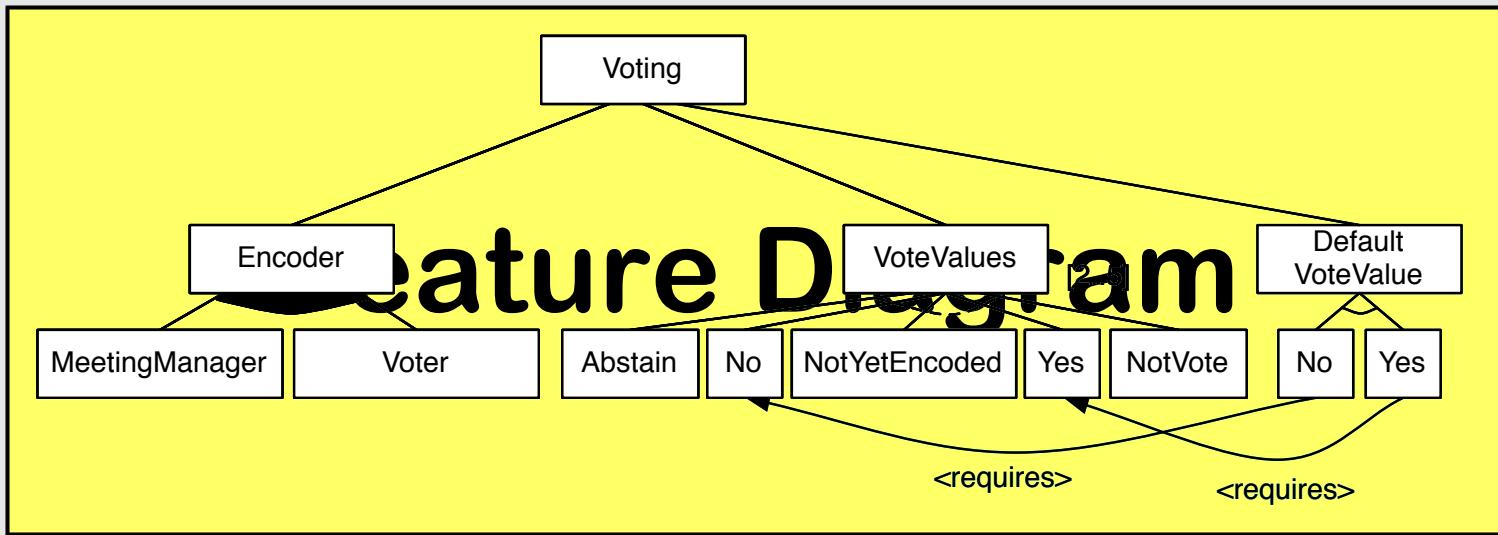


Xor-decomposition



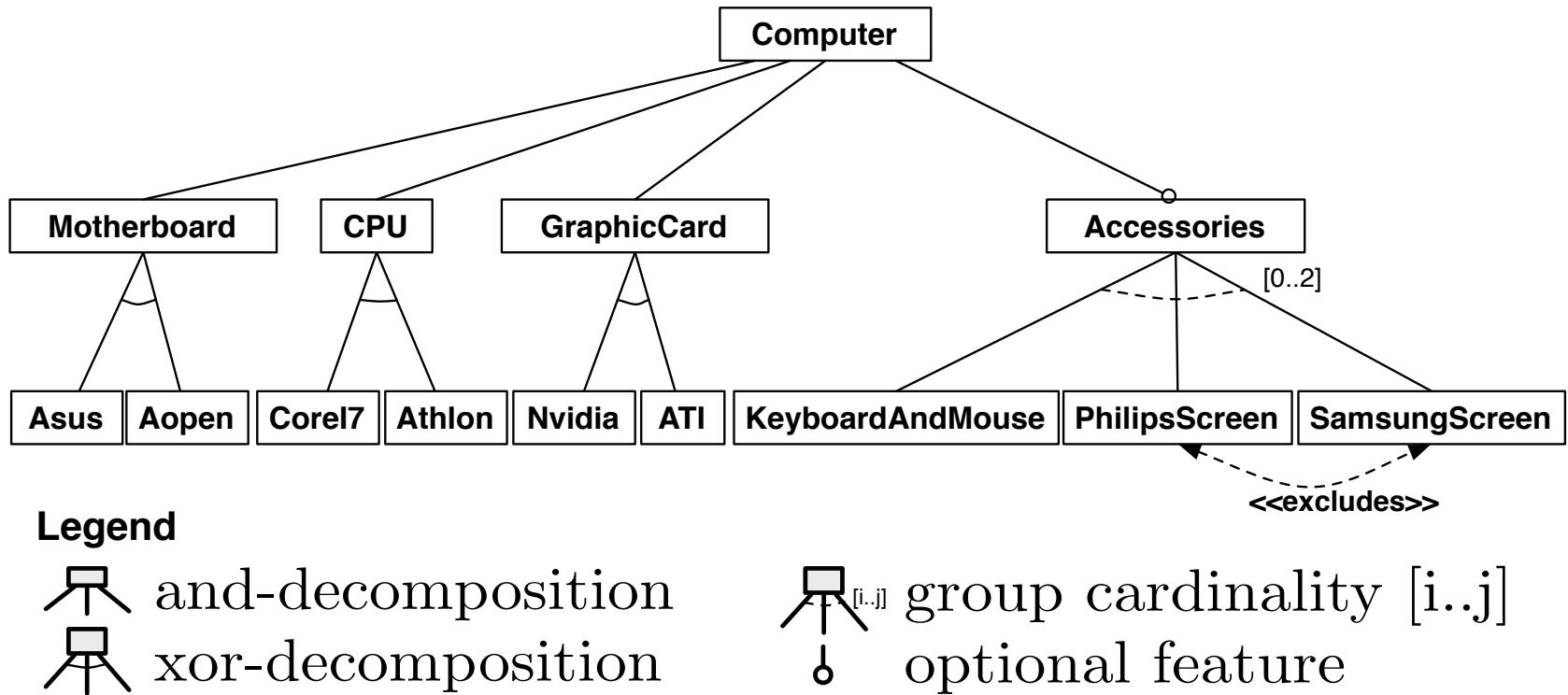
[i..j] group cardinality

TVL: Basic principles

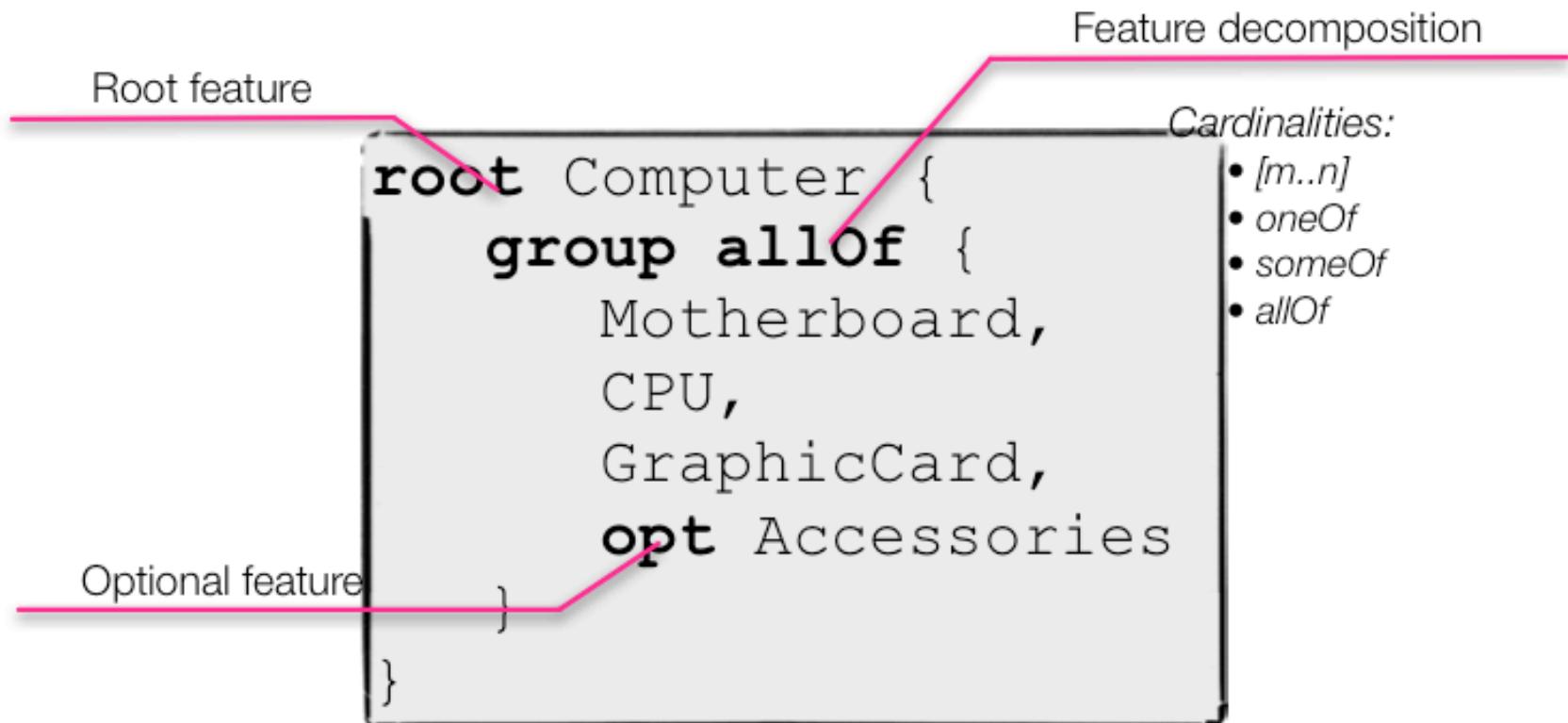


```
root Voting {  
    enum defaultVoteValue in {no, yes};  
    (defaultVoteValue==no) -> No;  
    (defaultVoteValue==yes) -> Yes;  
    group allOf {  
        Encoder {group someOf {MeetingManager, Voter}},  
        VoteValues group [2..*] {  
            Abstain,  
            No,  
            NotYetEncoded,  
            Yes,  
            NotVote  
        }  
    }  
}
```

Example FD



TVL – Feature hierarchy



TVL – Feature hierarchy (DAG)

```
root A {  
    group oneOf {  
        B group allOf { D },  
        C group allOf { shared D }  
    }  
}
```

Shared feature

TVL – Attributes (declaration)

4 attribute types

- *integer*
- *rational*
- *Boolean*
- *enumeration*

```
Computer {
    int price in [0..500];
    int width;
    int height;
    enum socket in {LGA1156, ASB1};
}
```

Domain interval

Domain enumeration

TVL – Attributes (assignment)

```
Accessories {  
    int price is sum (selectedChildren.price);  
    group [0..2] {  
        KeyboardAndMouse {  
            int price is 19;  
        }  
        SamsungScreen {  
            int price, ifIn: is 149, ifOut: is 0;  
        }  
    }  
}
```

Computed value

Fixed value

Guarded value

TVL – Constraints

```
Motherboard {  
    enum socket in {LGA1156, ASB1};  
    group oneOf {  
        Asus {  
            ifIn: parent.socket==LGA1156;  
        }  
        Aopen {  
            ifIn: parent.socket==ASB1;  
        }  
    }  
}
```

Attached to features

Guarded

TVL – Modularity

★ In the header

- ▶ Custom types definition for the entire feature model

```
enum cpuSocket {LGA1156, ASB1};
```

- ▶ Structured types definitition

```
struct motherBoardSize {  
    int height;  
    int width;  
}
```

- ▶ Constants definition

```
const int maxRamBlocks 4;
```

TVL – Modularity

★ In the feature model

- ▶ Other TVL files can be included

```
include (./some/other/file);
```

- ▶ Features can be extented anywhere in the code

```
root Computer {
    group allof {
        Motherboard,
        CPU,
        GraphicCard,
        opt Accessories
    }
}
Computer {
    int price is sum(selectedChildren.price);
    ...
}
Motherboard {
    motherBoardSize size;
    ...
}
```

More info online

- ★ Parser API (JAVA)
- ★ TVL to Boolean CNF implementation
- ★ Formal grammar
- ★ Formal semantics

www.info.fundp.ac.be/~acs/tvl

Feature Models

#3 Reasoning techniques?

to analyze properties of an SPL
(scalable and automated way)

Feature Models

and

Automated Reasoning

Decision problems and complexity

★ **Satisfiability:** $M_{FFD}(d) \neq \emptyset$

- NP-complete
- linear for many tree-shaped graphs

★ **Product checking:** $\{f_1, \dots, f_n\} \in M_{FFD}(d)$

- NP-complete
- linear for many tree-shaped graphs, or if $P = N$

★ **Equivalence:** $M_{FFD}(d_1) = M_{FFD}(d_2)$

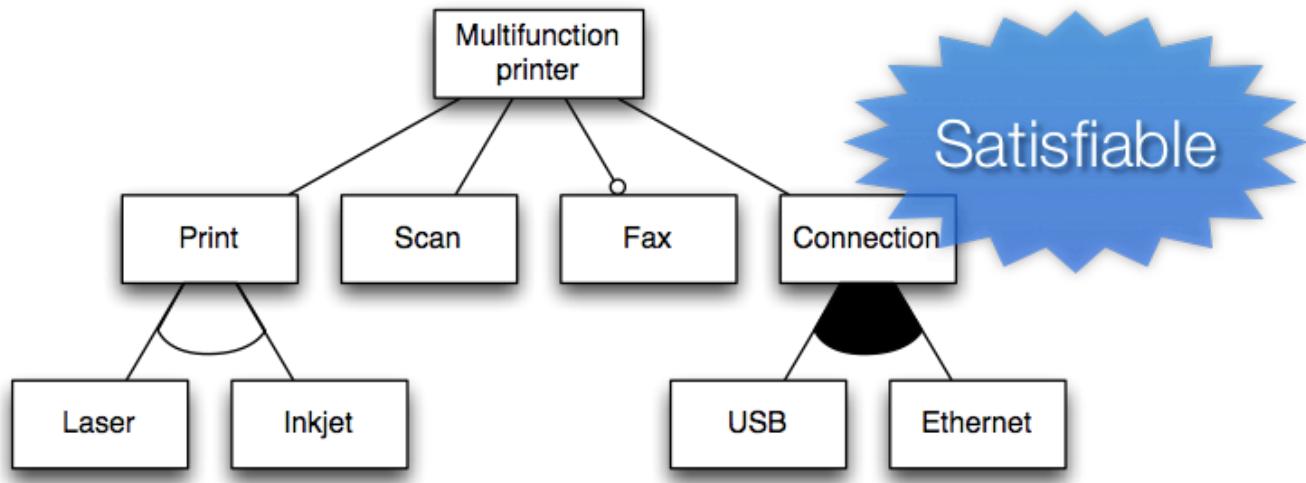
- Π_1 -complete

- ★ **Tractability issues** but **efficient algorithms** in many common cases
- ★ Existing **algorithms** can now be proved for **correctness** and **optimality**

[Schobbens et al., J. Computer Networks, 2007] 87



Satisfiability – example

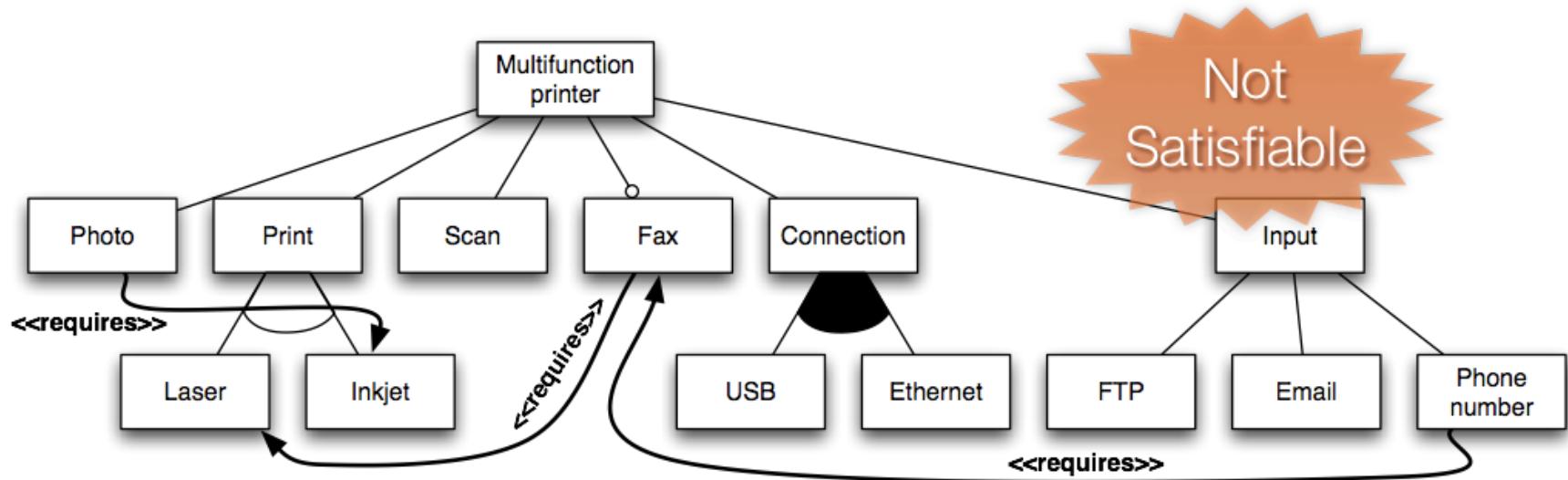


{ {MP, Print, Laser, Scan, Fax, Connection, USB, Ethernet},
 {MP, Print, Laser, Scan, Fax, Connection, Ethernet},
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 {MP, Print, Laser, Scan, Connection, USB, Ethernet},
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 {MP, Print, Inkjet, Scan, Fax, Connection, USB, Ethernet},
 {MP, Print, Inkjet, Scan, Fax, Connection, Ethernet},
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 {MP, Print, Inkjet, Scan, Connection, USB, Ethernet},
 {MP, Print, Inkjet, Scan, Connection, Ethernet},
 {MP, Print, Inkjet, Scan, Connection, USB} }

↔ Not empty



Satisfiability – example



More decision problems

- ★ **Dead features:** $P \setminus \cup M_{FFD}(d)$
- ★ **Commonality:** $\cap M_{FFD}(d)$
- ★ **Variability:** $P \setminus \cap M_{FFD}(d)$
- ★ **“Merging” FDs:** construct d_3 from d_1 and d_2 s.t.

➤ **Intersection:** $M_{FFD}(d_3) = M_{FFD}(d_1) \cap M_{FFD}(d_2)$

➤ **Union:** $M_{FFD}(d_3) = M_{FFD}(d_1) \cup M_{FFD}(d_2)$

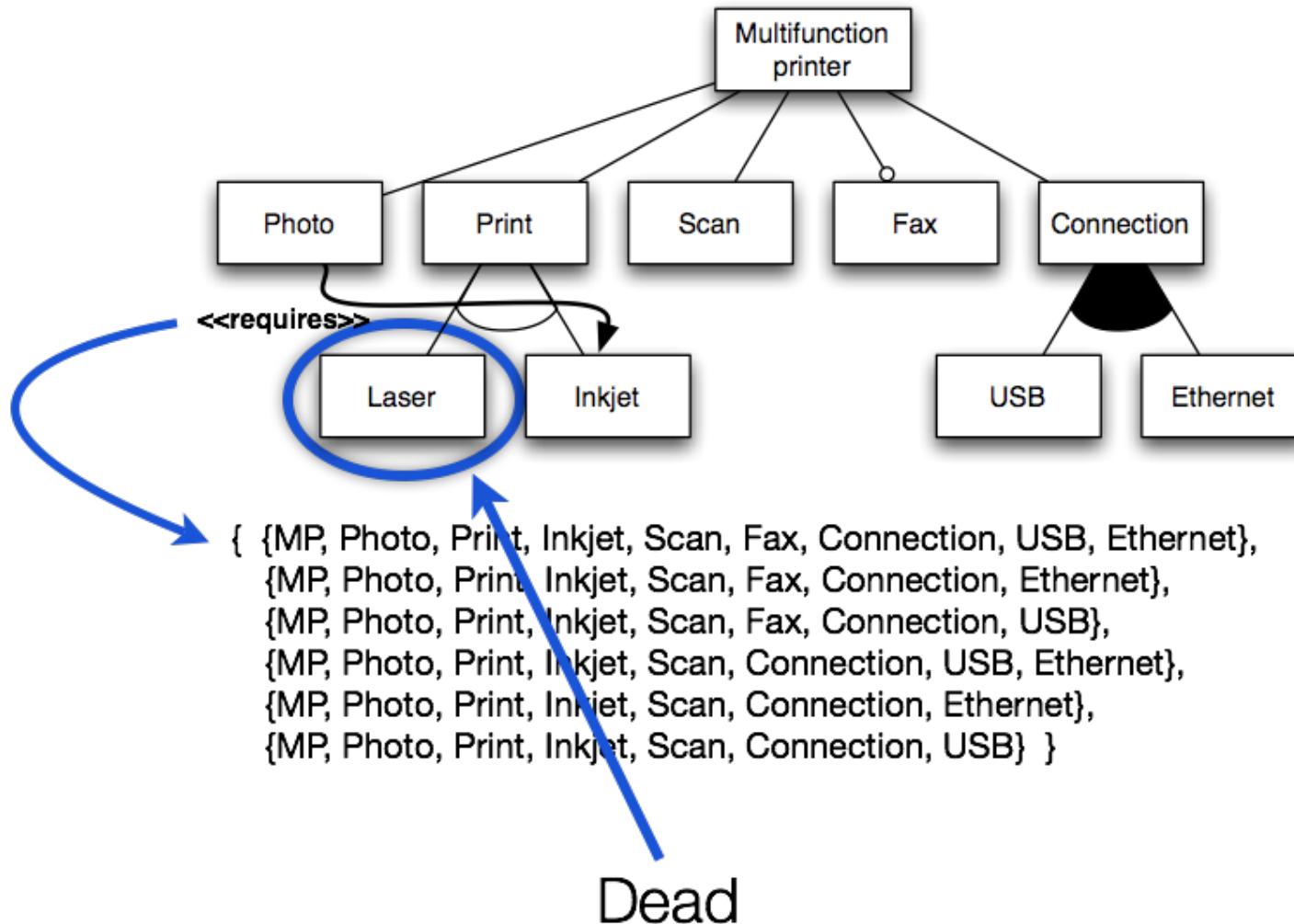
➤ **Reduced product:**

$$M_{FFD}(d_3) = \{p_1 \cup p_2 \mid p_1 \in M_{FFD}(d_1) \wedge p_2 \in M_{FFD}(d_2)\}$$

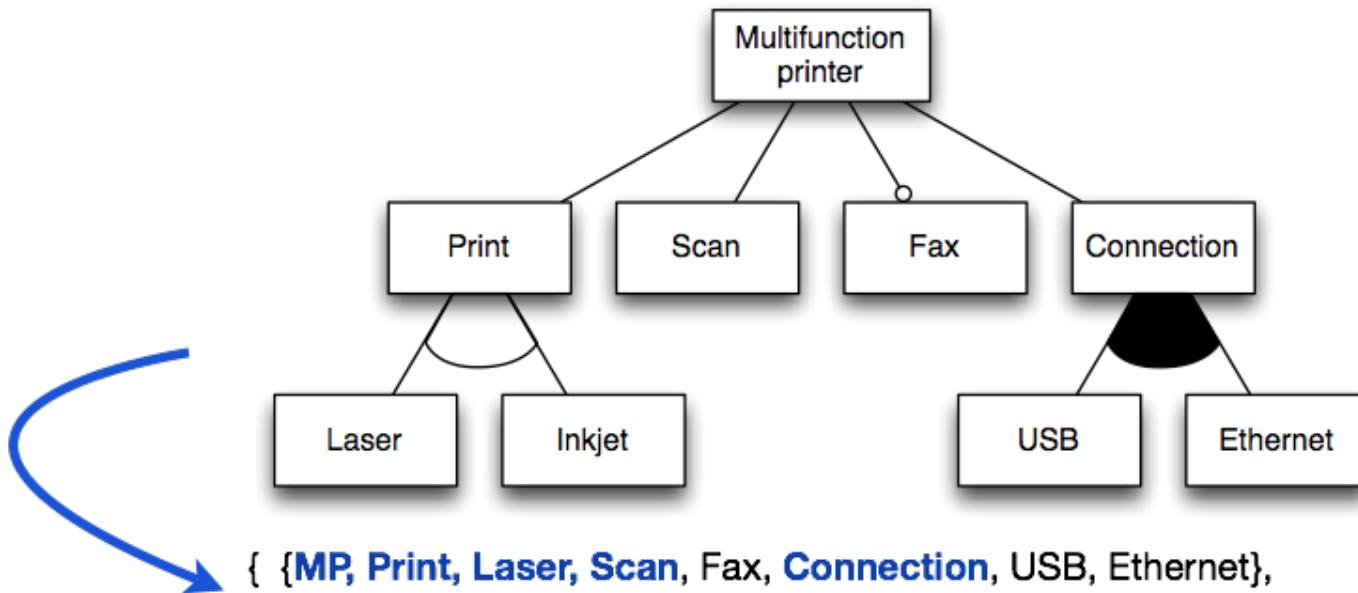
[*Schobbens et al., J. Computer Networks, 2007*] 90

- ★ etc... [*Benavides et al., JISBD, 2006*]

Dead feature – example



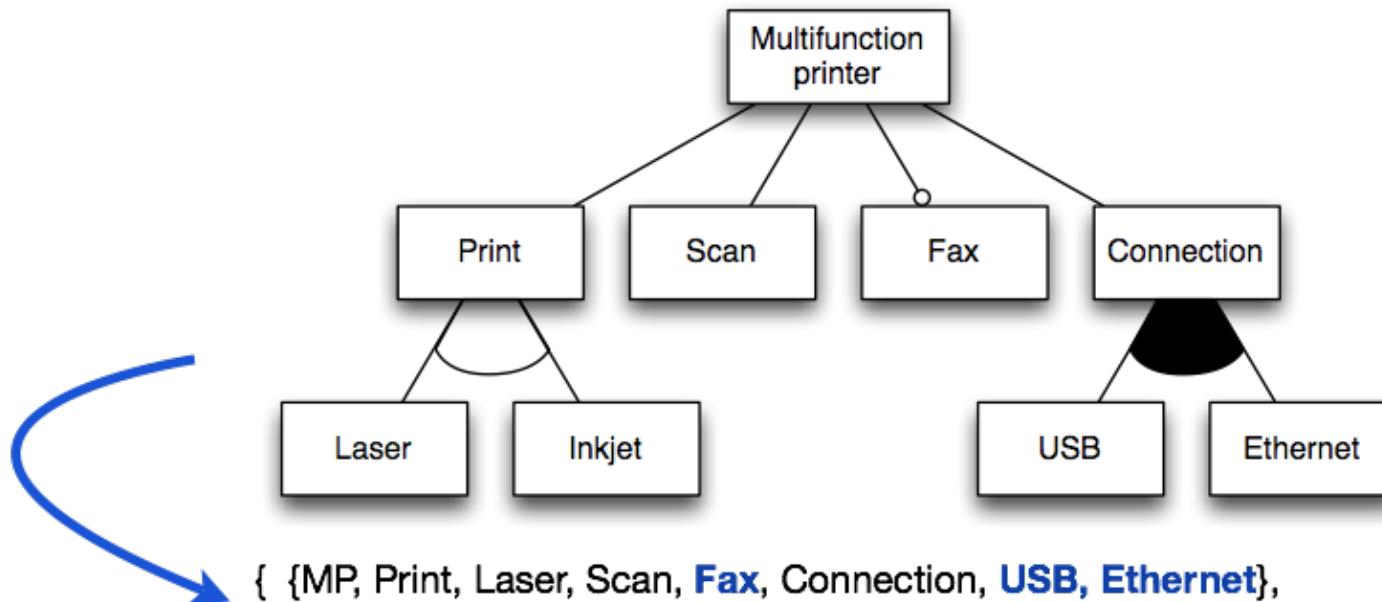
Commonality – example



{ {MP, Print, Laser, Scan, Fax, Connection, USB, Ethernet},
 {MP, Print, Laser, Scan, Fax, Connection, Ethernet},
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 {MP, Print, Laser, Scan, Connection, USB, Ethernet},
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 {MP, Print, Laser, Scan, Connection, Ethernet},
 {MP, Print, Laser, Scan, Connection, USB} }



Variability – example



{ {MP, Print, Laser, Scan, **Fax**, Connection, **USB, Ethernet**},
 {MP, Print, Laser, Scan, **Fax**, Connection, **Ethernet**},
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 {MP, Print, Laser, Scan, Connection, **USB, Ethernet**},
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 {MP, Print, Inkjet, Scan, Connection, **USB**} }



Feature Models and Automated Reasoning

Benavides et al. survey, 2010

Feature Models

SAT solvers

Binary Decision Diagrams (BDDs)

CSP solvers



Satisfiability (SAT) solver

- A “SAT solver” is a program that automatically decides whether a propositional logic formula is satisfiable.
 - If it is satisfiable, a SAT solver will produce an example of a truth assignment that satisfies the formula.
 - indispensable component of many formal verification and (more recently) program analysis applications.
- Basic idea: since all NP-complete problems are mutually reducible:
 - Write one really good solver for NP-complete problems (in fact, get lots of people to do it. Hold competitions.)
 - Translate your NP-complete problems to that problem.

SAT solver and CNF

- All current fast SAT solvers work on CNF
- Terminology:
 - A literal is a propositional variable or its negation (e.g., p or $\neg q$).
 - A clause is a disjunction of literals (e.g., $(p \vee \neg q \vee r)$). Since \vee is associative, we can represent clauses as lists of literals.
- A formula is in conjunctive normal form (CNF) if it is a conjunction of clauses e.g., $(p \vee q \vee \neg r) \wedge (\neg p \vee s \vee t \vee \neg u)$

SAT solver and Unit Propagation

- Unit propagation (or “Boolean constraint propagation”, BCP for short) is the key component to fast SAT solving.
- Whenever all the literals in a clause are false except one, the remaining literal must be true in any satisfying assignment (such a clause is called a **unit clause**).
 - Therefore, the algorithm can assign it to true immediately. After choosing a variable there are often many unit clauses.
 - Setting a literal in a unit clause often creates other unit clauses, leading to a cascade.
 - A good SAT solve often spends 80-90% of its time in unit propagation.

SAT solver and Unit Propagation

BCP():

Repeatedly search for unit clauses, and
set unassigned literal to required value.

If a literal is assigned conflicting values, return F
else return T;

satisfy(ϕ) {

if every clause of ϕ has a true literal, return T;

if BCP() == F, return F;

assign appropriate values to all pure literals;

choose an $x \in V$ that is unassigned in A ,
and choose $v \in \{T, F\}$.

$A(x) = v$;

if satisfy(ϕ) return T;

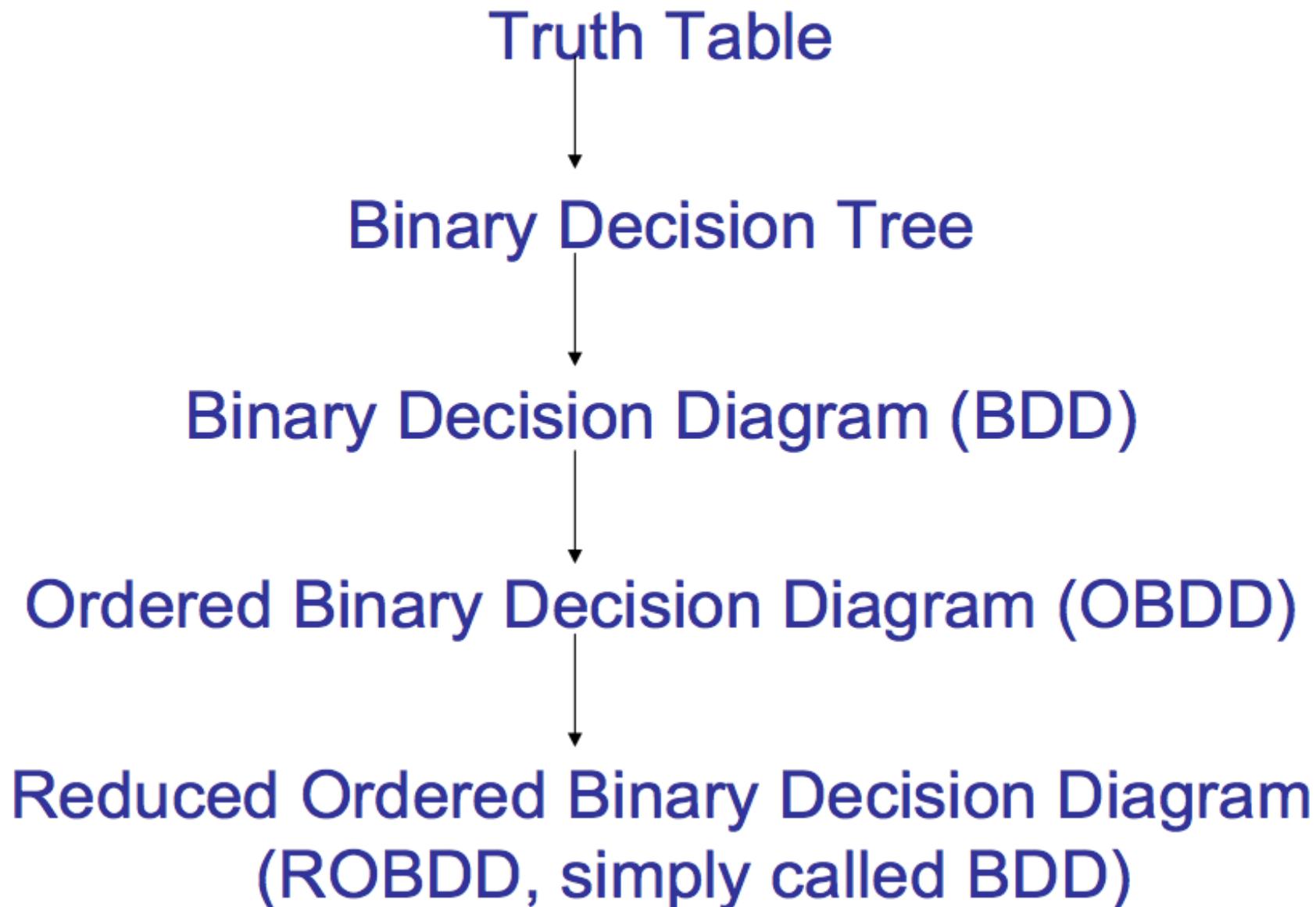
$A(x) = \neg v$;

if satisfy(ϕ) return T;

unassign $A(x)$; // undo assignment for backtracking.

return F; }

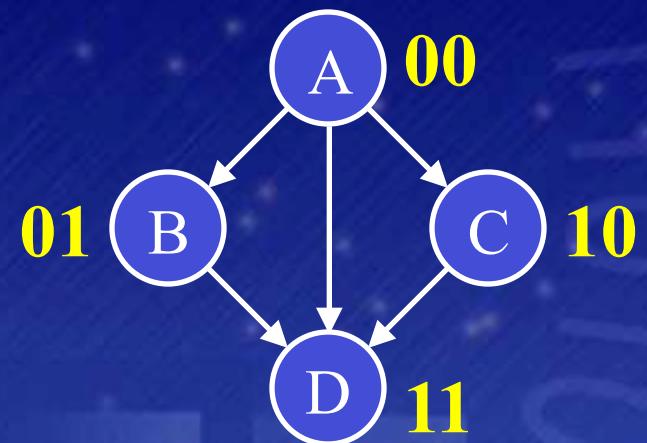
BDDs from Truth Tables



Truth table, boolean function

from		to		
x_1	x_2	x_3	x_4	f
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	0
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

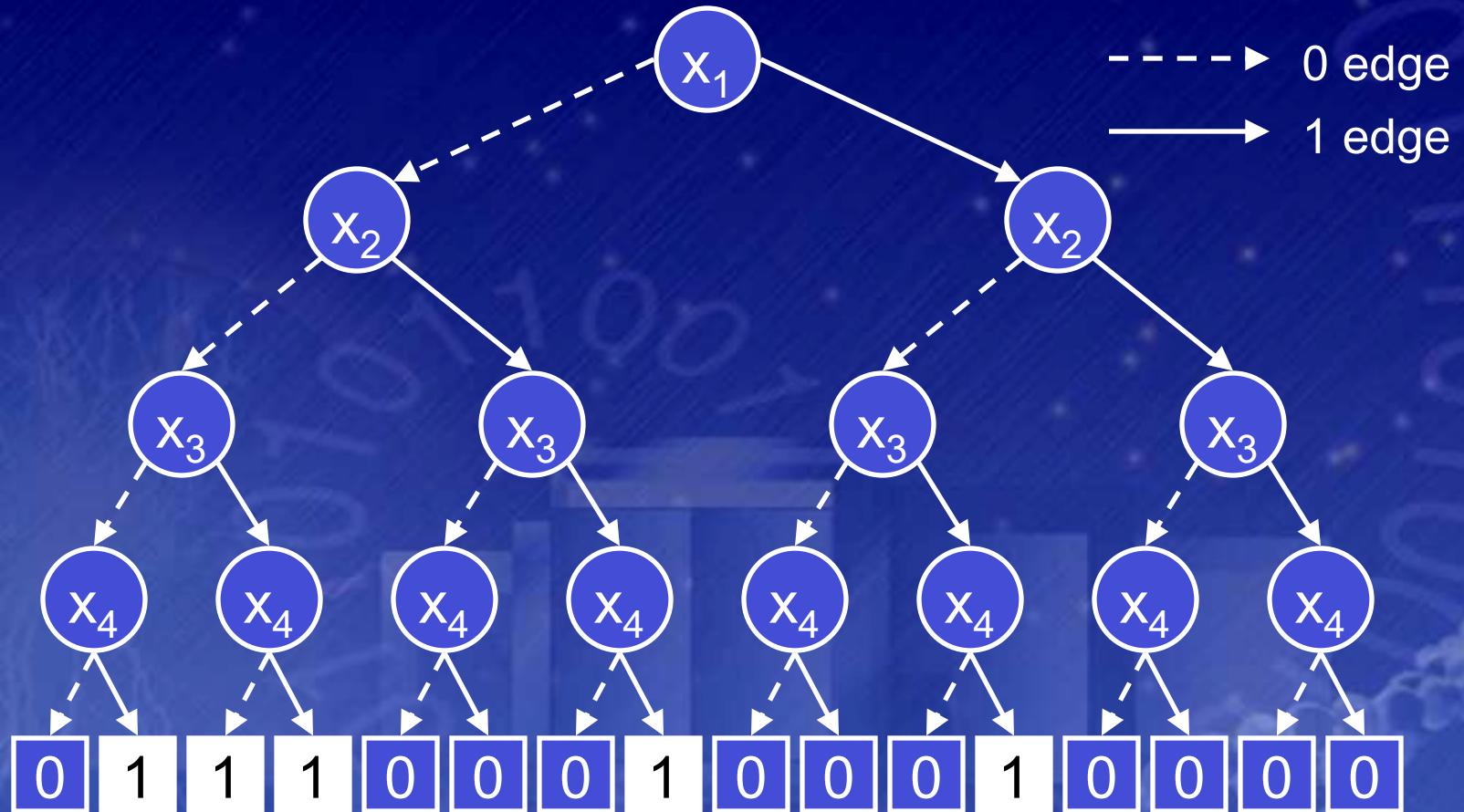
- Relation expressed as a binary function.
 - A=00, B=01, C=10, D=11



Binary Decision Diagrams

(Bryant 1986)

- Encoding of a truth table.



Reduction

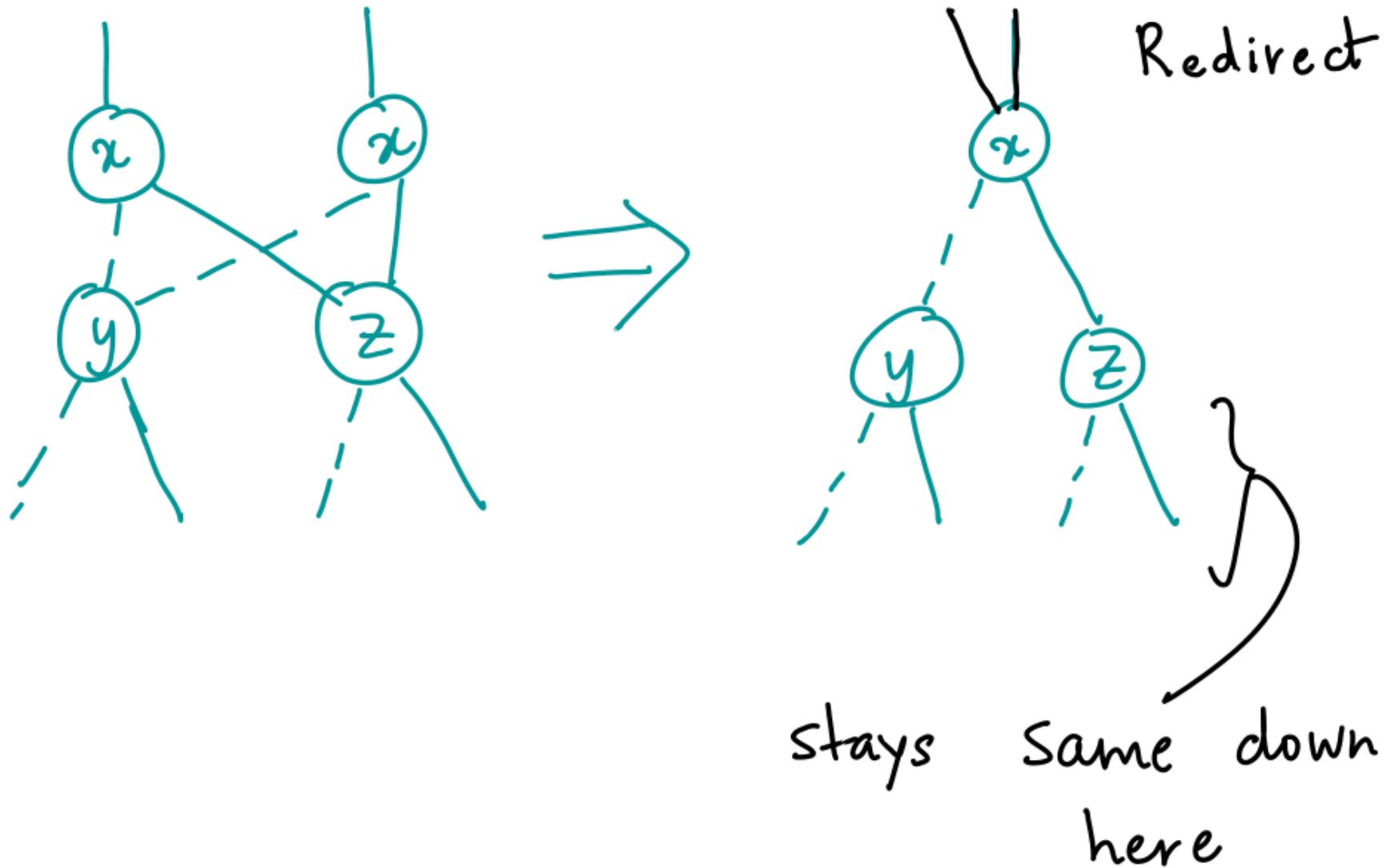
- Identify Redundancies
- 3 Rules
 - Merge equivalent leaves
 - Merge isomorphic nodes
 - Eliminate redundant tests

Merge equivalent leaves

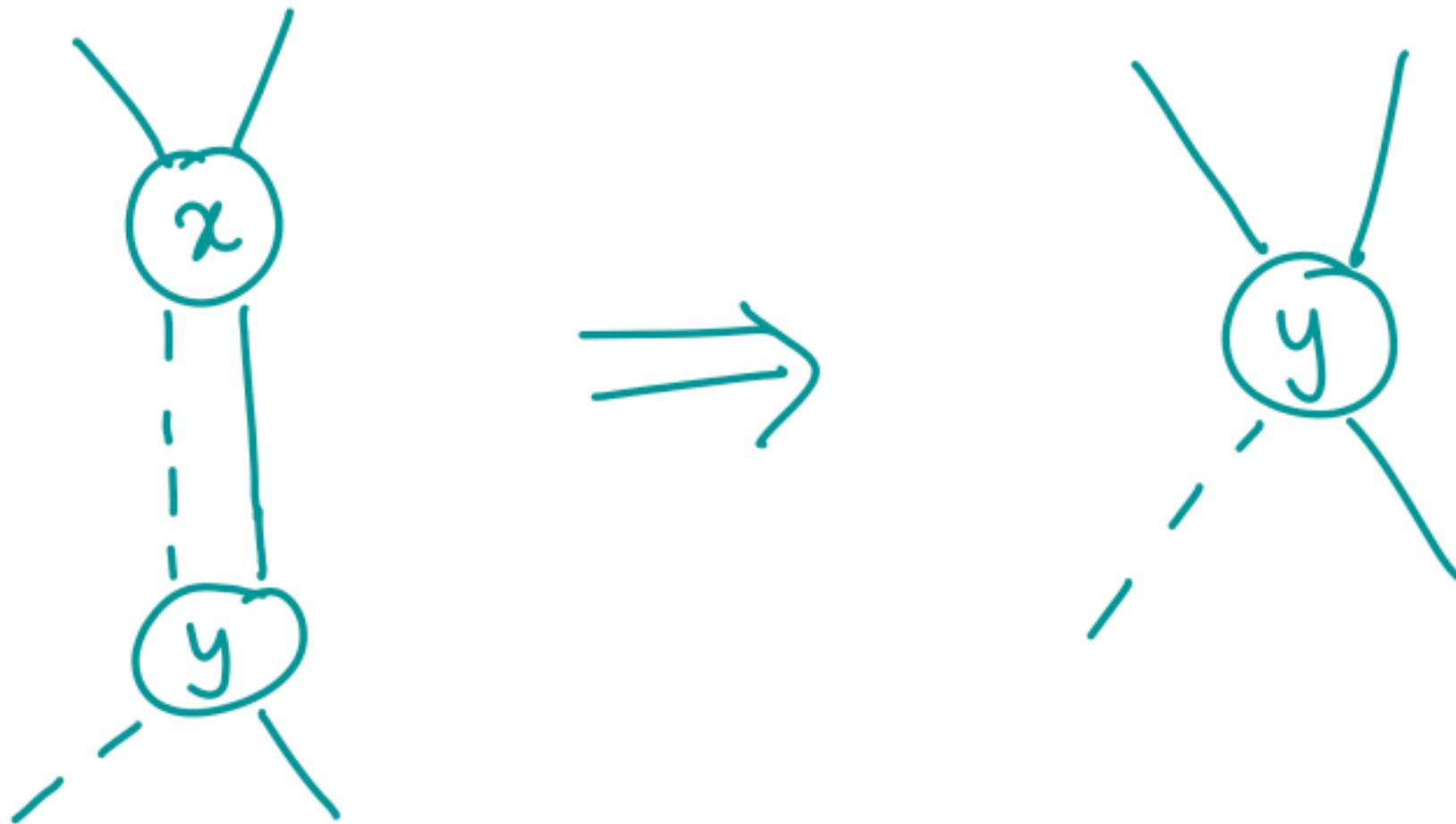


"a" is either 0 or 1

Merge isomorphic nodes

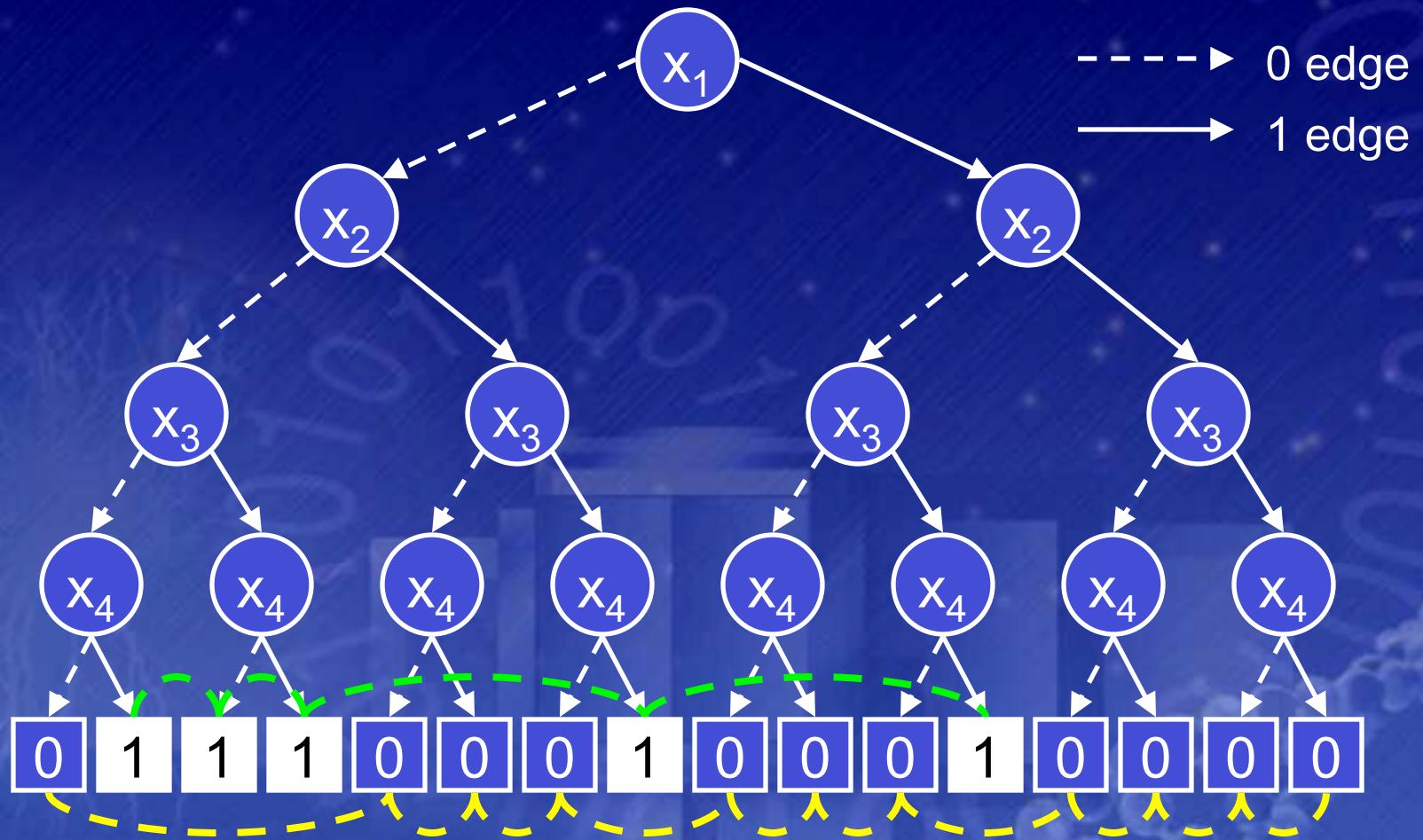


Eliminate redundant tests



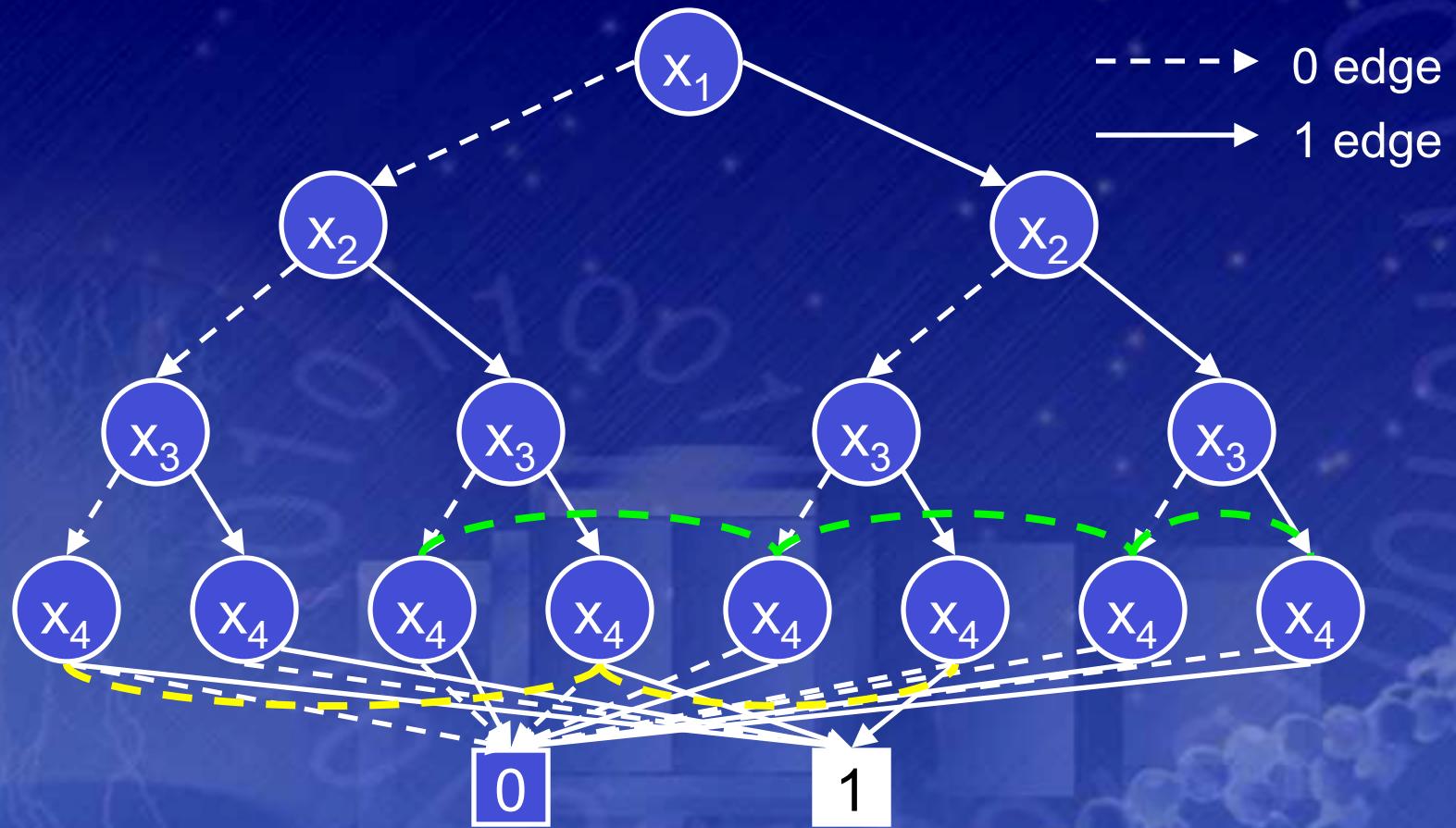
Binary Decision Diagrams

- Collapse redundant nodes.



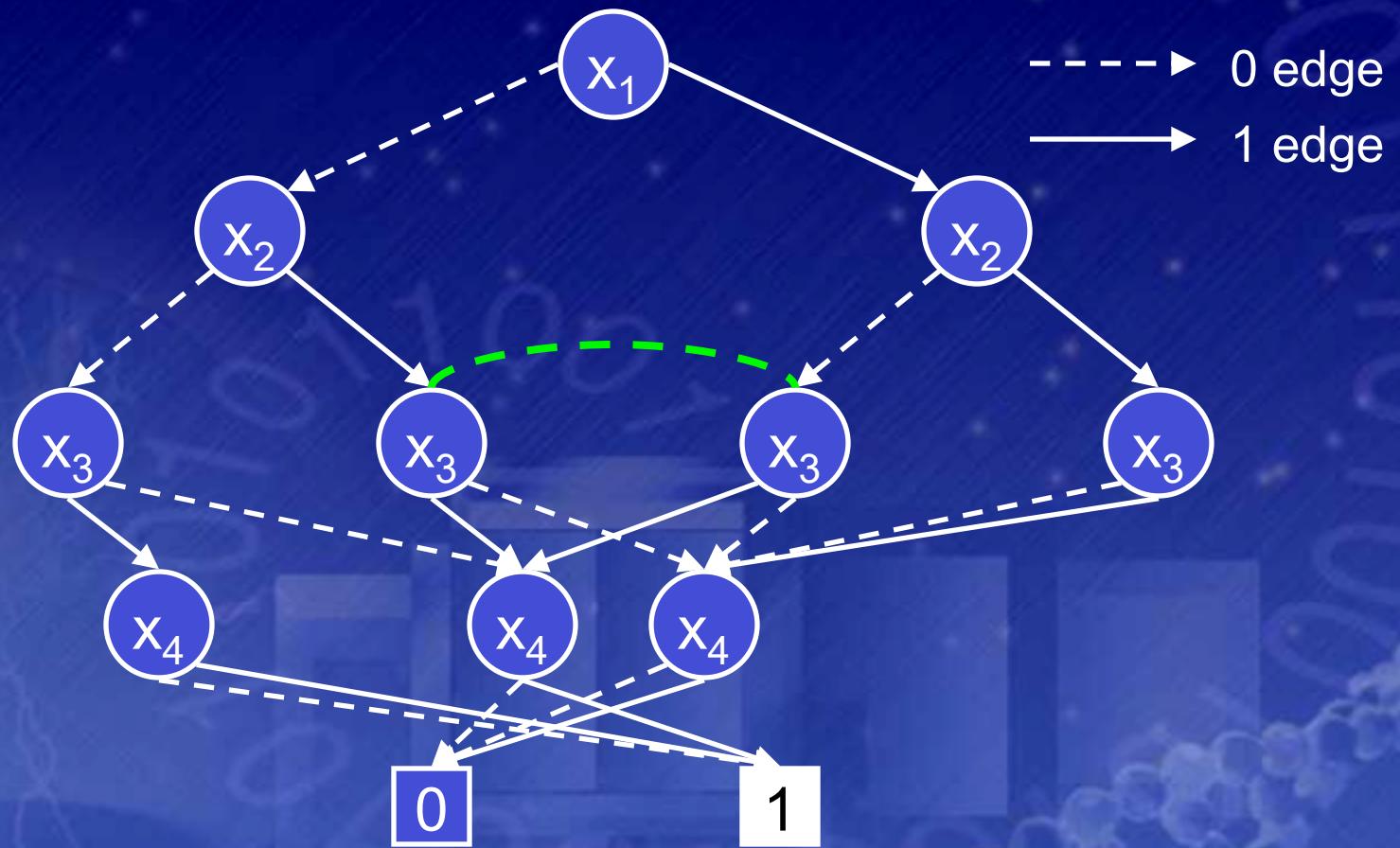
Binary Decision Diagrams

- Collapse redundant nodes.



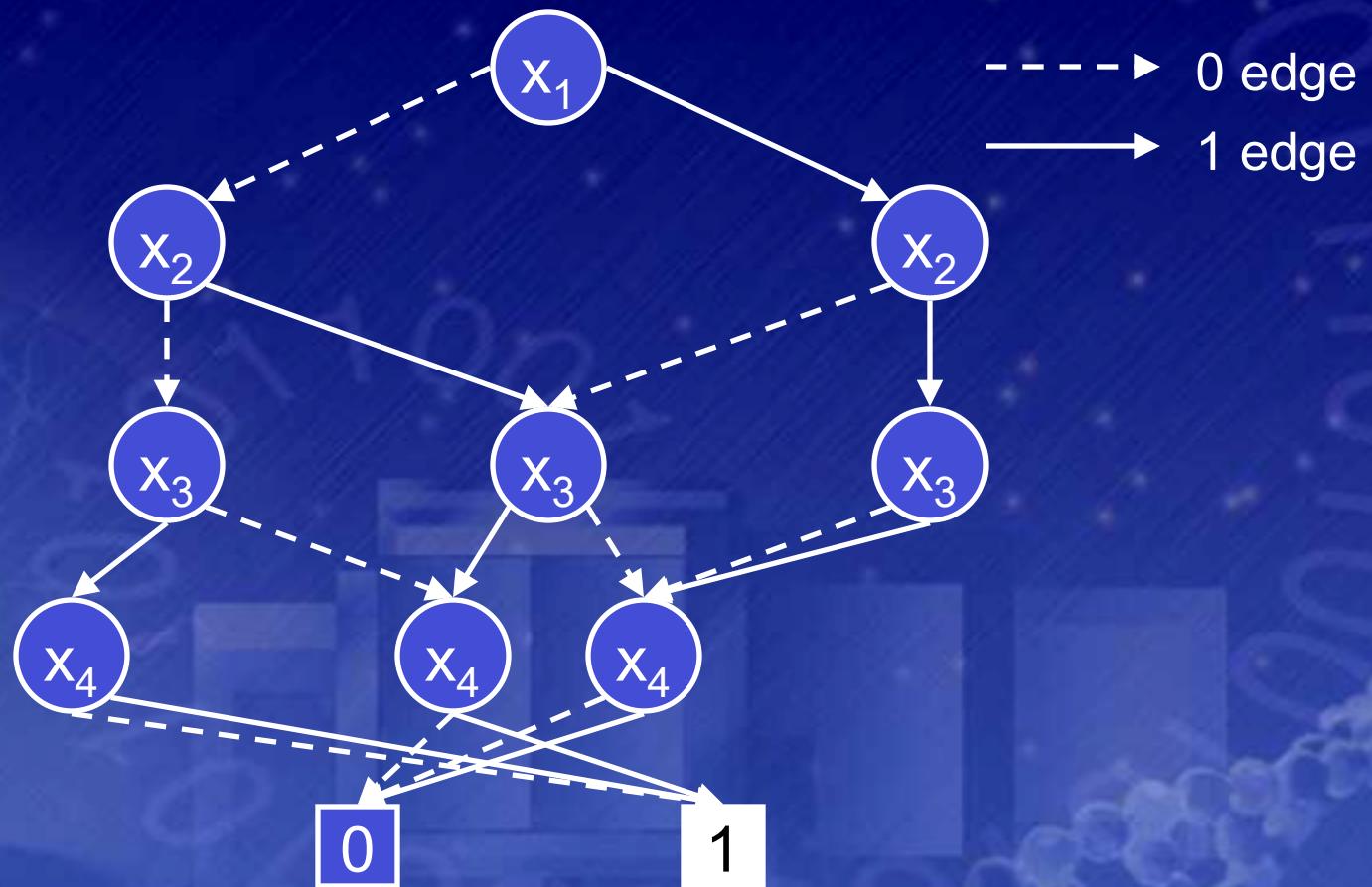
Binary Decision Diagrams

- Collapse redundant nodes.



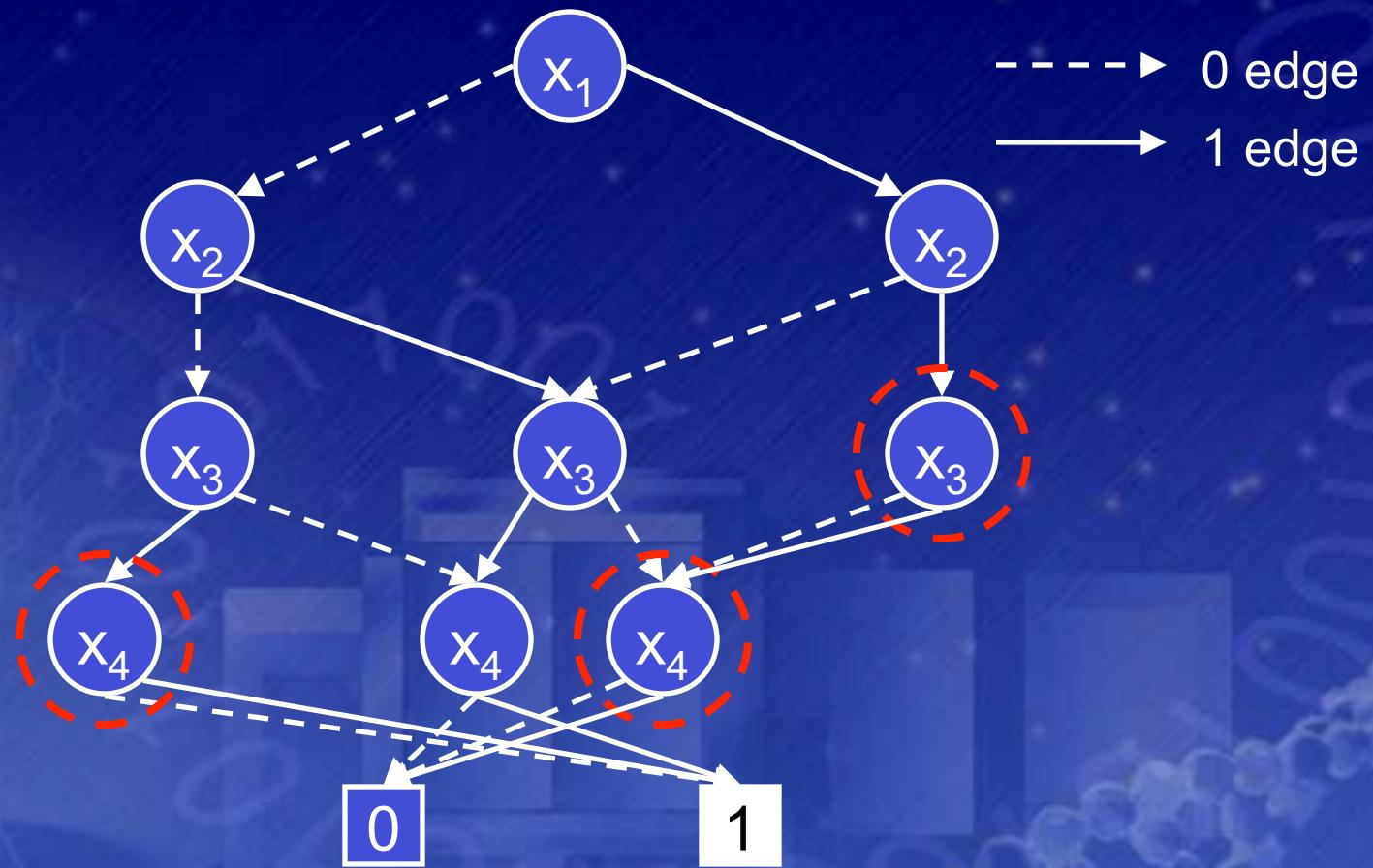
Binary Decision Diagrams

- Collapse redundant nodes.



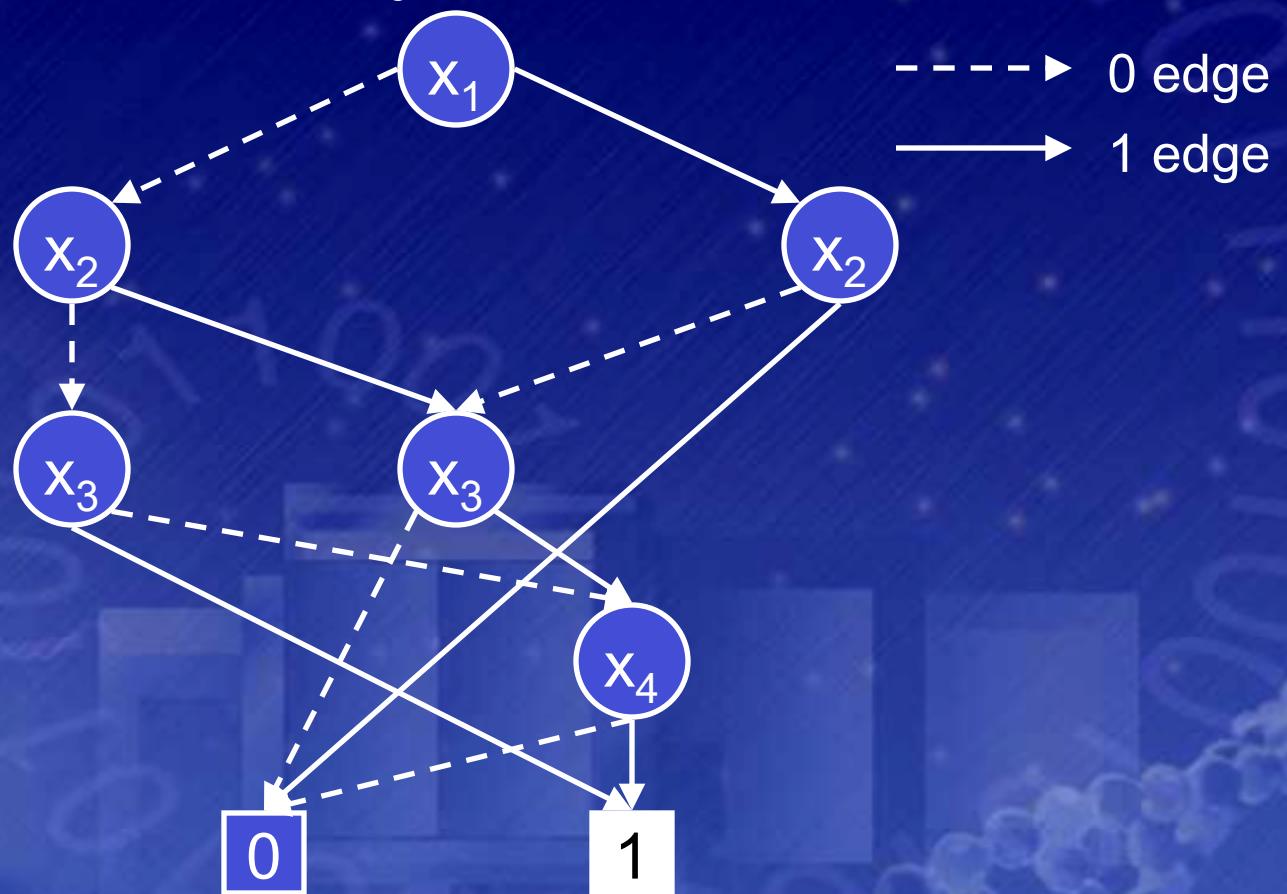
Binary Decision Diagrams

- Eliminate unnecessary nodes.



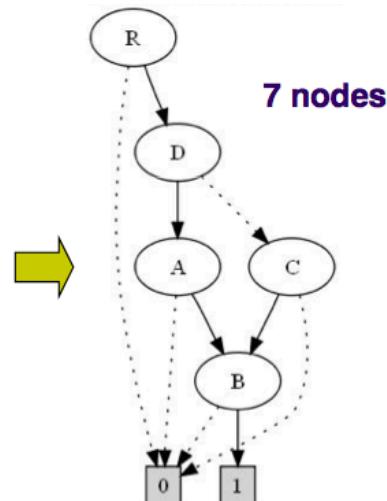
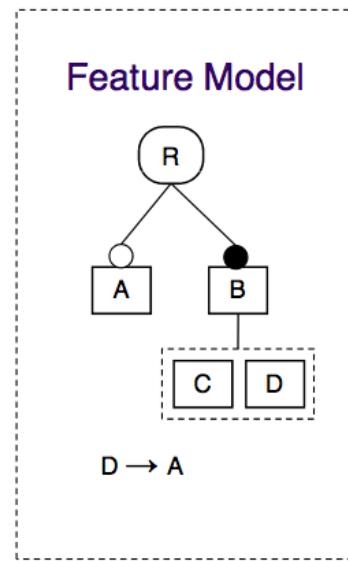
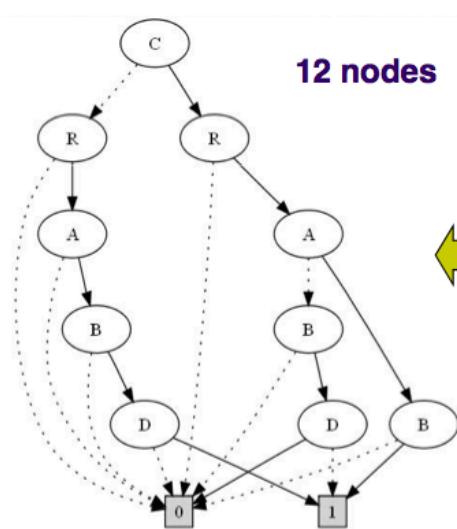
Binary Decision Diagrams

- Eliminate unnecessary nodes.



Binary Decision Diagrams

- The size of the BDD is very sensitive to the order of the BDD variables
 - Two equivalent BDDs for the same feature model



Variable Order: **C,R,A,B,D**

Variable Order: **R,D,A,C,B**

Constraint Satisfaction Problem

Given:

- **Variables** x_1, \dots, x_n ,
- **Domains** D_1, \dots, D_n ,

Constraint Satisfaction Problem (CSP):

$$\{\mathcal{C} ; x_1 \in D_1, \dots, x_n \in D_n\}$$

\mathcal{C} – constraints, each on a subsequence of x_1, \dots, x_n .

$(d_1, \dots, d_n) \in D_1 \times \dots \times D_n$ is a **solution** to

$$\{\mathcal{C} ; x_1 \in D_1, \dots, x_n \in D_n\}$$

if for every constraint $C \in \mathcal{C}$ on x_{i_1}, \dots, x_{i_m}

$$(d_{i_1}, \dots, d_{i_m}) \in C.$$

Constraint Satisfaction Problem: Feature Models

domain: {0, 1}

variables: features

constraints: see before

Given:

- **Variables** x_1, \dots, x_n ,
- **Domains** D_1, \dots, D_n ,

Constraint Satisfaction Problem (CSP):

$$\{\mathcal{C} ; x_1 \in D_1, \dots, x_n \in D_n\}$$

\mathcal{C} – constraints, each on a subsequence of x_1, \dots, x_n .

$(d_1, \dots, d_n) \in D_1 \times \dots \times D_n$ is a **solution**
to

$$\{\mathcal{C} ; x_1 \in D_1, \dots, x_n \in D_n\}$$

if for every constraint $C \in \mathcal{C}$ on x_{i_1}, \dots, x_{i_m}

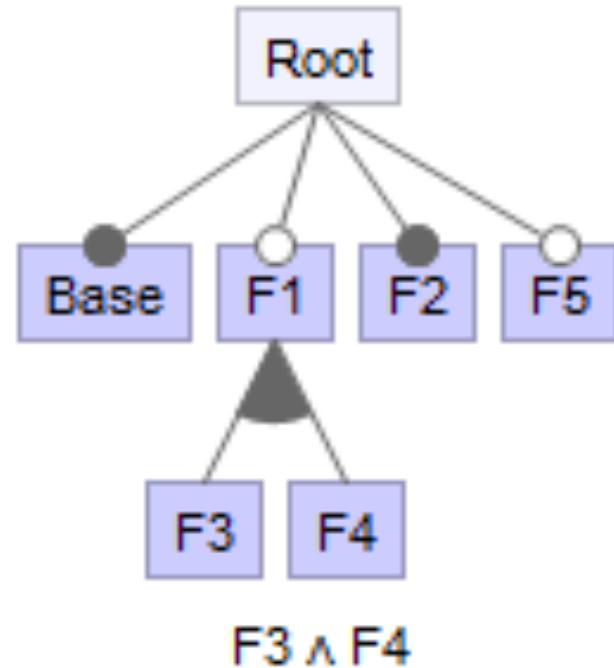
$$(d_{i_1}, \dots, d_{i_m}) \in C.$$

Feature Models and Automated Reasoning

**Operations can be realized using
SAT solvers, BDDs or CSP
(satisfiability problem)**

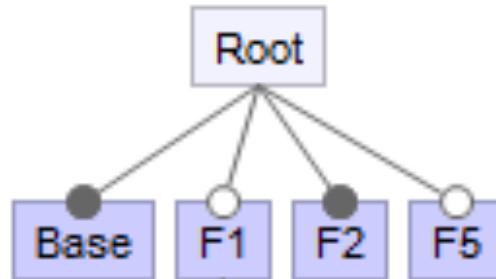
Consistency

- SAT-Solver
 - SAT(FM)



Core and dead features

- Dead : $\text{SAT}(\text{FM} \wedge F)$
- Core: $\text{SAT}(\text{FM} \wedge \text{not}(F))$



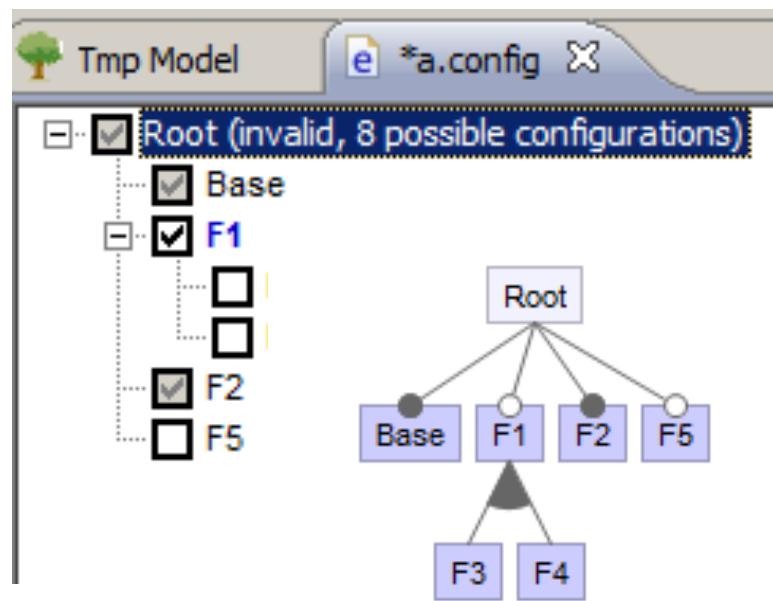
$$F5 \Rightarrow F4 \vee \text{Base}$$

$$F3 \Rightarrow F2 \wedge F5$$

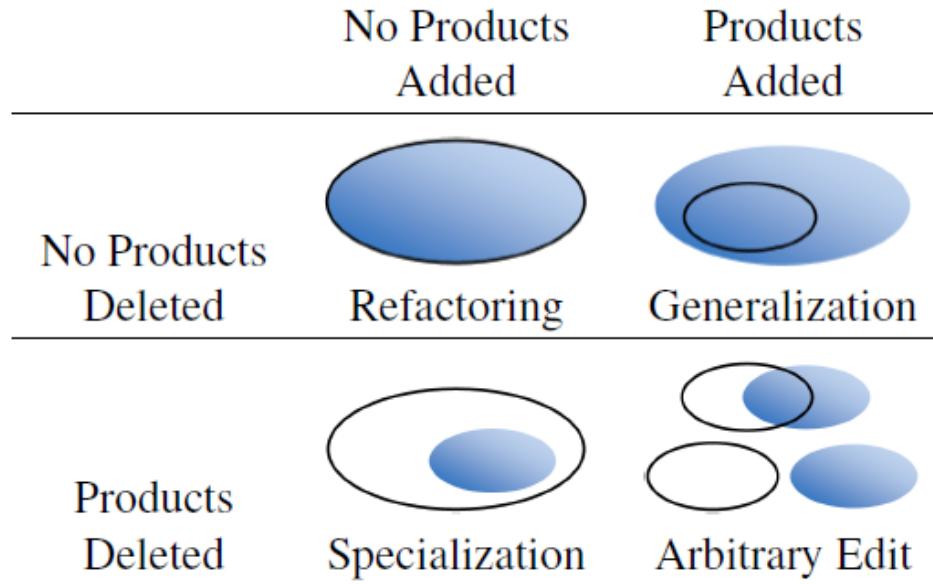
$$\neg(F4 \wedge F2)$$

Partial configuration

- $\text{SAT}(\text{FM} \wedge \text{PK} \wedge \text{F})$
- $\text{SAT}(\text{FM} \wedge \text{PK} \wedge \text{not(F)})$



Relationship between feature models



- Refactoring
 - Tautology: $(FM1 \Leftrightarrow FM2)$
 $= \text{not SAT}(\text{not } (FM1 \Leftrightarrow FM2))$

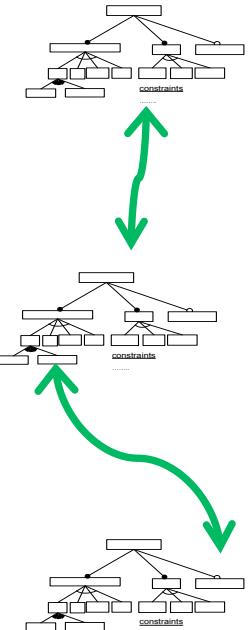
FAMILIAR language and environment

Reasoning operations implemented in a dedicated language

The screenshot displays the FeatureIDE platform interface, showing various components of the Frasco framework.

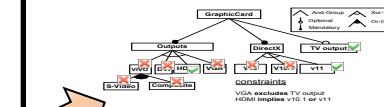
- Feature Diagram:** A tree diagram illustrating the inheritance structure of features. The root node is "component.factory". It branches into "delegate_memory_general" and "fractl_bootstrap_class_provide". "delegate_memory_general" further branches into "component_provider" and "generator". "generator" has children "sgo" and "sgo_provide". "sgo_provide" has a child "sgo". A legend indicates feature types: Mandatory (solid circle), Optional (open circle), Alternative (triangle), Abstract (triangle with dot), and Concrete (square).
- Code Editor:** A Groovy script titled "#!/usr/bin/env groovy" containing logic for renaming features and setting up feature slices. It includes imports for `frasco.core`, `frasco.core.sco`, `frasco.core.sco.impl`, and `frasco.core.sco.util`. The script renames features like `feMerle.Metamodel` and `feMerle.rest`, and performs operations like `setDiff` and `slice` on feature arches.
- Configuration Editors:** Two windows titled "*Simple.config" and "*Full.config" showing configurations for "GraphProductLine". Both windows have a tree view with nodes for "Algorithms", "Graphs", "Search", and "Implementation". The "Graphs" node under "GraphProductLine" is expanded, showing "GraphType" with "Directed" checked (highlighted in green) and "Undirected", "Weight", "Weighted", and "UnWeighted" options. The "Implementation" node shows "DFS" and "BFS" checked (highlighted in green). The "Search" node shows "OnlyNeighbors" and "Edges" options.

FAMILIAR language and environment

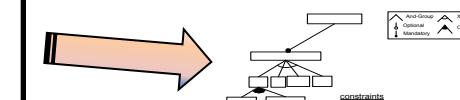


```
// foo.fml
fm1 = FM ("foo1.tvl")
fm2 = FM ("foo2.m")
fm3 = merge intersection { fm1 fm2 }
c3 = counting fm3
renameFeature fm3.TV as "OutputTV"
fm5 = aggregate { fm3 FM ("foo4.xml") }
assert (isValid fm5)
fm6 = slice fm5 including fm5.TV.*
export fm6
```

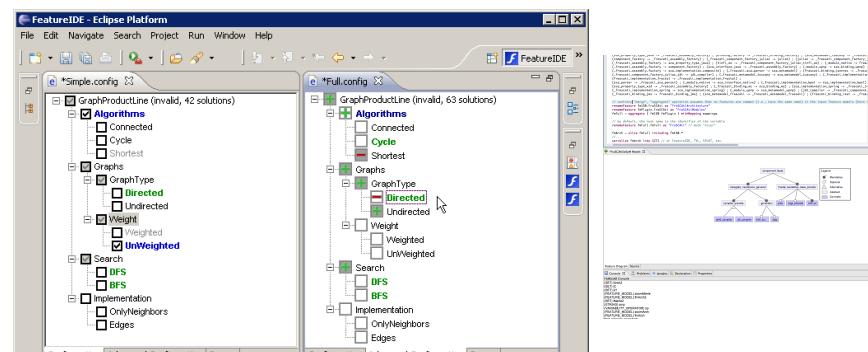
FAMILIAR



True/False
8759
"OutputTV", "TV"



Language facilities



Environment



Feature Models

#4 Applications of variability models?
variability model and so what?

Feature model in the SPL framework

- Not restricted to document/communicate
- Product/model derivation
 - reminder of previous course
- Safe composition and automated reasoning
- Configurators

Feature Models

+

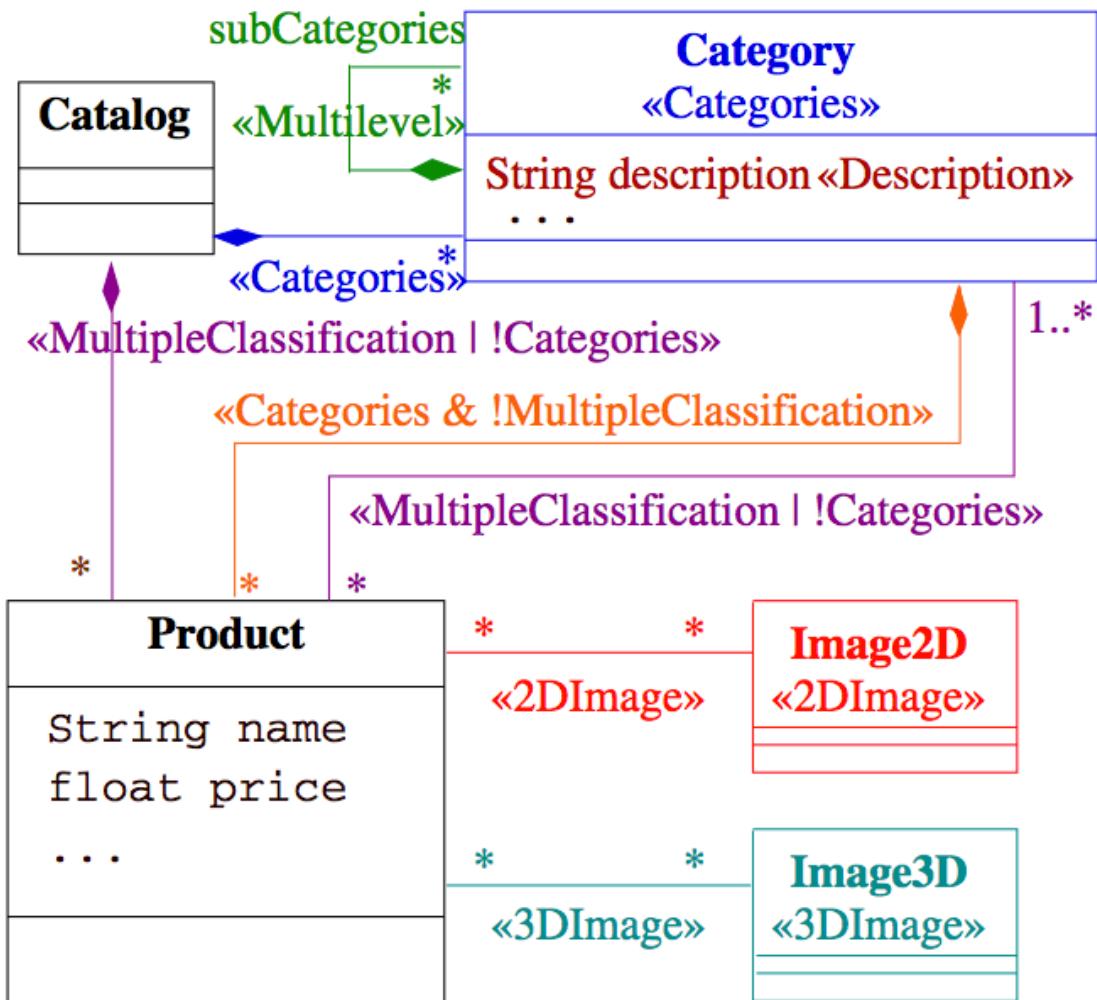
other artefacts

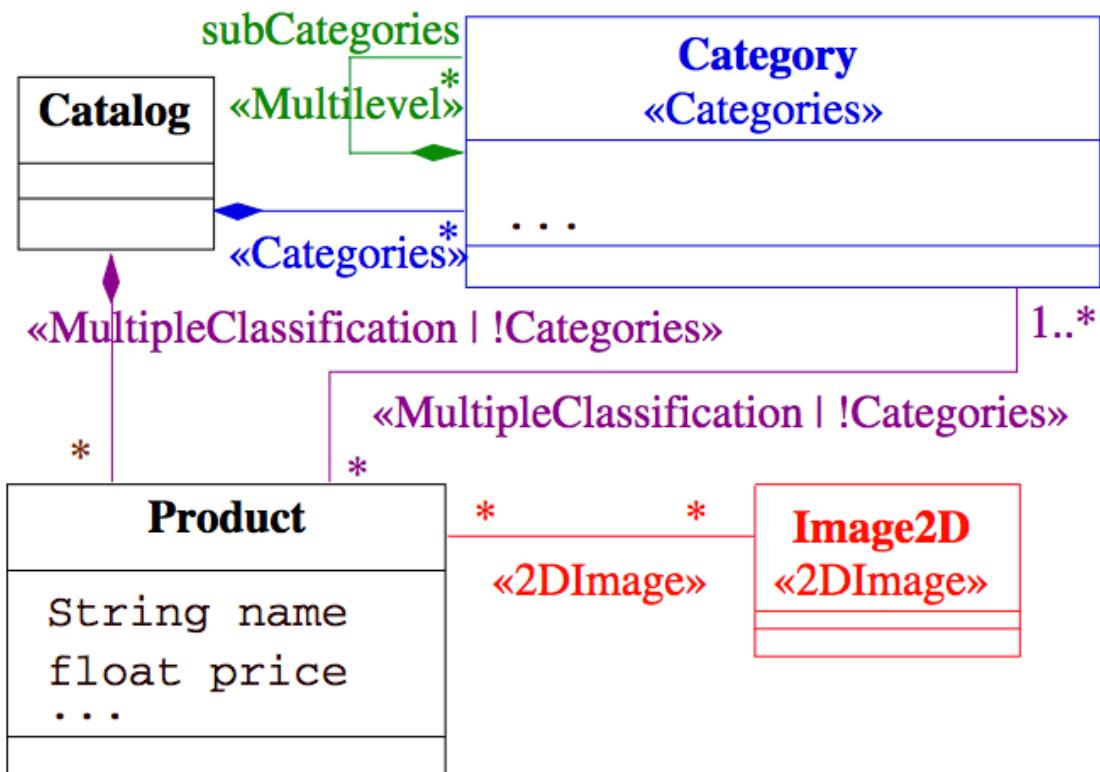
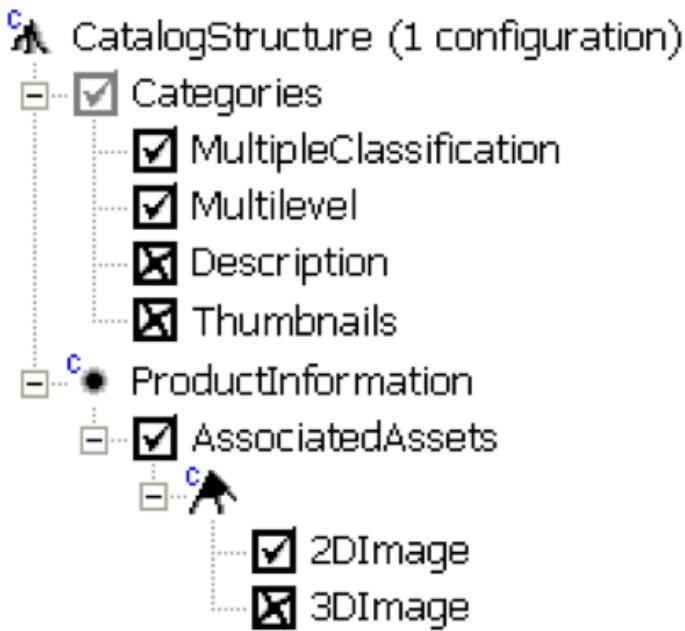
(what we can do with feature models)

**“features” in feature models:
a label**

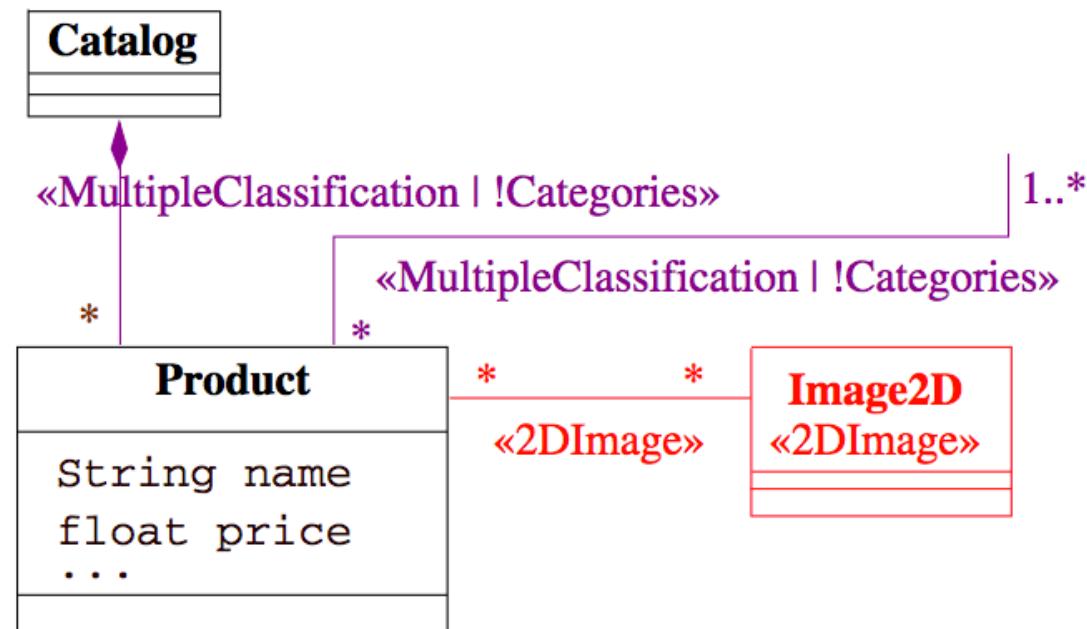
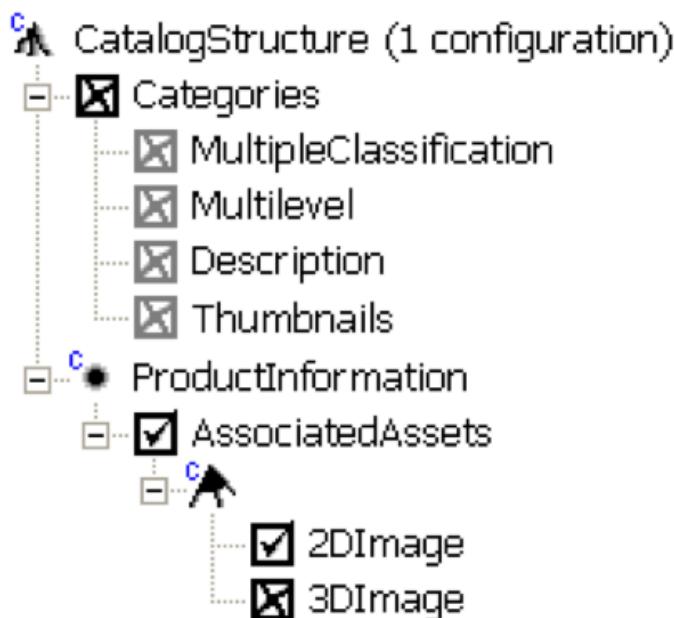
▲ CatalogStructure (52 configurations)

- Categories
 - MultipleClassification
 - Multilevel
 - Description
 - Thumbnails
- ProductInformation
 - AssociatedAssets
 - 2DImage
 - 3DImage



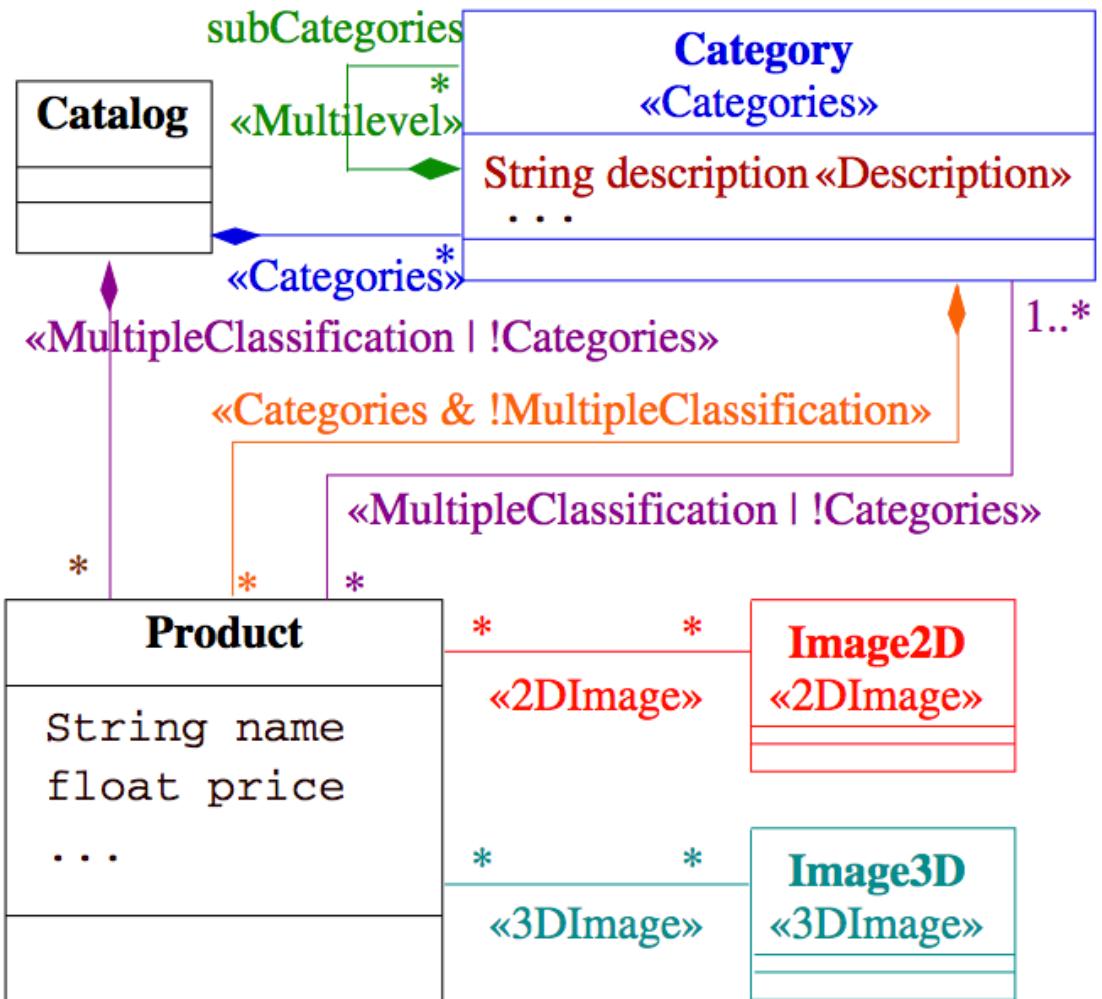
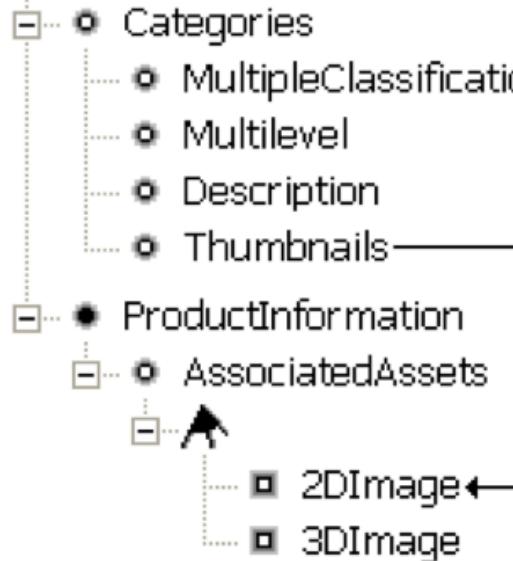


Ooops

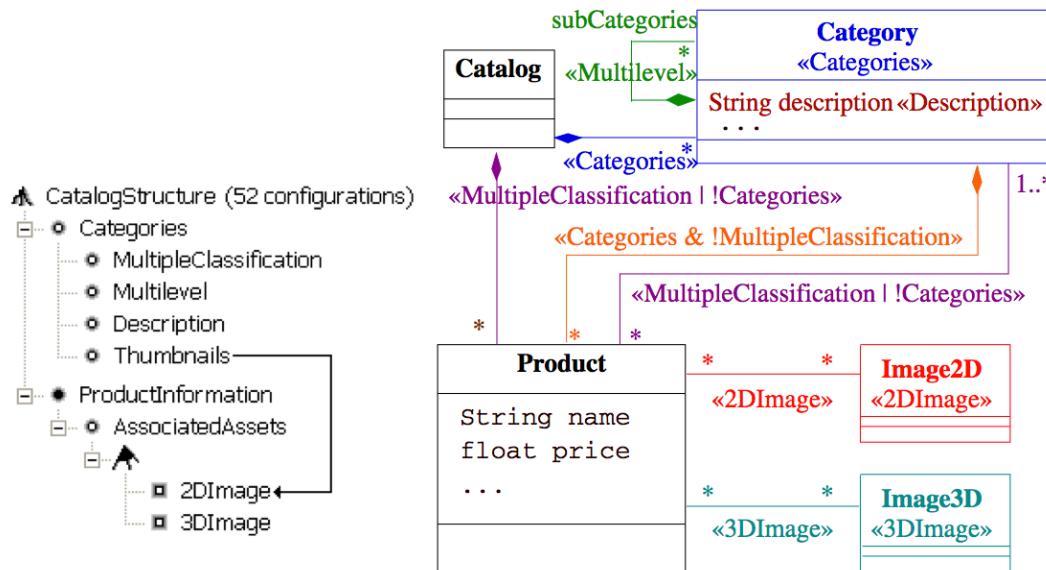


Safe composition? No!

CatalogStructure (52 configurations)

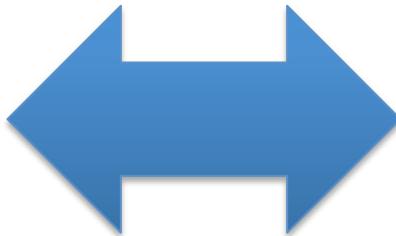


Safe composition: how does it work?



```

qFM          = cs ∧
root:        (ct ⇒ cs) ∧ (mc ⇒ ct) ∧ (ml ⇒ ct) ∧
child-parent: (ds ⇒ ct) ∧ (tn ⇒ ct) ∧ (pi ⇒ cs) ∧
group:       (aa ⇒ pi) ∧ (i2 ⇒ aa) ∧ (i3 ⇒ aa) ∧
mandatory:  (aa ⇒ choice1,2(i2, i3)) ∧
additional: (cs ⇒ pi) ∧
            (tn ⇒ i2)
  
```



**Another
propositional
formula**

Mapping: an example

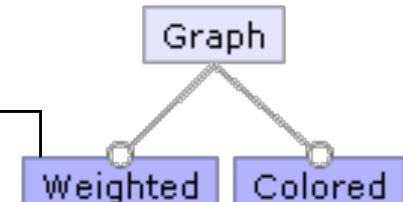
```
class Graph {  
    Vector nv = new Vector(); Vector ev = new Vector();  
    Edge add(Node n, Node m) {  
        Edge e = new Edge(n, m);  
        nv.add(n); nv.add(m); ev.add(e);  
        e.weight = new Weight();  
        return e;  
    }  
    Edge add(Node n, Node m, Weight w)  
    Edge e = new Edge(n, m);  
    nv.add(n); nv.add(m); ev.add(e);  
    e.weight = w; return e;  
}  
void print() {  
    for(int i = 0; i < ev.size(); i++) {  
        ((Edge)ev.get(i)).print();  
    }  
}
```

```
class Node {  
    int id = 0;  
    Color color = new Color();  
    void print() {  
        Color.setDisplayColor(color);  
        System.out.print(id);  
    }  
}
```

```
class Edge {  
    Node a, b;  
    Color color = new Color();  
    Weight weight = new Weight();  
    Edge(Node _a, Node _b) { a = _a; b = _b; }  
    void print() {  
        Color.setDisplayColor(color);  
        a.print(); b.print();  
        weight.print();  
    }  
}
```

```
class Color {  
    static void setDisplayColor(Color c) { ... }  
}
```

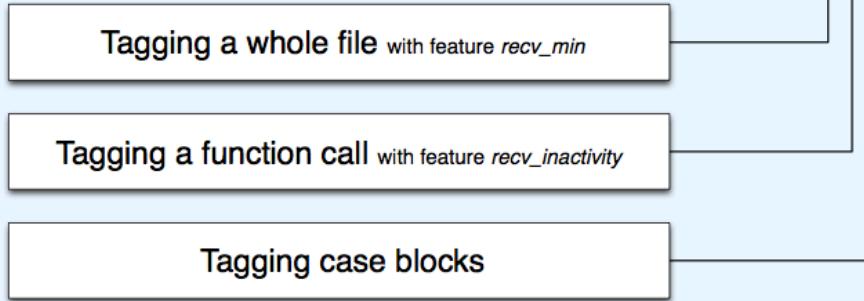
```
class Weight { void print() { ... } }
```



Tag and prune

Tag: annotate blocks of code with *features*

Prune: remove blocks tagged with non-selected features



```

/*@feature:recv_min@*/ /*@file_feature@*/
void cfdp_receiver_handle_PDU(cfdp_receiver* const me, struct cfdp_buffer* PDU_buffer,
CFDP_PDU_type_t PDU_type) {
    /*@feature:recv_inactivity@*/
    /* Restart inactivity timer */
    cfdp_timer_start(&(me->timer_inactivity), me->config.timeout_inactivity);
}

/* Handle PDU and dispatch it depending on its type */
switch (PDU_type)
{
    /*@feature:recv_min_ack@*/
    case CFDP_PDU_ACK_FINISHED:
    {
        cfdp_receiver_handle_PDU_ack_no_error(me, PDU_buffer);
    }
    break;

    case CFDP_PDU_EOF_NO_ERROR:
    {
        cfdp_receiver_handle_PDU_eof_no_error(me, PDU_buffer);
    }
    break;

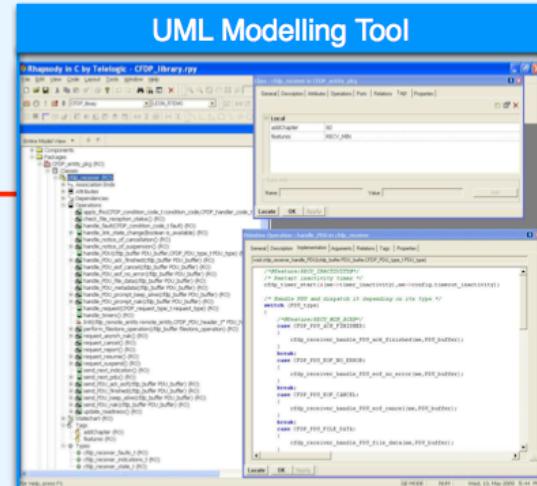
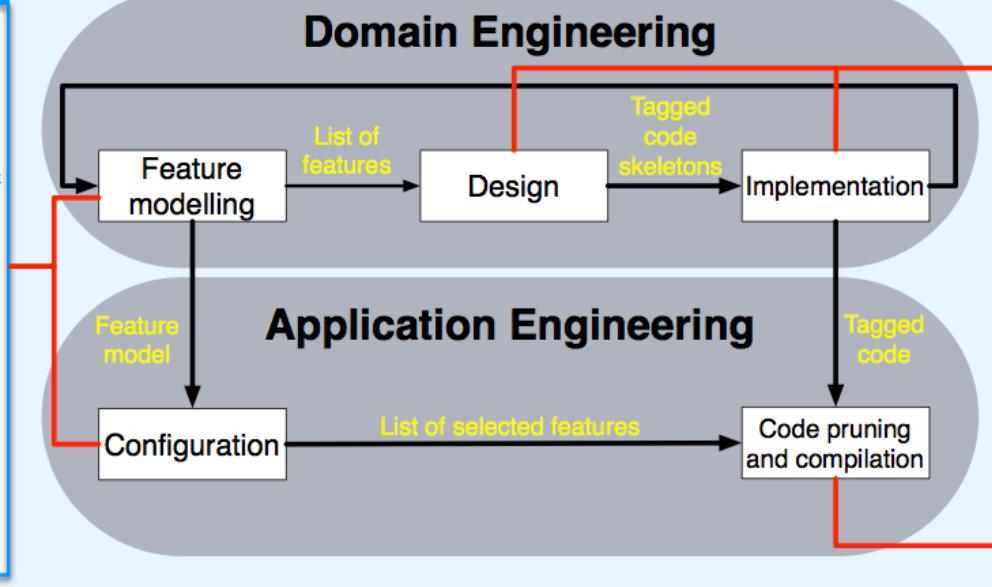
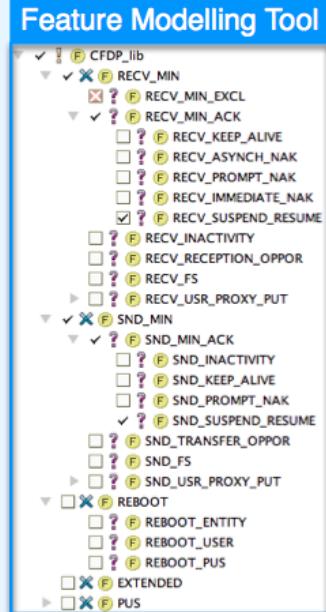
    /*@feature:recv_keep_alive@*/
    case CFDP_PDU_PROMPT_KEEP_ALIVE:
    {
        cfdp_receiver_handle_PDU_prompt_keep_alive(me, PDU_buffer);
    }
    break;

    /*@feature:recv_prompt_nak@*/
    case CFDP_PDU_PROMPT_NAK:
    {
        cfdp_receiver_handle_PDU_prompt_nak(me, PDU_buffer);
    }
    break;

    default:
    {
        /* Unexpected PDU -> discard */
    }
    break;
}
}

```

AST (Abstract Syntax Tree) diagram showing the structure of the annotated code.

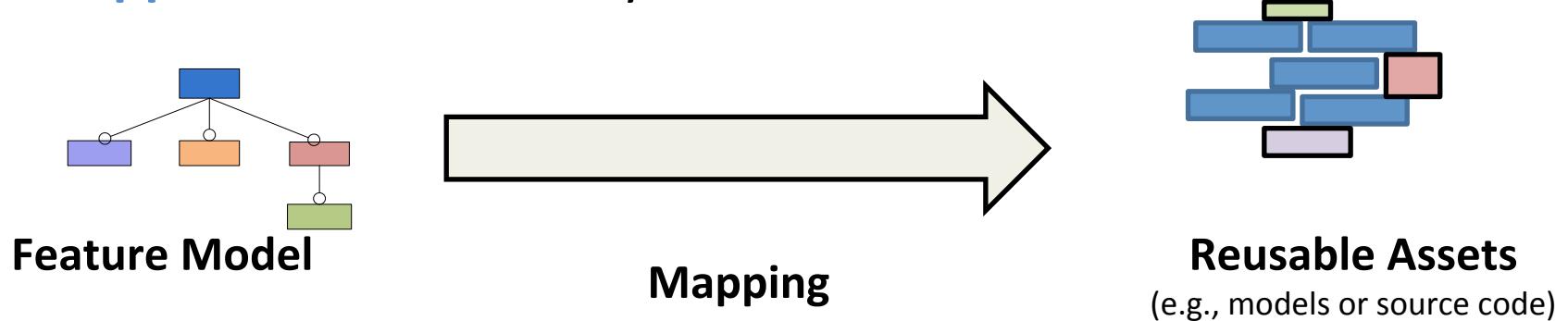


Feature models in the **real**

- SPLOT repository
 - more than 100 feature models reported from the literature (various domains)
- Linux feature model
 - worst case: more than 6300 features!
 - eCos, FreeBSD, BusyBox, etc.
- Automotive industry
 - thousands of features
- Wiki matrix
 - wiki engines: ~ 2000 features

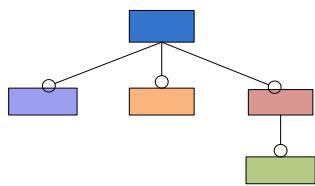
Summary

- **Variability models**
 - ... are needed for mastering complexity: variability is everywhere, precise specification, automated reasoning
- Feature Models
 - **Formalism and theory**: modeling principles, semantics, logics
 - **Language** to elaborate/build variability models
 - **Reasoning techniques** to analyze properties of an SPL (scalable and automated way)
 - **Applications**: variability models and so what?

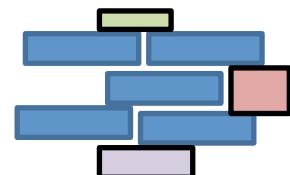


Summary

Language (TVL, FAMILIAR)



Feature Model



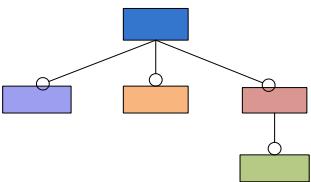
Reusable Assets

(e.g., models or source code)

 **Automated reasoning**

Summary

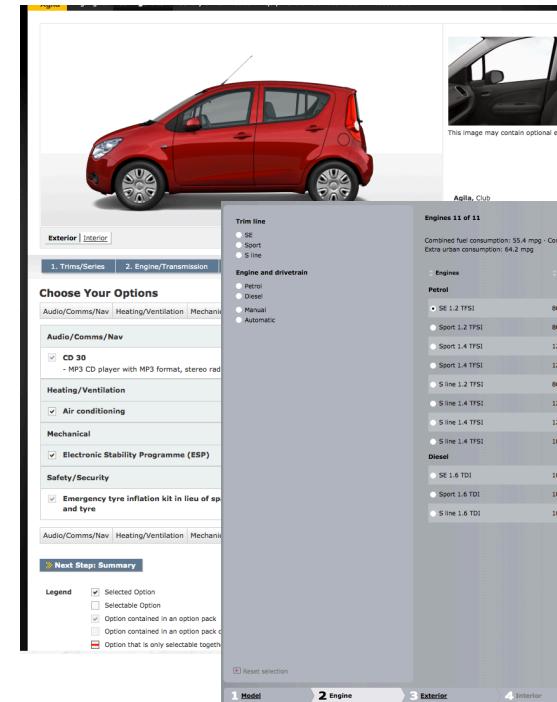
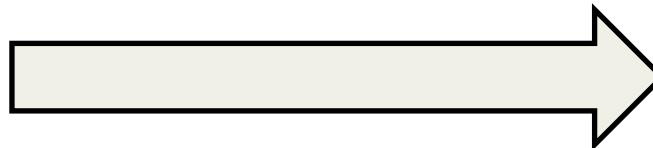
Language (TVL, FAMILIAR)



Feature Model

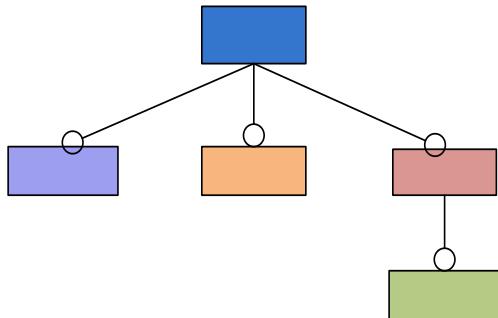


Automated reasoning

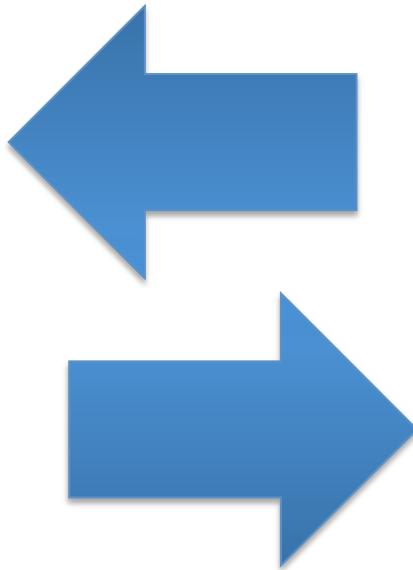


Running project

- Re-engineer a car configurator
 - we are making some progress, right?



Variability Model
(Feature Model)



The screenshot shows a red Audi Agila Club car on the left. To its right is a detailed view of the interior. Below the car is a summary table titled 'Engines 11 of 11' comparing engine options across various trim levels. The table includes columns for Engine Type, Power (PS), Gearbox, Drive train, and RRP (GBP). It lists options like SE 1.2 TFSI, Sport 1.2 TFSI, Sport 1.4 TFSI, S line 1.2 TFSI, S line 1.4 TFSI, and S line 1.4 TFSI. The table also includes sections for Diesel engines and a legend for option status.

Engine Type	Power (PS)	Gearbox	Drive train	RRP (GBP)
SE 1.2 TFSI	86	5 speed	Front-wheel drive	13,335.00
Sport 1.2 TFSI	86	5 speed	Front-wheel drive	15,175.00
Sport 1.4 TFSI	122	6 speed	Front-wheel drive	15,585.00
S line 1.2 TFSI	86	5 speed	Front-wheel drive	17,035.00
S line 1.4 TFSI	122	6 speed	Front-wheel drive	16,720.00
S line 1.4 TFSI	122	5 speed	Front-wheel drive	17,130.00
S line 1.4 TFSI	122	5 speed	Front-wheel drive	18,580.00
S line 1.4 TFSI	185	5 speed	Front-wheel drive	20,510.00
Diesel				
SE 1.6 TDI	105	5 speed	Front-wheel drive	14,395.00
Sport 1.6 TDI	105	5 speed	Front-wheel drive	16,235.00
S line 1.6 TDI	105	5 speed	Front-wheel drive	17,780.00

Homework for the next course

- No homework!
 - No “course” next week, but interactive session for learning feature models, TVL language, etc.
- Feature modeling in practice
 - Domain analysis: the domain of cars
 - Elaboration of a feature model
 - Use of TVL
 - Use of automated reasoning techniques
- First step towards a car configurator