Datapath Design

Book of David A. Patterson

Pp: 203-211

Book of P. Hayes

PP: 266-273

Example of Floating point Addition

 $9.999 \times 10^{1} + 1.610 \times 10^{-1}$. we can store four decimal digits of the significand and two decimal digits of the exponent.

Step 1: Align the decimal point of the number that has the smaller exponent.

$$1.610_{\text{ten}} \times 10^{-1} = 0.1610_{\text{ten}} \times 10^{0} = 0.01610_{\text{ten}} \times 10^{1}$$

After shifting, the number is $0.016_{ten} \times 10^{1}$ Step 2: Add the significands.

$$9.999_{\text{ten}} + 0.016_{\text{ten}} = 10.015_{\text{ten}}$$

The sum is $10.015_{ten} \times 10^{1}$

Example of Floating point Addition

<u>Step 3:</u>

Normalize the result.

$$10.015_{\text{ten}} \times 10^1 = 1.0015_{\text{ten}} \times 10^2$$

Check for underflow and overflow. +127>2>-126

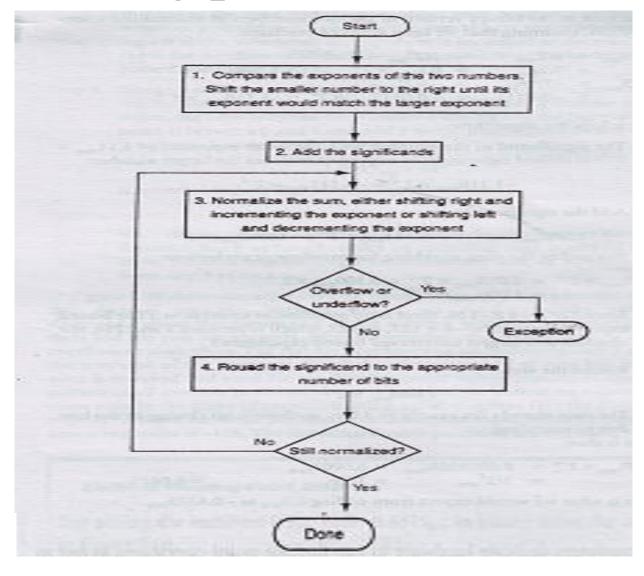
Step 4:

Round the result.

Rules: truncate the number if the digit to the right of the desired point is between 0 and 4. Add 1 to the digit if the number to the right is between 5 and 9.

 $1.002_{\rm ten} \times 10^2$

Floating point Addition



Add the numbers 0.5 and -0.4375 in binary.

$$0.5_{\text{ten}} = 1/2_{\text{ten}} = 1/2_{\text{ten}}^{1}$$

$$= 0.1_{\text{two}} = 0.1_{\text{two}} \times 2^{0} = 1.000_{\text{two}} \times 2^{-1}$$

$$-0.4375_{\text{ten}} = -7/16_{\text{ten}} = -7/2_{\text{ten}}^{4}$$

$$= -0.0111_{\text{two}} = -0.0111_{\text{two}} \times 2^{0} = -1.110_{\text{two}} \times 2^{-2}$$

Step 1:
$$-1.110_{\text{two}} \times 2^{-2} = -0.111_{\text{two}} \times 2^{-1}$$

Step 2:
$$1.000_{two} \times 2^{-1} + (-0.111_{two} \times 2^{-1}) = 0.001_{two} \times 2^{-1}$$

Step 3:
$$0.001_{two} \times 2^{-1} = 0.010_{two} \times 2^{-2} = 0.100_{two} \times 2^{-3} = 1.000_{two} \times 2^{-4}$$

Since $127 \ge -4 \ge -126$, there is no overflow and underflow.

Step 4: Round the sum 1.000_{two} × 2⁻⁴

$$1.000_{\text{two}} \times 2^{-4}$$

The sum is

$$1.000_{\text{two}} \times 2^{-4} = 0.0001000_{\text{two}} = 0.0001_{\text{two}}$$

= $1/2^{4}_{\text{ten}} = 1/16_{\text{ten}} = 0.0625_{\text{ten}}$

Example of Floating-Point Multiplication

 $1.110 \times 10^{10} \times 9.200 \times 10^{-5}$. we can store only four digits of the significand and two digits of the exponent.

Step 1:

Calculate the exponent of the product by simply adding the exponents of the operands together: $N_{ew exponent} = 10 + (-5) = 5$

Using the biased notation,

New exponent =
$$(10+127)+(-5+127) - 127$$

= $137 + 122 - 127$
= $132 = 5 + 127$

Example of Floating Point Multiplication

Step 2: Multiply the significands.

The product is 10.212000_{ten} and the final product is 10.212×10^5 Step 3:

Normalize the product and check for underflow and overflow.

$$10.212_{\text{ten}} \times 10^5 = 1.0212_{\text{ten}} \times 10^6$$

Example of Floating Point Multiplication

<u>Step 4:</u>

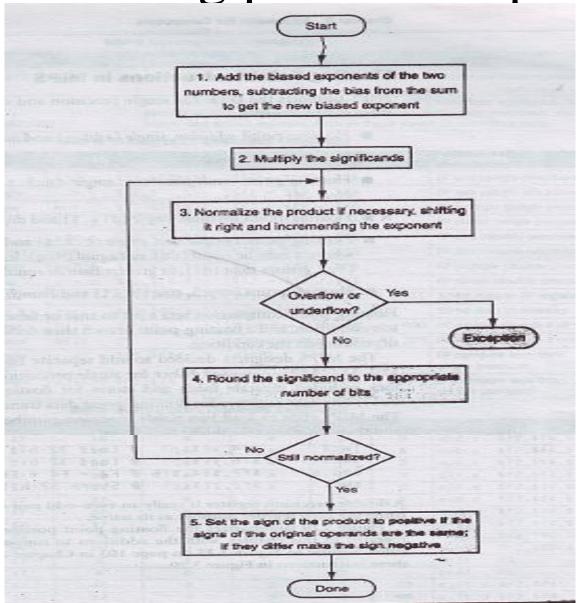
The number 1.0212×10^6 is rounded to 1.021×10^6

Step 5:

Set the sign the of the product. The sign of the product depends on the sign of the original operands. If they are both the same, the sign is positive. Otherwise it is negative.

$$+ 1.021 \times 10^{6}$$

Floating point Multiplication



Multiply the numbers 0.5_{ten} and -0.4375_{ten} .

In binary, $1.000_{\text{two}} \times 2^{-1} \text{ by } - 1.110_{\text{two}} \times 2^{-2}$.

Step 1:
$$-1+(-2) = -3$$

In biased representation:

$$(-1+127) + (-2+127) - 127 = (-1-2) + (127+127-127)$$

= -3 + 127 = 124

Step 2: Multiply the significands.

```
1.000<sub>two</sub>

x 1.110<sub>two</sub>

0000

1000

1000

1000

1110000<sub>two</sub>
```

The product is 1.110000×2^{-3} . Final product is 1.110×2^{-3} .

Step 3: The product is already normalized and there is no overflow and underflow since, $127 \ge -3 \ge -126$

Step 4: Rounding the product makes no change.

Step 5: The final product is - 1.110×2^{-3}

Converting to decimal,

$$-1.110_{\text{two}} \times 2^{-3} = -0.001110_{\text{two}} = -0.00111_{\text{two}}$$

= $-7/2^{5}_{\text{ten}} = -7/32_{\text{ten}} = -0.21875_{\text{ten}}$

Floating Point Arithmetic

Let (X_M, X_E) be a floating point representation of a number X, that has the numerical value $X_M \times B^{XE}$, where X_M and X_E are fixed point numbers.

Basic Arithmetic Operations on Floating-point Number:

Addition
$$X + Y = (X_{M}2^{X_{E} - Y_{E}} + Y_{M}) \times 2^{Y_{E}}$$
 where $X_{E} \leq Y_{E}$

Subtraction $X - Y = (X_{M}2^{X_{E} - Y_{E}} - Y_{M}) \times 2^{Y_{E}}$

Multiplication $X \times Y = (X_{M} \times Y_{M}) \times 2^{X_{E} + Y_{E}}$

Division $X/Y = (X_{M}/Y_{M}) \times 2^{X_{E} - Y_{E}}$

Floating-point Addition and Subtraction

Floating-point addition and subtraction have 3 main steps:

- 1. Compute $Y_E X_E$, a fixed point subtraction.
- 2. Shift Y_E - X_E places to the right to form X_M 2^{XE-YE}.
- 3. Compute $X_M 2^{XE-YE} \pm Y_M$, a fixed point addition and subtraction.

Example:

$$X = 1.32400111 \times 10^{17}$$
 and $Y = 1.04799245 \times 10^{21}$

$$Y_E - X_E = 21 - 17 = 4$$

$$X_{\rm M}2^{-4} = 0.00013240$$

$$X_M 2^{-4} + Y_M = 0.00013240 + 1.04799245 = 1.04812485$$

The final result is $1.04812485 \times 10^{21}$

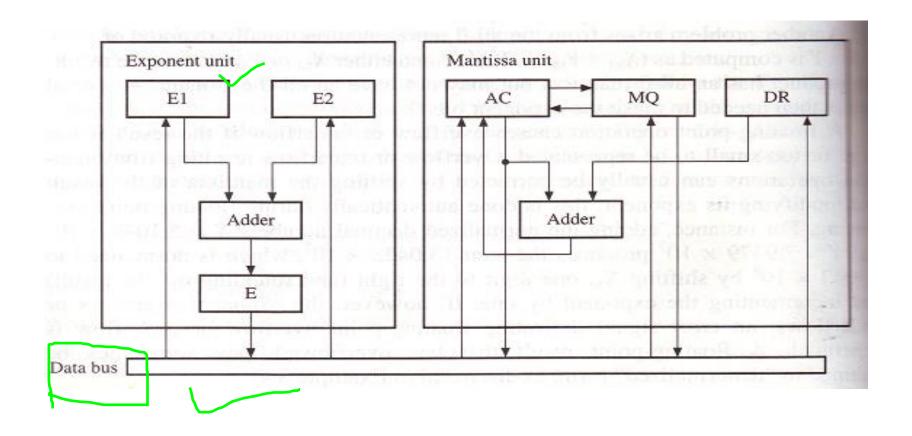
Difficulties

- 1. If biased exponents are added and subtracted using fixed-point arithmetic in the course of a floating-point calculation, the resulting exponent is doubly biased and must be corrected by subtracting the bias.
- 2. If $X \times Y$ is computed as $(X_M \times Y_M) \times 2^{XE+YE}$ and either X_M or Y_M is 0, the resulting product has an all-0 mantissa but may not have all-0 exponent. A special step is then needed to make the exponent bits 0.
- 3. Overflow and underflow may arise in both mantissa and exponents manipulation. Mantissa overflow and underflow can be adjusted by shifting exponent. If the exponents overflow or underflow then error must be detected.

Difficulties

4. We need two guard bits to preserve the accuracy of floating-point calculations: 1. for rounding purpose. 2. to maintain precision during normalization.

Floating-point Unit

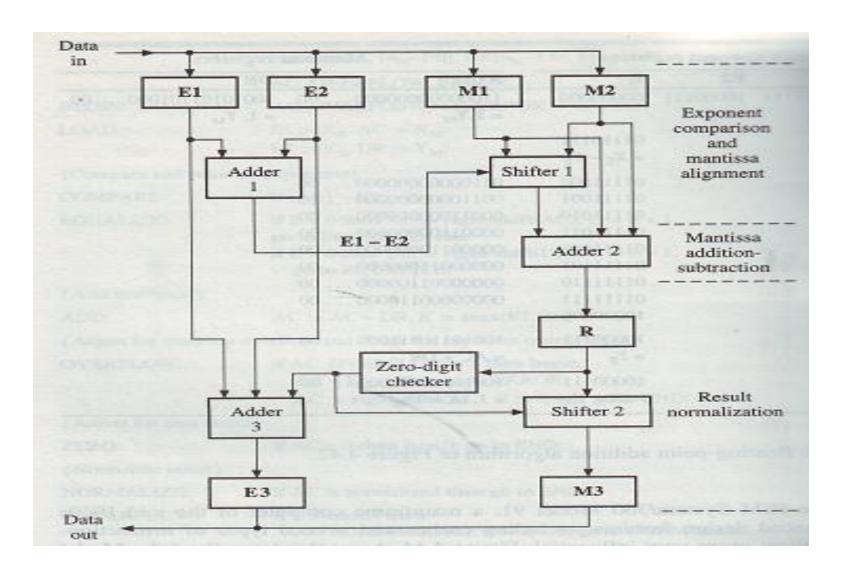


Algorithm for Floating-point Addition

```
register AC [n_M-1:0], DR[n_M-1:0], E[n_E-1:0], E[n_E-1:0], E[n_E-1:0], E[n_E-1:0]
                       AC_OVERFLOW, ERROR;
BEGIN
                        AC OVERFLOW := 0, ERROR := 0
LOAD:
                       E1 := X_E, AC := X_M;
                       E2 := Y_E, DR := Y_M;
(Compare and equalize exponents)
                       E := E1 - E2:
COMPARE:
EOUALIZE:
                       if E < 0 then AC := right-shift(AC), E := E + 1
                       go to EOUALIZE; else
                       if E > 0 then DR := right\text{-shift}(DR), E := E - 1
                          go to EQUALIZE:
(Add mantissas)
ADD:
                        AC := AC + DR, E := max(E1,E2);
{Adjust for mantissa overflow and check for exponent overflow}
OVERFLOW:
                       if AC_OVERFLOW = 1 then begin
                          if E = E_{MAX} then go to ERROR:
                          AC := right\text{-shift}(AC), E := E + 1, go to END; end
{Adjust for zero result}
ZERO:
                        if AC = 0 then E := 0, go to END;
{Normalize result}
NORMALIZE:
                        if AC is normalized then go to END;
                        if E > EMIN then
UNDERFLOW:
                          AC := left\text{-shift}(AC), E := E - 1, go to NORM
(Set error flag indicating overflow or underflow)
ERROR:
                        ERROR := 1;
END:
```

1909	Exponent registers			Mantissa registers	
	E1	E2	E	AC	DR
THE .	01111111	10000111	00000000	11000000000000 00	10010101101000 00
	$=X_{E}$	$=Y_{\rm E}$		$= 1.X_{\rm M}$	= 1. Y _M
MURARE			01110111		
			$= X_{\mathrm{E}} - Y_{\mathrm{E}}$		
INDIAL ZE			01111000	01100000000000 00	
			01111001	001100000000000 00	
			01111010	00011000000000 00	
			01111011	00001100000000 00	
			01111100	00000110000000 00	
	- 2		01111101	00000011000000 00	
			01111110	00000001100000 00	
			01111111	00000000110000 00	
			10000000		
400			10000111	10010110011000 00	
			$=Y_{\mathbb{E}}$	= AC + DR	
Tilmost:			10000111	10010110011000 00	
			$=(X+Y)_{\mathrm{E}}$	$= 1. (X + Y)_{M}$	

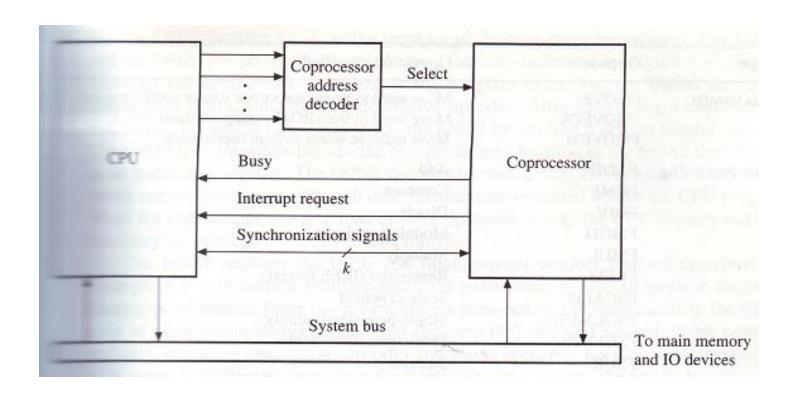
Floating Unit of IBM System



Coprocessor

- ✓ An auxiliary processors to provide fast, low-cost hardware implementation of some special functions.
- ✓ A separate instruction set processor that is closely coupled with to the CPU and whose instructions and registers are direct extensions of CPU.
- ✓ It requires specialized control logic to link the CPU with the coprocessor and to handle the instructions that are executed by the coprocessor.

Coprocessor



Coprocessor

- ✓ A coprocessor instruction contains three fields:
 - 1. Opcode
 - 2. The address of the particular coprocessor.
 - 3. Particular operation to be executed by the coprocessor.