1. Description

1.1. Project

Project Name	STM32F746I
Board Name	STM32F746I
Generated with:	STM32CubeMX 4.12.0
Date	12/17/2015

1.2. MCU

|	
MCU Series	STM32F7
MCU Line	STM32F7x6
MCU name	STM32F746IGTx
MCU Package	LQFP176
MCU Pin number	176

2. Pinout Configuration
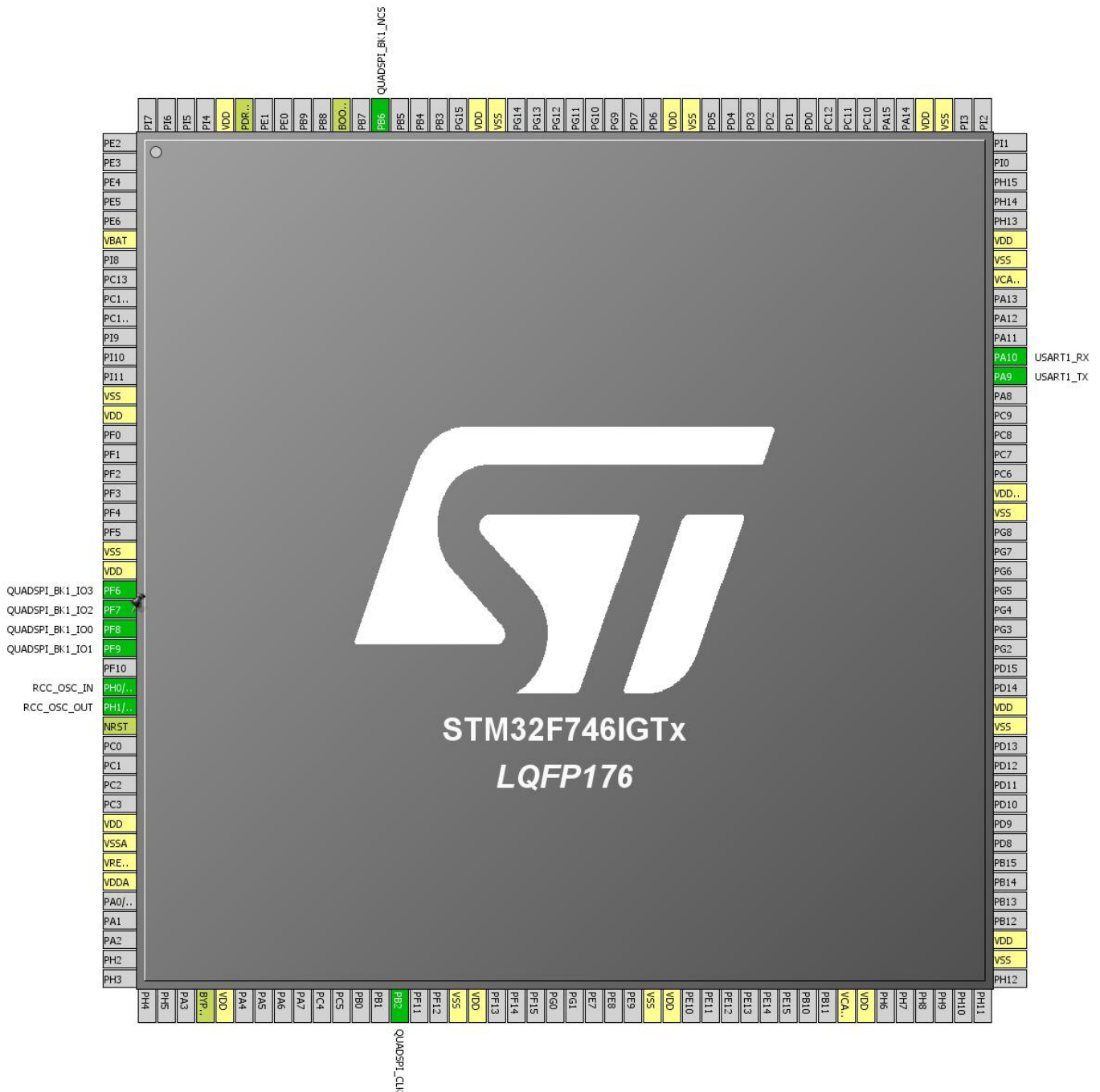
3. Pins Configuration

Pin Number LQFP176	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
14	VSS	Power		
15	VDD	Power		
22	VSS	Power		
23	VDD	Power		
24	PF6	I/O	QUADSPI_BK1_IO3	
25	PF7	I/O	QUADSPI_BK1_IO2	
26	PF8	I/O	QUADSPI_BK1_IO0	
27	PF9	I/O	QUADSPI_BK1_IO1	
29	PH0/OSC_IN	I/O	RCC_OSC_IN	
30	PH1/OSC_OUT	I/O	RCC_OSC_OUT	
31	NRST	Reset		
36	VDD	Power		
37	VSSA	Power		
38	VREF+	Power		
39	VDDA	Power		
48	BYPASS_REG	Reset		
49	VDD	Power		
58	PB2	I/O	QUADSPI_CLK	
61	VSS	Power		
62	VDD	Power		
71	VSS	Power		
72	VDD	Power		
81	VCAP_1	Power		
82	VDD	Power		
90	VSS	Power		
91	VDD	Power		
102	VSS	Power		
103	VDD	Power		
113	VSS	Power		
114	VDDUSB	Power		
120	PA9	I/O	USART1_TX	
121	PA10	I/O	USART1_RX	
125	VCAP_2	Power		
126	VSS	Power		
127	VDD	Power		

Pin Number LQFP176	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
135	VSS	Power		
136	VDD	Power		
148	VSS	Power		
149	VDD	Power		
158	VSS	Power		
159	VDD	Power		
164	PB6	I/O	QUADSPI_BK1_NCS	
166	BOOT0	Boot		
171	PDR_ON	Reset		
172	VDD	Power		

4. Clock Tree Configuration
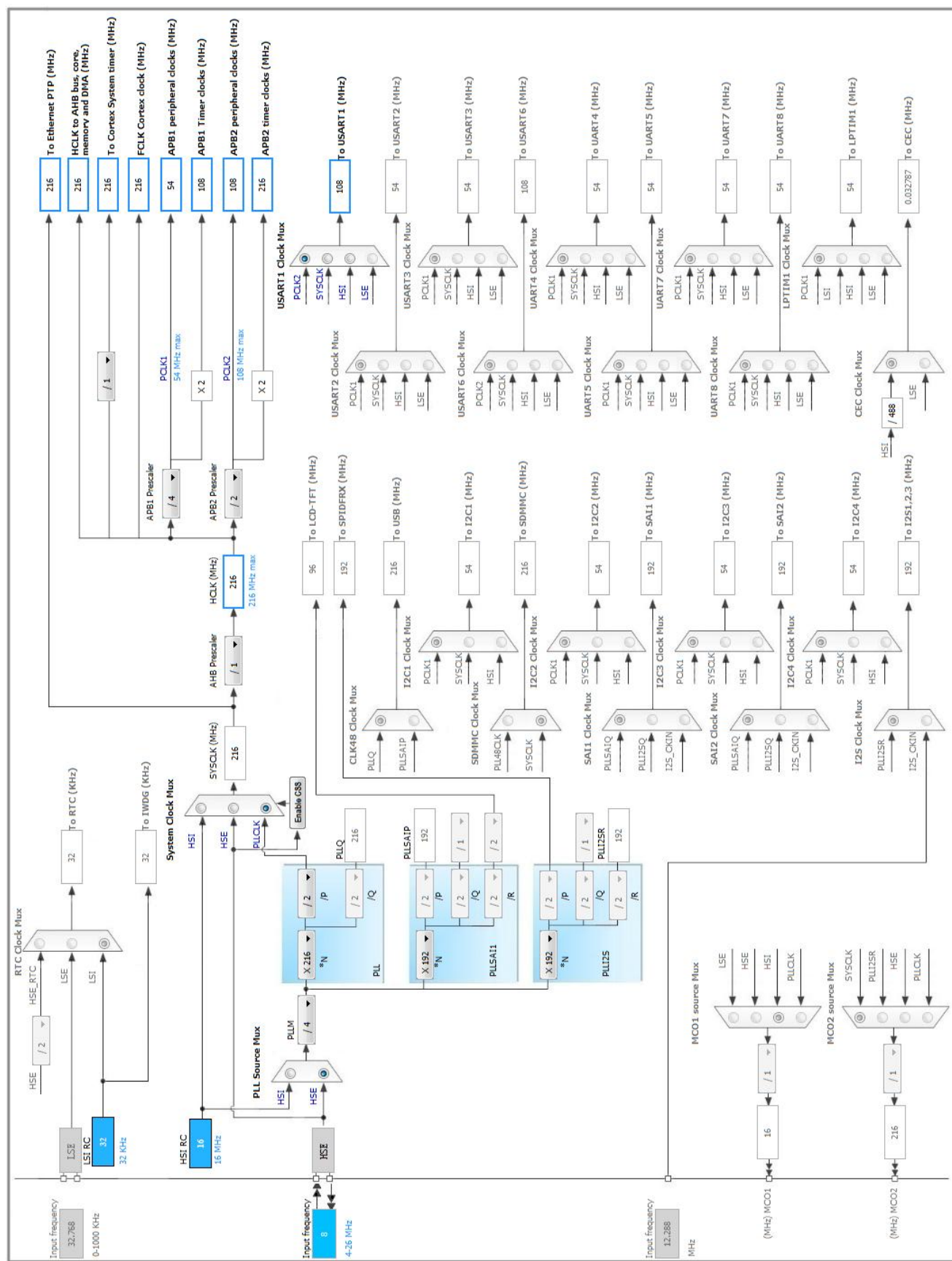
5. IPs and Middleware Configuration

5.1. QUADSPI

QuadSPI Mode: Bank1 with Quad SPI Lines

5.1.1. Parameter Settings:

General Parameters:

Clock Prescaler	**2 ***
Fifo Threshold	**4 ***
Sample Shifting	**Sample Shifting Half Cycle ***
Flash Size	**23 ***
Chip Select High Time	**2 Cycles ***
Clock Mode	Low

5.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

5.2.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Flash Latency(WS)	7 WS (8 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
TIM Prescaler Selection	Disabled

Power Parameters:

Power Over Drive	Enabled
Power Regulatror Voltage Scale	Power Regulator Voltage Scale 1

5.3. USART1

Mode: Asynchronous

5.3.1. Parameter Settings:

Basic Parameters:

Baud Rate	115200
Word Length	**8 Bits (including Parity) ***
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	Receive and Transmit
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

* User modified value

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
QUADSPI	PF6	QUADSPI_BK1_IO3	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PF7	QUADSPI_BK1_IO2	Alternate Function Push Pull	**Pull-up ***	**High ***	
	PF8	QUADSPI_BK1_IO0	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PF9	QUADSPI_BK1_IO1	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PB2	QUADSPI_CLK	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PB6	QUADSPI_BK1_NCS	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
RCC	PH0/OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1/OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
USART1	PA9	USART1_TX	Alternate Function Push Pull	Pull-up	**High ***	
	PA10	USART1_RX	Alternate Function Push Pull	Pull-up	**High ***	

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
System tick timer	true	0	0
Non maskable interrupt	unused		
Hard fault interrupt	unused		
Memory management fault	unused		
Pre-fetch fault, memory access fault	unused		
Undefined instruction or illegal state	unused		
Debug monitor	unused		
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USART1 global interrupt	unused		
QUADSPI global interrupt	unused		

* User modified value

7. Power Plugin report

7.1. Microcontroller Selection

Series	STM32F7
Line	STM32F7x6
MCU	STM32F746IGTx
Datasheet	027590_Rev1

7.2. Parameter Selection

Temperature	25
Vdd	3.3

8. Software Project

8.1. Project Settings

Name	Value
Project Name	STM32F746I
Project Folder	C:\Users\Administrator\Desktop\stm32cube\STM32F746I\31.QuadSPI
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F7 V1.3.0

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy all used libraries into the project folder
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No