

RR	A process		B process		C process		D process		Reschedule		
Tick	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	Running before	Running after	
1	60	1	60	0	60	0	60	0	A	A	
2	60	2	60	0	60	0	60	0	A	A	
3	60	3	60	0	60	0	60	0	A	A	
10	60	10	60	0	60	0	60	0	A	B	
11	60	10	60	1	60	0	60	0	B	B	
12	60	10	60	2	60	0	60	0	B	B	
20	60	10	60	10	60	0	60	0	B	C	
21	60	10	60	10	60	1	60	0	C	C	
22	60	10	60	10	60	2	60	0	C	C	
30	60	10	60	10	60	10	60	0	C	D	
31	60	10	60	10	60	10	60	1	D	D	
32	60	10	60	10	60	10	60	2	D	D	
50	60	20	60	10	60	10	60	10	A	B	
51	60	20	60	11	60	10	60	10	B	B	
52	60	20	60	12	60	10	60	10	B	B	
70	60	20	60	20	60	20	60	10	C	D	
71	60	20	60	20	60	20	60	11	D	D	
72	60	20	60	20	60	20	60	12	D	D	
100	60	30	60	30	60	20	60	20	B	C	
101	58	15	58	15	55	11	65	10	C	C	
102	58	15	58	15	55	12	65	10	C	C	
110	58	15	58	15	55	20	65	10	C	C	
200	58	15	58	15	55	110	65	10	C	C	

201	54	8	54	8	78	55	63	5 C	A
202	54	9	54	8	78	55	63	5 A	A

Tick	A process		B process		C process		D process		Reschedule	
	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	p_uspri	p_cpu	Running before	Running after
1	60	1	60	0	60	0	60	0	A	A
2	60	2	60	0	60	0	60	0	A	A
3	60	3	60	0	60	0	60	0	A	A
99	60	99	60	0	60	0	60	0	A	A
100	63	50	50	0	50	0	60	0	A	B
101	63	50	50	1	50	0	60	0	B	B
102	63	50	50	2	50	0	60	0	B	B
199	63	50	50	99	50	0	60	0	B	B
200	56	25	63	50	50	0	60	0	B	C
201	56	25	63	50	50	1	60	0	C	C