

# 实验4

## 个人信息

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        - 如果 ucore 的缺页服务例程在执行过程中访问内存, 出现了页访问异常, 请问硬件要做哪些事情?
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- 完成实验后，请分析ucore\_lab中提供的参考答案，并在实验报告中说明你的实现与参考答案的区别
- 列出你认为本实验中重要的知识点，以及与对应的OS原理中的知识点，并简要说明你对二者的含义，关系，差异等方面的理解（也可能出现实验中的知识点没有对应的原理知识点）
- 列出你认为OS原理中很重要，但在实验中没有对应上的知识点

## 实验名称

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实验4 虚拟内存管理

## 实验目的

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- 了解虚拟内存的 Page Fault 异常处理实现
- 了解页替换算法在操作系统中的实现

## 实验要求

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- 本次实验是在前一实验的基础上，借助于页表机制和 lab1 中涉及的中断异常处理机制，完成 **Page Fault 异常处理**和 **FIFO 页替换算法**的实现。
- 实验原理最大的区别是在设计了如何在磁盘上缓存内存页，从而能够支持虚存管理，提供一个比实际物理内存空间“更大”的虚拟内存空间给系统使用。
- 这个实验与实际操作系统中的实现比较起来要简单，不过需要了解 UCORE 实验一和实验二的具体实现。实际操作系统系统中的虚拟内存管理设计与实现是相当复杂的，涉及到与进程管理系统、文件系统等的交叉访问。

## 实验内容

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- 练习0：填写已有实验
- 练习1：给未被映射的地址映射上物理页（需要编程）
- 练习2：补充完成基于 **FIFO** 的页面替换算法（需要编程）

## 实验环境

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使用老师提供的 `mooc-os-2015.vdi`，在虚拟机中创建 64 位的 Ubuntu 虚拟机并加载该 vdi，获得了版本为：

```
Linux moocos-VirtualBox 3.13.0-24-generic #46-Ubuntu SMP Thu Apr 10 19:11:08 UTC 2014
x86_64 x86_64 x86_64 GNU/Linux
```

的虚拟机操作系统

并且使用 vscode 配合 Remote SSH 插件，实现通过远程终端在 windows 环境的对文件的编辑

# 实验过程

## 练习0：填写已有实验

本实验依赖 ucore 实验1/2。请把你做的 ucore 实验1/2的代码填入本实验中代码中有“LAB1”，“LAB2”的注释相应部分。

根据之前的实验，需要修改的地方为

- kern/debug/kdebug.c
  - print\_stackfram()
- kern/mm/default\_pmm.c
  - default\_init\_memmap()
  - default\_alloc\_pages()
  - default\_free\_pages()
- kern/mm/pmm.c
  - get\_pte()
  - page\_remove\_pte()
- kern/trap/trap.c
  - idt\_init()
  - trap\_dispatch()

详情见下图

```
labcodes > lab2 > kern > debug > C kdebug.c
298 * (2) call read_eip() to get the value of eip. the type is (u
299 * (3) from 0 .. STACKFRAME_DEPTH
300 * (3.1) printf value of ebp, eip
301 * (3.2) (uint32_t)calling arguments [0..4] = the contents
302 * (3.3) cprintf("\n");
303 * (3.4) call print_debuginfo(eip-1) to print the C calling
304 * (3.5) popup a calling stackframe
305 * NOTICE: the calling funciton's return addr eip =
306 * the calling funciton's ebp = ss:[ebp]
307 */
308
309 uint32_t ebp = read_ebp(); // 获取
310 uint32_t eip = read_eip(); // 获取
311
312 int i = 0;
313 for (i = 0; ebp != 0 && i < STACKFRAME_DEPTH; i++)
314 {
315     cprintf("ebp = 0x%08x\teip = 0x%08x\n", ebp, eip);
316     uint32_t *arguments = (uint32_t *) ebp + 2; // 拿到
317     cprintf("\targ0 = 0x%08x\targ1 = 0x%08x\targ2 = 0x%08x\targ3 = 0x%08x\targ4 = 0x%08x\n",
318           *(arguments), *(arguments + 1), *(arguments + 2), *(arguments + 3), *(arguments + 4));
319     print_debuginfo(eip - 1);
320     eip = ((uint32_t *)ebp)[1]; // 将 e
321     ebp = ((uint32_t *)ebp)[0]; // ebp
322 }
323
324 }
325
```

```
labcodes > lab3 > kern > debug > C kdebug.c
299 * (3) from 0 .. STACKFRAME_DEPTH
300 * (3.1) printf value of ebp, eip
301 * (3.2) (uint32_t)calling arguments [0..4] = the contents
302 * (3.3) cprintf("\n");
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304 * (3.5) popup a calling stackframe
305 * NOTICE: the calling funciton's return addr eip =
306 * the calling funciton's ebp = ss:[ebp]
307 */
308
309 uint32_t ebp = read_ebp(); // 获取
310 uint32_t eip = read_eip(); // 获取
311
312 int i = 0;
313 for (i = 0; ebp != 0 && i < STACKFRAME_DEPTH; i++)
314 {
315     cprintf("ebp = 0x%08x\teip = 0x%08x\n", ebp, eip);
316     uint32_t *arguments = (uint32_t *) ebp + 2; // 拿到
317     cprintf("\targ0 = 0x%08x\targ1 = 0x%08x\targ2 = 0x%08x\targ3 = 0x%08x\targ4 = 0x%08x\n",
318           *(arguments), *(arguments + 1), *(arguments + 2), *(arguments + 3), *(arguments + 4));
319     print_debuginfo(eip - 1);
320     eip = ((uint32_t *)ebp)[1]; // 将 e
321     ebp = ((uint32_t *)ebp)[0]; // ebp
322 }
323
324 }
325
```

```
labcodes > lab2 > kern > mm > C default_pmm.c
98 #define nr_free (free_area.nr_free)
99 #define nr_free (free_area.nr_free)
100
101 static void
102 default_init(void) {
103     list_init(&free_list);
104     nr_free = 0;
105 }
106
107 static void
108 default_init_memmap(struct Page *base, size_t n) {
109     assert(n > 0);
110     struct Page *p = base;
111     for (; p != base + n; p++) {
112         assert(PageReserved(p)); // 检查是否为保留页，是则中止
113         p->flags = p->property = 0;
114         SetPageProperty(p); // 设置为保留页
115         set_page_ref(p, 0);
116         list_add_before(&free_list, &(p->page_link)); // 加入空闲列
117     }
118     base->property = n; // 这是第一页，因此 property 需要设置为总页数
119     // SetPageProperty(base); // 在循环中已经设置好了
120     nr_free += n; // 空闲内存页的总数
121     // list_add(&free_list, &(base->page_link));
122 }
123
```

```
labcodes > lab3 > kern > mm > C default_pmm.c
98 #define nr_free (free_area.nr_free)
99 #define nr_free (free_area.nr_free)
100
101 static void
102 default_init(void) {
103     list_init(&free_list);
104     nr_free = 0;
105 }
106
107 static void
108 default_init_memmap(struct Page *base, size_t n) {
109     assert(n > 0);
110     struct Page *p = base;
111     for (; p != base + n; p++) {
112         assert(PageReserved(p)); // 检查是否为保留页，是则中止
113         p->flags = p->property = 0;
114         SetPageProperty(p); // 设置为保留页
115         set_page_ref(p, 0);
116         list_add_before(&free_list, &(p->page_link)); // 加入空闲列
117     }
118     base->property = n; // 这是第一页，因此 property 需要设置为总页数
119     // SetPageProperty(base); // 在循环中已经设置好了
120     nr_free += n; // 空闲内存页的总数
121     // list_add(&free_list, &(base->page_link));
122 }
123
```

```
labcodes > lab2 > kern > mm > C default_pmm.c
125 static struct Page *
126 > default_alloc_pages(size_t n) { ...
170 }
171 }

labcodes > lab3 > kern > mm > C default_pmm.c
125 static struct Page *
126 > default_alloc_pages(size_t n) { ...
170 }
171 }
172 static void
```

```
labcodes > lab2 > kern > mm > C default_pmm.c
171 static void
172 > default_free_pages(struct Page *base, size_t n) { ...
226 }
227 }
228 static size_t
```

```
labcodes > lab2 > kern > mm > C pmm.c
361 // 上面的东西没有动
362
363 pte_t *pdir = &pgdir[PDX(la)]; // 获取一级页表位置
364 if (!(*pdir & PTE_P)) // 如果不存在, 则需要判断是否需要创建二级
365 {
366     struct Page *p;
367     // 如果 create 为 0, 则返回 NULL
368     // 如果 create 不为 0, 则创建页表:
369     // 如果失败, 返回 NULL
370     // 如果创建成功, 返回对应二级页表
371     if (create == 0 || (p = alloc_page()) == NULL) // 利用短路
372         return NULL;
373     set_page_ref(p, 1); // 如果要查找该页表, 则增
374     uintptr_t pa = page2pa(p); // 得到物理地址
375     memset(KADDR(pa), 0, PGSIZE); // 但是没有找到对应的页表
376     // 将物理地址转化为虚拟地址
377     // 并且由于该页虚拟地址才
378     // *pdir = pa | PTE_P; // 设置存在
379     // *pdir = *pdir | PTE_U; // 设置可读
380     // *pdir = *pdir | PTE_W; // 设置可写
381     // *pdir = pa | PTE_P | PTE_U | PTE_W; // 统一设置更加方便
382     *pdir = pa | PTE_USER;
383 }
384
385 return &((pte_t *)KADDR(PDE_ADDR(*pdir)))[PTX(la)]; // 得到二级
386 }
387
388 // get_page - get related Page struct for linear address la using PD
389
```

```
labcodes > lab3 > kern > mm > C pmm.c
372 return NULL; // (8) return page table entry
373
374 #endif
375 // 上面的东西没有动
376
377 pte_t *pdir = &pgdir[PDX(la)]; // 获取一级页表位置
378 if (!(*pdir & PTE_P)) // 如果不存在, 则需要判断是否需要创建二级
379 {
380     struct Page *p;
381     // 如果 create 为 0, 则返回 NULL
382     // 如果 create 不为 0, 则创建页表:
383     // 如果失败, 返回 NULL
384     // 如果创建成功, 返回对应二级页表
385     if (create == 0 || (p = alloc_page()) == NULL) // 利用短路
386         return NULL;
387     set_page_ref(p, 1); // 如果要查找该页表, 则增
388     uintptr_t pa = page2pa(p); // 得到物理地址
389     memset(KADDR(pa), 0, PGSIZE); // 但是没有找到对应的页表
390     // 将物理地址转化为虚拟地址
391     // 并且由于该页虚拟地址才
392     // *pdir = pa | PTE_P; // 设置存在
393     // *pdir = *pdir | PTE_U; // 设置可读
394     // *pdir = *pdir | PTE_W; // 设置可写
395     // *pdir = pa | PTE_P | PTE_U | PTE_W; // 统一设置更加方便
396     *pdir = pa | PTE_USER;
397 }
398
399 return &((pte_t *)KADDR(PDE_ADDR(*pdir)))[PTX(la)]; // 得到二级
400
```

```
labcodes > lab2 > kern > mm > C pmm.c
428 // (5) clear second page table entry
429 // (6) flush tlb
430 }
431 #endif
432
433 if ((*ptep & PTE_P) // 只有存在的时候才会后续
434 {
435     struct Page *p = pte2page(*ptep); // 获取到页
436     if (page_ref_dec(p) == 0) // 减少引用次数, 同时判断次数是否为
437         free_page(p); // 如果只被上一级页表引用一次, 那么可以直接
438     // 如果引用超过1次, 那么不能释放页表, 但是可以取消二级页表的映
439     // 无论是是否释放页, 都要取消二级页表的映射
440
441     *ptep = 0;
442     tlb_invalidate(pgdir, la);
443 }
444
445 // 如果不存在, 什么都不干
446 return;
447 }
```

```
labcodes > lab3 > kern > mm > C pmm.c
444 #endif
445
446 if ((*ptep & PTE_P) // 只有存在的时候才会后续
447 {
448     struct Page *p = pte2page(*ptep); // 获取到页
449     if (page_ref_dec(p) == 0) // 减少引用次数, 同时判断次数是否为
450         free_page(p); // 如果只被上一级页表引用一次, 那么可以直接
451     // 如果引用超过1次, 那么不能释放页表, 但是可以取消二级页表的映
452     // 无论是是否释放页, 都要取消二级页表的映射
453
454     *ptep = 0;
455     tlb_invalidate(pgdir, la);
456 }
457
458 // 如果不存在, 什么都不干
459 return;
460 }
461
462 // page_remove - free an Page which is related linear address la and
463 void
```

```
labcodes > lab2 > kern > trap > C trap.c
32 };
33
34 /* idt_init - initialize IDT to each of the entry points in kern/tr
35 void
36 idt_init(void) {
37     /* LAB1 YOUR CODE : STEP 2 */
38     /* (1) Where are the entry addrs of each Interrupt Service Rou
39     * All ISR's entry addrs are stored in __vectors, where is
40     * __vectors[] is in kern/trap/vector.S which is produced
41     * (try "make" command in lab1, then you will find vector.
42     * You can use "extern uintptr_t __vectors[];" to define
43     * (2) Now you should setup the entries of ISR in Interrupt De
44     * Can you see idt[256] in this file? Yes, it's IDT! you c
45     * (3) After setup the contents of IDT, you will let CPU know
46     * You don't know the meaning of this instruction? just go
47     * Notice: the argument of lidt is idt_pd. try to find it!
48     */
49     extern uintptr_t __vectors[];
50     int i = 0;
51     for (i = 0; i < 256; i++)
52     {
53         SETGATE(idt[i], 0, GD_KTEXT, __vectors[i], DPL_KERNEL);
54     }
55     SETGATE(idt[T_SWITCH_TOK], 0, GD_KTEXT, __vectors[T_SWITCH_TOK],
56     lidt(&idt_pd);
57 }
58 }
```

```
labcodes > lab3 > kern > trap > C trap.c
34 };
35
36 /* idt_init - initialize IDT to each of the entry points in kern/tr
37 void
38 idt_init(void) {
39     /* LAB1 YOUR CODE : STEP 2 */
40     /* (1) Where are the entry addrs of each Interrupt Service Rou
41     * All ISR's entry addrs are stored in __vectors, where is
42     * __vectors[] is in kern/trap/vector.S which is produced
43     * (try "make" command in lab1, then you will find vector.
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45     * (2) Now you should setup the entries of ISR in Interrupt De
46     * Can you see idt[256] in this file? Yes, it's IDT! you c
47     * (3) After setup the contents of IDT, you will let CPU know
48     * You don't know the meaning of this instruction? just go
49     * Notice: the argument of lidt is idt_pd. try to find it!
50     */
51     extern uintptr_t __vectors[];
52     int i = 0;
53     for (i = 0; i < 256; i++)
54     {
55         SETGATE(idt[i], 0, GD_KTEXT, __vectors[i], DPL_KERNEL);
56     }
57     SETGATE(idt[T_SWITCH_TOK], 0, GD_KTEXT, __vectors[T_SWITCH_TOK],
58     lidt(&idt_pd);
59 }
60 }
```

```
labcodes > lab2 > kern > trap > C trap.c
148 static void
149 trap_dispatch(struct trapframe *tf) {
150     char c;
151
152     switch (tf->tf_trapno) {
153     case IRQ_OFFSET + IRQ_TIMER:
154         /* LAB1 YOUR CODE : STEP 3 */
155         /* handle the timer interrupt */
156         /* (1) After a timer interrupt, you should record this even
157          * (2) Every TICK_NUM cycle, you can print some info using
158          * (3) Too Simple? Yes, I think so!
159         */
160         {
161             ticks++;
162             if (ticks == TICK_NUM)
163             {
164                 ticks = 0;
165                 print_ticks();
166             }
167         }
168         break;
169     case IRQ_OFFSET + IRQ_COM1:
170         c = cons_getc();
171         cprintf("serial [%03d] %c\n", c, c);
172         break;
173     }
174 }

labcodes > lab3 > kern > trap > C trap.c
183 if ((ret = pgfault_handler(tf)) != 0) {
184     print_trapframe(tf);
185     panic("handle pgfault failed. %e\n", ret);
186 }
187 break;
188 case IRQ_OFFSET + IRQ_TIMER:
189 #if 0
190     LAB3 : If some page replacement algorithm(such as CLOCK PRA) ne
191     then you can add code here.
192 #endif
193 /* LAB1 YOUR CODE : STEP 3 */
194 /* handle the timer interrupt */
195 /* (1) After a timer interrupt, you should record this even
196  * (2) Every TICK_NUM cycle, you can print some info using
197  * (3) Too Simple? Yes, I think so!
198  */
199 {
200     ticks++;
201     if (ticks == TICK_NUM)
202     {
203         ticks = 0;
204         print_ticks();
205     }
206 }
207 break;
208 case IRQ_OFFSET + IRQ_COM1:
```

进行测试，出现如下显示

## DEBUG CONSOLE

## PROBLEMS

## OUTPUT

## TERMINAL

p

### 练习1：给未被映射的地址映射上物理页



- 完成 `do_pgfault` (`mm/vmm.c`) 函数，给未被映射的地址映射上物理页。设置访问权限的时候需要参考页面所在 VMA 的权限，同时需要注意映射物理页时需要操作内存控制结构所指定的页表，而不是内核的页表。注意：在 LAB3 EXERCISE 1 处填写代码。执行 `make qemu` 后，如果通过 `check_pgfault` 函数的测试后，会有 `check_pgfault() succeeded!` 的输出，表示练习1基本正确。
- 请在实验报告中简要说明你的设计实现过程。请回答如下问题：
  - 请描述页目录项（Page Directory Entry）和页表项（Page Table Entry）中组成部分对 ucore 实现页替换算法的潜在用处。
  - 如果 ucore 的缺页服务例程在执行过程中访问内存，出现了页访问异常，请问硬件要做哪些事情？

## 查看 vmm.c 开头的注释

查看 `kern/mm/vmm.c` 开头的注释：

```
/*
 * vmm design include two parts: mm_struct (mm) & vma_struct (vma)
 * mm is the memory manager for the set of continuous virtual memory
 * area which have the same PDT. vma is a continuous virtual memory area.
 * There a linear link list for vma & a redblack link list for vma in mm.
 *
 * -----
 * mm related functions:
 * global functions
 *   struct mm_struct * mm_create(void)
 *   void mm_destroy(struct mm_struct *mm)
 *   int do_pgfault(struct mm_struct *mm, uint32_t error_code, uintptr_t addr)
 *
 * -----
 * vma related functions:
 * global functions
 *   struct vma_struct * vma_create (uintptr_t vm_start, uintptr_t vm_end,...)
 *   void insert_vma_struct(struct mm_struct *mm, struct vma_struct *vma)
 *   struct vma_struct * find_vma(struct mm_struct *mm, uintptr_t addr)
 * local functions
 *   inline void check_vma_overlap(struct vma_struct *prev, struct vma_struct *next)
 *
 * -----
 * check correctness functions
 *   void check_vmm(void);
 *   void check_vma_struct(void);
 *   void check_pgfault(void);
 */
```

注释中介绍了两种结构体：

- `mm` 是内存管理器，用于管理使用相同 pdt 的连续虚拟内存空间的集合，相关函数为：
  - 全局函数
    - `mm_create()` 用于创建 mm
    - `mm_destroy()` 用于销毁 mm
    - `do_pgfault()` 用于处理页错误
- `vma` 则是连续虚拟内存空间，相关函数为：
  - 全局函数
    - `vma_create()` 用于创建 vma
    - `insert_vma_struct(mm, vma)` 用于将 vma 插入到 mm 的列表连接中
    - `find_vma(mm, addr)` 用于在 mm 中搜寻 vma (`vma->vm_start <= addr <= vma->vm_end`)

- 局部函数
  - `check_vma_overlap(vma1, vma2)` 用于判断 `vma1` 是否交叠 `vma2`
- 其他校验函数：
  - `check_vmm()` 用于检查 `vmm` 的正确性
  - `check_vma_struct()` 用于检查 `vma` 的正确性
  - `check_pgfault()` 用于检查 `pgfault_handler` 的正确性

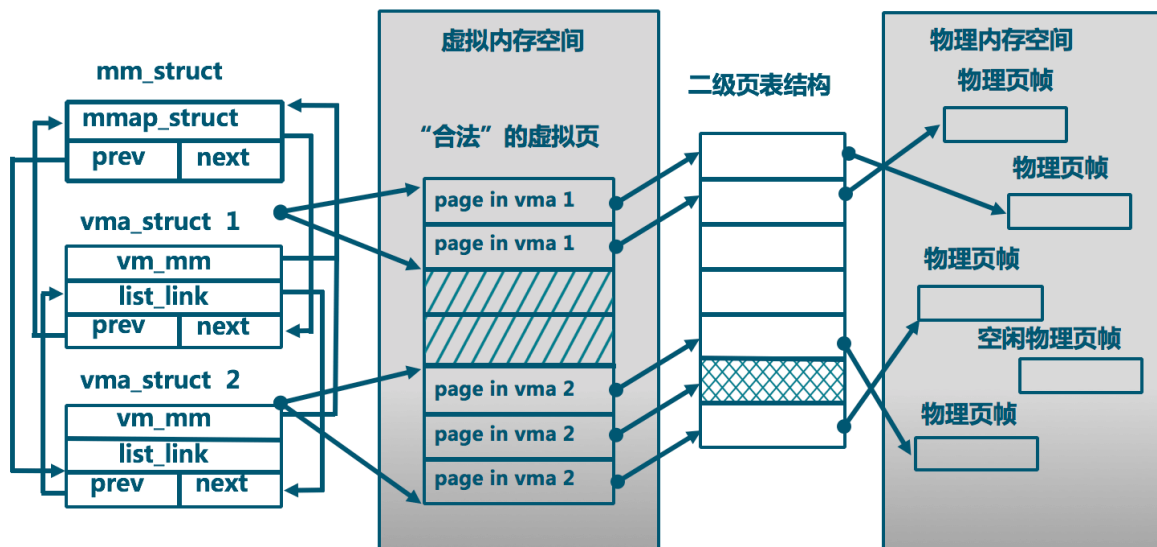
分别查看上述结构体的组成和函数的使用方法

## 查看 `do_pgfault()` 函数的介绍

查看 `kern/mm/vmm.c` 中 `do_pgfault()` 函数的注释：

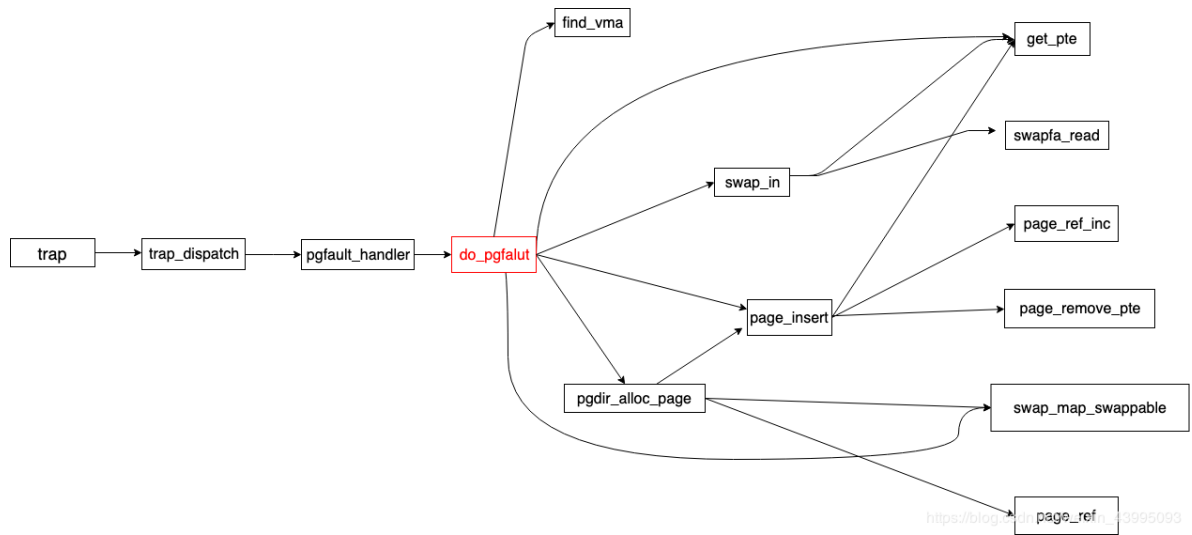
```
/* do_pgfault - 用于处理页错误的中断处理程序
 * @mm          : 用于控制使用相同 pdt 的 vma 的结构体
 * @error_code   : 错误代码，由 x86 硬件设置，被记录在 trapframe->tf_err 中
 * @addr        : 导致内存访问异常的地址，是 CR2 寄存器中的内容
 *
 * 调用关系图（具体见下图）： trap--> trap_dispatch-->pgfault_handler-->do_pgfault
 * 处理器为ucore的do_pgfault函数提供两项信息，以帮助诊断异常并从中恢复。
 *   (1) CR2 寄存器的内容。处理器通过加载 CR2 寄存器获取32位线性地址，该地址会由于异常而更新
 *   do_pgfault() 函数能够通过这个地址定位到相关的页目录和页表条目
 *   (2) 内核堆栈上的错误代码/页错误的错误代码和其他异常的格式不同
 *   错误代码告诉异常处理程序三件事情：
 *   -- The P flag (bit 0) 页面不存在 (0) | 访问权限冲突或保留位的使用 (1)
 *   -- The W/R flag (bit 1) 导致异常内存访问的操作是读 (0) | 写 (1)
 *   -- The U/S flag (bit 2) 发生异常时，异常处理程序是在用户态 (1) 还是特权级 (0) 执行
 */
```

查阅资料，了解关于虚拟地址及其空间和物理地址之间的关系：



以及 `do_pgfault()` 函数的调用关系：





## 查看 do\_pgfault() 函数中练习1的相关注释

查看相关注释：

```

/*LAB3 EXERCISE 1: YOUR CODE
 * Maybe you want help comment, BELOW comments can help you finish the code
 *
 * Some Useful MACROs and DEFINES, you can use them in below implementation.
 * MACROs or Functions:
 *   get_pte : 如果在包含该 pte 的 pt 不存在, 那么获取一个 pte 并且, 为 la 返回该 pte 的内核虚拟
地址, 并为 pt 分配一个页 (需要注意第三个参数为 '1')
 *   pgdir_alloc_page : 调用 alloc_page() 和 page_insert() 函数分别进行:
 *                       (1) 分配一页大小的内存
 *                       (2) 设置有线性地址 la 和 pdt pgdir 的地址映射 pa <---> la
 * DEFINES:
 *   VM_WRITE : 如果 vma->vm_flags & VM_WRITE == 1/0, 代表该 vma 是可写/不可写
 *   PTE_W     0x002 // 可写
 *   PTE_U     0x004 // 用户可用
 * VARIABLES:
 *   mm->pgdir : vma 的 pdt
 *
 */

```

```

#if 0
/*LAB3 EXERCISE 1: YOUR CODE*/
pte_t ptep = ??? // (1) try to find a pte, if pte's PT(Page Table) isn't
existed, then create a PT.
if (*ptep == 0) {
    // (2) if the phy addr isn't exist, then alloc a page & map
the phy addr with logical addr
}
else {
    if(swap_init_ok) {
        struct Page *page=NULL;
        // (1) According to the mm AND addr, try to load
the content of right disk page
        // into the memory which page managed.
        // (2) According to the mm, addr AND page, setup
the map of phy addr <---> logical addr
        // (3) make the page swappable.
    }
}

```

```

        else {
            cprintf("no swap_init_ok but ptep is %x, failed\n", *ptep);
            goto failed;
        }
    }
}
#endif

```

因此，具体步骤为：

1. 尝试寻找 pte，如果 pte 的 pt 不存在，那么创建一个 pt
2. 如果 pa（物理地址）不存在，那么分配一个页，并且将 **逻辑地址** 和 pa 映射起来

## 完成 do\_pgfault() 函数

修改 do\_pgfault() 函数的代码为：

```

int
do_pgfault(struct mm_struct *mm, uint32_t error_code, uintptr_t addr) {
    int ret = -E_INVAL;
    //try to find a vma which include addr
    struct vma_struct *vma = find_vma(mm, addr);

    pgfault_num++;
    //If the addr is in the range of a mm's vma?
    if (vma == NULL || vma->vm_start > addr) {
        cprintf("not valid addr %x, and can not find it in vma\n", addr);
        goto failed;
    }
    //check the error_code
    switch (error_code & 3) {
    default:
        /* error code flag : default is 3 ( W/R=1, P=1): write, present */
    case 2: /* error code flag : (W/R=1, P=0): write, not present */
        if (!(vma->vm_flags & VM_WRITE)) {
            cprintf("do_pgfault failed: error code flag = write AND not present, but
the addr's vma cannot write\n");
            goto failed;
        }
        break;
    case 1: /* error code flag : (W/R=0, P=1): read, present */
        cprintf("do_pgfault failed: error code flag = read AND present\n");
        goto failed;
    case 0: /* error code flag : (W/R=0, P=0): read, not present */
        if (!(vma->vm_flags & (VM_READ | VM_EXEC))) {
            cprintf("do_pgfault failed: error code flag = read AND not present, but
the addr's vma cannot read or exec\n");
            goto failed;
        }
    }

    /* IF (write an existed addr ) OR
    *   (write an non_existed addr && addr is writable) OR
    *   (read an non_existed addr && addr is readable)
    * THEN
    *   continue process
    */
    uint32_t perm = PTE_U;
    if (vma->vm_flags & VM_WRITE) {
        perm |= PTE_W;
    }
}

```

```

    }
    addr = ROUNDDOWN(addr, PGSIZE);

    ret = -E_NO_MEM;

    pte_t *ptep=NULL;
#if 0
    /*LAB3 EXERCISE 1: YOUR CODE*/
    ptep = ???          //(1) try to find a pte, if pte's PT(Page Table) isn't
    existed, then create a PT.
    if (*ptep == 0) {
        //(2) if the phy addr isn't exist, then alloc a page & map
        the phy addr with logical addr

    }
    else {
        /*LAB3 EXERCISE 2: YOUR CODE
        * Now we think this pte is a swap entry, we should load data from disk to a page
        with phy addr,
        * and map the phy addr with logical addr, trigger swap manager to record the
        access situation of this page.
        *
        * Some Useful MACROs and DEFINES, you can use them in below implementation.
        * MACROs or Functions:
        * swap_in(mm, addr, &page) : alloc a memory page, then according to the swap
        entry in PTE for addr,
        *
        find the addr of disk page, read the content of
        disk page into this memroy page
        * page_insert : build the map of phy addr of an Page with the linear addr la
        * swap_map_swappable : set the page swappable
        */
        if(swap_init_ok) {
            struct Page *page=NULL;
            //(1) According to the mm AND addr, try to load
            the content of right disk page
            // into the memory which page managed.
            //(2) According to the mm, addr AND page, setup
            the map of phy addr <---> logical addr
            //(3) make the page swappable.

        }
        else {
            cprintf("no swap_init_ok but ptep is %x, failed\n",*ptep);
            goto failed;
        }
    }
#endif
    // 以下是 练习1 代码
    ptep = get_pte(mm->pgdir, addr, 1); // 获取 ptep
    if (ptep == NULL) // 如果 ptep 的 pt 不存在
    {
        cprintf("ptep isn't existed, get_pte() in do_pafault() failed\n");
        goto failed;
    }

    if (*ptep == 0) // 如果 pa 不存在
    {
        if(pgdir_alloc_page(mm->pgdir, addr, perm) == NULL) // 尝试分配页并且映射
        {

```

```

        cprintf("pa isn't existed, and alloc page failed in do_pgfault()
failed\n");
        goto failed;
    }
}

ret = 0;
failed:
    return ret;
}

```

使用 `make` 生成文件后, 运行 `make qemu-nox`, 得到结果如下:

具体结果如下:

```

memory management: default_pmm_manager
e820map:
    memory: 0009fc00, [00000000, 0009fbff], type = 1.
    memory: 00000400, [0009fc00, 0009ffff], type = 2.
    memory: 00010000, [000f0000, 000fffff], type = 2.
    memory: 07efe000, [00100000, 07ffdfdf], type = 1.
    memory: 00002000, [07ffe000, 07ffffff], type = 2.
    memory: 00040000, [fffc0000, ffffffff], type = 2.
check_alloc_page() succeeded!
check_pgdir() succeeded!
check_boot_pgdir() succeeded!
----- BEGIN -----
PDE(0e0) c0000000-f8000000 38000000 urw
|-- PTE(38000) c0000000-f8000000 38000000 -rw
PDE(001) fac00000-fb000000 00400000 -rw
|-- PTE(000e0) faf00000-fafe0000 000e0000 urw
|-- PTE(00001) fafeb000-fafec000 00001000 -rw
----- END -----
check_vma_struct() succeeded!
page fault at 0x00000100: K/W [no page found].
check_pgfault() succeeded!
check_vmm() succeeded.
ide 0:    10000(sectors), 'QEMU HARDDISK'.
ide 1:    262144(sectors), 'QEMU HARDDISK'.
SWAP: manager = fifo swap manager
BEGIN check_swap: count 31995, total 31995
setup Page Table for vaddr 0X1000, so alloc a page
setup Page Table vaddr 0~4MB OVER!
set up init env for check_swap begin!
page fault at 0x00001000: K/W [no page found].
page fault at 0x00002000: K/W [no page found].
page fault at 0x00003000: K/W [no page found].
page fault at 0x00004000: K/W [no page found].
set up init env for check_swap over!
write Virt Page c in fifo_check_swap
write Virt Page a in fifo_check_swap
write Virt Page d in fifo_check_swap
write Virt Page b in fifo_check_swap
write Virt Page e in fifo_check_swap
page fault at 0x00005000: K/W [no page found].
page fault at 0x000000ae: K/R [no page found].
not valid addr ae, and can not find it in vma

```

完整结果如下:

由于图片过长, 在生成 pdf 时会被截断; 在第二个红框处能够得到正确的结果

```

mijialong$>make
+ cc kern/mm/vmm.c
+ ld bin/kernel
10000+0 records in
10000+0 records out
512000 bytes (5.1 MB) copied, 0.034659 s, 148 MB/s
1+0 records in
1+0 records out
512 bytes (512 B) copied, 0.000159231 s, 3.2 MB/s
308+1 records in
308+1 records out
158124 bytes (158 kB) copied, 0.00161199 s, 98.1 MB/s
mijialong$>make qemu-nox
(THU.CST) os is loading ...

```

Special kernel symbols:

```

entry 0xc0100036 (phys)
etext 0xc0108aca (phys)
edata 0xc0122000 (phys)
end    0xc0123130 (phys)

```

Kernel executable memory footprint: 141KB

```

ebp = 0xc011ef38      eip = 0xc01009ec
      arg0 = 0x00010094      arg1 = 0x00000000
      arg2 = 0xc011ef68      arg3 = 0xc01000d7
      kern/debug/kdebug.c:310: print_stackframe+22
ebp = 0xc011ef48      eip = 0xc0100cdf
      arg0 = 0x00000000      arg1 = 0x00000000
      arg2 = 0x00000000      arg3 = 0xc011efb8
      kern/debug/kmonitor.c:129: mon_backtrace+10
ebp = 0xc011ef68      eip = 0xc01000d7
      arg0 = 0x00000000      arg1 = 0xc011ef90
      arg2 = 0xffff0000      arg3 = 0xc011ef94
      kern/init/init.c:57: grade_backtrace2+33
ebp = 0xc011ef88      eip = 0xc0100100
      arg0 = 0x00000000      arg1 = 0xffff0000
      arg2 = 0xc011efb4      arg3 = 0x0000002a
      kern/init/init.c:62: grade_backtrace1+38
ebp = 0xc011efa8      eip = 0xc010011e
      arg0 = 0x00000000      arg1 = 0xc0100036
      arg2 = 0xffff0000      arg3 = 0x0000001d
      kern/init/init.c:67: grade_backtrace0+23
ebp = 0xc011efc8      eip = 0xc0100143
      arg0 = 0xc0108afc      arg1 = 0xc0108ae0
      arg2 = 0x00001130      arg3 = 0x00000000
      kern/init/init.c:72: grade_backtrace+34
ebp = 0xc011eff8      eip = 0xc010008b
      arg0 = 0xc0108cc0      arg1 = 0xc0108cc8
      arg2 = 0xc0100c65      arg3 = 0xc0108ce7
      kern/init/init.c:32: kern_init+84

```

memory management: default\_pmm\_manager

e820map:

```

memory: 0009fc00, [00000000, 0009fbff], type = 1.
memory: 00000400, [0009fc00, 0009ffff], type = 2.
memory: 00010000, [000f0000, 000fffff], type = 2.
memory: 07efe000, [00100000, 07ffdfff], type = 1.
memory: 00002000, [07ffe000, 07ffffff], type = 2.
memory: 00040000, [ffff0000, ffffffff], type = 2.

```

```

check_alloc_page() succeeded!
check_pgdir() succeeded!
check_boot_pgdir() succeeded!

```

```

----- BEGIN -----
PDE(0e0) c0000000-f8000000 38000000 urw
|-- PTE(38000) c0000000-f8000000 38000000 -rw
PDE(001) fac00000-fb000000 00400000 -rw

```

```
|-- PTE(000e0) faf00000-fafe0000 000e0000 urw
|-- PTE(00001) fafeb000-fafec000 00001000 -rw
```

END

```
check_vma_struct() succeeded!
page fault at 0x00000100: K/W [no page found].
check_pgfault() succeeded!
check_vmm() succeeded.
ide 0:      10000(sectors), 'QEMU HARDDISK'.
ide 1:      262144(sectors), 'QEMU HARDDISK'.
SWAP: manager = fifo swap manager
BEGIN check_swap: count 31995, total 31995
setup Page Table for vaddr 0x1000, so alloc a page
setup Page Table vaddr 0~4MB OVER!
set up init env for check_swap begin!
page fault at 0x00001000: K/W [no page found].
page fault at 0x00002000: K/W [no page found].
page fault at 0x00003000: K/W [no page found].
page fault at 0x00004000: K/W [no page found].
set up init env for check_swap over!
write Virt Page c in fifo_check_swap
write Virt Page a in fifo_check_swap
write Virt Page d in fifo_check_swap
write Virt Page b in fifo_check_swap
write Virt Page e in fifo_check_swap
page fault at 0x00005000: K/W [no page found].
page fault at 0x000000ae: K/R [no page found].
not valid addr ae, and can not find it in vma
trapframe at 0xc011ecf4
edi 0x00000001
esi 0x00000000
ebp 0xc011ed70
oesp 0xc011ed14
ebx 0x00007cfb
edx 0xc0303000
ecx 0x00005000
eax 0x00000092
ds 0x----0010
es 0x----0010
fs 0x----0023
gs 0x----0023
trap 0x0000000e Page Fault
err 0x00000000
eip 0xc0106199
cs 0x----0008
flag 0x00000046 PF_ZF_T0PI=0
```

```
kernel panic at kern/trap/trap.c:185:
handle pgfault failed. invalid parameter
```

```
stack traceback:
ebp = 0xc011ec68      eip = 0xc01009ec
    arg0 = 0xc0108df4      arg1 = 0xc011ecac
    arg2 = 0x000000b9      arg3 = 0xc00b80a0
    kern/debug/kdebug.c:310: print_stackframe+22
ebp = 0xc011ec98      eip = 0xc0100d50
    arg0 = 0xc010916e      arg1 = 0x000000b9
    arg2 = 0xc010917f      arg3 = 0xffffffff
    kern/debug/panic.c:27: __panic+105
ebp = 0xc011ecc8      eip = 0xc01026e0
    arg0 = 0xc011ecf4      arg1 = 0xc0101663
    arg2 = 0x00000000      arg3 = 0x00000007
    kern/trap/trap.c:185: trap_dispatch+150
ebp = 0xc011ece8      eip = 0xc01027c9
    arg0 = 0xc011ecf4      arg1 = 0x00000001
    arg2 = 0x00000000      arg3 = 0xc011ed70
    kern/trap/trap.c:242: trap+16
```



```

ebp = 0xc011ed70      eip = 0xc01027e1
    arg0 = 0xc0303000      arg1 = 0x00000001
    arg2 = 0x00000000      arg3 = 0xc011edec
    kern/trap/trapentry.S:24: <unknown>+0
ebp = 0xc011eda0      eip = 0xc0104735
    arg0 = 0x00000001      arg1 = 0x00000305
    arg2 = 0x00305000      arg3 = 0x00000f00
    kern/mm/pmm.c:171: alloc_pages+99
ebp = 0xc011edd0      eip = 0xc010510f
    arg0 = 0xc0120000      arg1 = 0x00005000
    arg2 = 0x00000006      arg3 = 0x00000057
    kern/mm/pmm.c:515: pgdir_alloc_page+17
ebp = 0xc011ee10      eip = 0xc0107cf1
    arg0 = 0xc0303000      arg1 = 0x00000002
    arg2 = 0x00005000      arg3 = 0x00000000
    kern/mm/vmm.c:409: do_pgfault+325
ebp = 0xc011ee40      eip = 0xc0102629
    arg0 = 0xc011ee9c      arg1 = 0xc0100315
    arg2 = 0x0000000a      arg3 = 0x00004000
    kern/trap/trap.c:167: pgfault_handler+63
ebp = 0xc011ee70      eip = 0xc01026a9
    arg0 = 0xc011ee9c      arg1 = 0xc0100352
    arg2 = 0xc0100304      arg3 = 0xc011ee9c
    kern/trap/trap.c:183: trap_dispatch+95
ebp = 0xc011ee90      eip = 0xc01027c9
    arg0 = 0xc011ee9c      arg1 = 0x00000001
    arg2 = 0x00000000      arg3 = 0xc011eef8
    kern/trap/trap.c:242: trap+16
ebp = 0xc011eef8      eip = 0xc01027e1
    arg0 = 0x00309000      arg1 = 0x00305000
    arg2 = 0xc011ef24      arg3 = 0x00000025
    kern/trap/trapentry.S:24: <unknown>+0
ebp = 0xc011ef18      eip = 0xc0106560
    arg0 = 0xc010a17c      arg1 = 0x00004000
    arg2 = 0x00000000      arg3 = 0x00000000
    kern/mm/swap.c:164: check_content_access+15
ebp = 0xc011ef98      eip = 0xc0106ae4
    arg0 = 0xc0109e33      arg1 = 0xc010a3a1
    arg2 = 0x74004021      arg3 = 0xc011ed84
    kern/mm/swap.c:254: check_swap+1403
ebp = 0xc011efc8      eip = 0xc01060c4
    arg0 = 0xc0108afc      arg1 = 0xc0108ae0
    arg2 = 0x00001130      arg3 = 0x00000000
    kern/mm/swap.c:48: swap_init+138
ebp = 0xc011eff8      eip = 0xc01000a9
    arg0 = 0xc0108cc0      arg1 = 0xc0108cc8
    arg2 = 0xc0100c65      arg3 = 0xc0108ce7
    kern/init/init.c:42: kern_init+114
Welcome to the kernel debug monitor!!
Type 'help' for a list of commands.
K> 

```

## 回答问题

请描述目录项（Page Directory Entry）和页表项（Page Table Entry）中组成部分对 ucore 实现页替换算法的潜在用处。

从实验3和 kern/mm/mmu.h 中可以得到

```

/* page table/directory entry flags */
#define PTE_P          0x001          // Present
#define PTE_W          0x002          // Writeable
#define PTE_U          0x004          // User
#define PTE_PWT        0x008          // Write-Through

```

```
#define PTE_PCD      0x010      // Cache-Disable
#define PTE_A        0x020      // Accessed
#define PTE_D        0x040      // Dirty
#define PTE_PS       0x080      // Page Size
#define PTE_MBZ      0x180      // Bits must be zero
#define PTE_AVAIL    0xE00      // Available for software use
                                // The PTE_AVAIL bits aren't used by
                                // the kernel or interpreted by the
                                // hardware, so user processes are
                                // allowed to set them arbitrarily.
#define PTE_USER      (PTE_U | PTE_W | PTE_P)
```

可以得到如下表格

名称	地址所在位	作用
PTE_P	0	存在位
PTE_W	1	可写
PTE_U	2	访问该页所需要的特权级
PTE_PWT	3	是否使用 write-through
PTE_PCD	4	是否使用缓存，1位不使用
PTE_A	5	是否被使用过
PTE_D	6	脏位
PTE_PS	7	页大小
PTE_MBZ	8	必须为0
PTE_AVAIL	9~11	内核和中断无效，用户可以设置

已知页替换的两个操作：**换入**和**换出**：

- 换入：将**虚拟地址对应的磁盘页内容**读取到**内存**中
- 换出：将**虚拟页的内容**写入到磁盘

根据上述操作的相关步骤，可以得到

- 页表项记录虚拟页在磁盘中的位置，在换入换出操作中提供**磁盘位置相关信息**
- 页目录可以用于**索引**对应的页表

并且在上述表格中，可以看到 **PTE\_AVAIL** 是保留的三位，这三位可供操作系统为页替换算法提供支持

如果 ucore 的缺页服务例程在执行过程中访问内存，出现了页访问异常，请问硬件要做哪些事情？

## 练习2：补充完成基于 FIFO 的页面替换算法

- 完成 vmm.c 中的 **do\_pgfault** 函数，并且在实现 FIFO 算法的 swap\_fifo.c 中完成 **map\_swappable** 和 **swap\_out\_victim** 函数。通过对 swap 的测试。注意：在 LAB3 EXERCISE 2 处填写代码。执行 **make qemu** 后，如果通过 check\_swap 函数的测试后，会有 **check\_swap() succeeded!** 的输出，表示练习2基本正确。
- 请在实验报告中回答如下问题：

- 如果要在 ucore 上实现“**extended clock页替换算法**”，现有的 swap\_manager 框架是否足以支持在 ucore 中实现此算法？
  - 如果是，请给你的设计方案。
  - 如果不是，请给出你的新的扩展和基此扩展的设计方案。
  - 并需要回答如下问题：
    - 需要被换出的页的特征是什么？
    - 在 ucore 中如何判断具有这样特征的页？
    - 何时进行换入和换出操作？

## 完善 do\_pgfault() 函数

查看相关的注释

```
#if 0
/*LAB3 EXERCISE 1: YOUR CODE*/
ptep = ???          //(1) try to find a pte, if pte's PT(Page Table) isn't
existed, then create a PT.
if (*ptep == 0) {
                    //(2) if the phy addr isn't exist, then alloc a page & map
the phy addr with logical addr

}
else {
/*LAB3 EXERCISE 2: YOUR CODE
* pte 现在是交换项
* 应该使用 pa 将数据从磁盘加载到页
* 使用逻辑地址映射 pa ，触发交换管理器来记录页面访问情况
*
* Some Useful MACROs and DEFINES, you can use them in below implementation.
* MACROs or Functions:
* swap_in(mm, addr, &page) : 分配内存页，然后根据 pte 中地址的交换项，找到磁盘页面的地
址，将磁盘页面的地址读入此内存页
* page_insert : 建立页中的 pa 和线性地址的映射
* swap_map_swappable : 使页可交换
*/
if(swap_init_ok) {
    struct Page *page=NULL;
                                //(1) 根据 mm 和 addr ，尝试从正确的磁盘页中加载内容到页
管理的内存
                                //(2) 根据 mm, addr 和 页，设置 pa <----> 逻辑地址的映
射
                                //(3) 使页可交换
}
else {
    cprintf("no swap_init_ok but ptep is %x, failed\n", *ptep);
    goto failed;
}
}
#endif
```

查看注释，可以得到相关步骤：

1. 根据 mm 和 addr ，尝试从正确的磁盘页中加载内容到页管理的内存
2. 根据 mm, addr 和 页，设置 pa <----> 逻辑地址 的映射
3. 使页可交换

需要注意的地方：

- 在设置映射时，需要设置**物理页权限**，用于保证和对应的**虚拟页权限**一致
- 在设置完页**可交换**之后，需要设置页对应的**虚拟地址**

完善 do\_pgfault() 函数：

```
int
do_pgfault(struct mm_struct *mm, uint32_t error_code, uintptr_t addr) {
    int ret = -E_INVAL;
    //try to find a vma which include addr
    struct vma_struct *vma = find_vma(mm, addr);

    pgfault_num++;
    //If the addr is in the range of a mm's vma?
    if (vma == NULL || vma->vm_start > addr) {
        cprintf("not valid addr %x, and can not find it in vma\n", addr);
        goto failed;
    }
    //check the error_code
    switch (error_code & 3) {
    default:
        /* error code flag : default is 3 ( W/R=1, P=1): write, present */
    case 2: /* error code flag : (W/R=1, P=0): write, not present */
        if (!(vma->vm_flags & VM_WRITE)) {
            cprintf("do_pgfault failed: error code flag = write AND not present, but
the addr's vma cannot write\n");
            goto failed;
        }
        break;
    case 1: /* error code flag : (W/R=0, P=1): read, present */
        cprintf("do_pgfault failed: error code flag = read AND present\n");
        goto failed;
    case 0: /* error code flag : (W/R=0, P=0): read, not present */
        if (!(vma->vm_flags & (VM_READ | VM_EXEC))) {
            cprintf("do_pgfault failed: error code flag = read AND not present, but
the addr's vma cannot read or exec\n");
            goto failed;
        }
    }
    uint32_t perm = PTE_U;
    if (vma->vm_flags & VM_WRITE) {
        perm |= PTE_W;
    }
    addr = ROUNDDOWN(addr, PGSIZE);

    ret = -E_NO_MEM;

    pte_t *ptep=NULL;
#if 0
    /*LAB3 EXERCISE 1: YOUR CODE*/
    ptep = ??? // (1) try to find a pte, if pte's PT(Page Table) isn't
existed, then create a PT.
    if (*ptep == 0) {
        // (2) if the phy addr isn't exist, then alloc a page & map
the phy addr with logical addr

```

```

    }
    else {
        if(swap_init_ok) {
            struct Page *page=NULL;

            //(1) According to the mm AND addr, try to load
the content of right disk page
            //    into the memory which page managed.
            //(2) According to the mm, addr AND page, setup
the map of phy addr <--> logical addr
            //(3) make the page swappable.

        }
        else {
            cprintf("no swap_init_ok but ptep is %x, failed\n",*ptep);
            goto failed;
        }
    }
#endif
    // 以下是 练习1 代码
    ptep = get_pte(mm->pgdir, addr, 1); // 获取 ptep
    if (ptep == NULL)                // 如果 ptep 的 pt 不存在
    {
        cprintf("ptep isn't existed, get_pte() in do_pafault() failed\n");
        goto failed;
    }

    if (*ptep == 0) // 如果 pa 不存在
    {
        if(pgdir_alloc_page(mm->pgdir, addr, perm) == NULL) // 尝试分配页并且映射
        {
            cprintf("pa isn't existed, and alloc page failed in do_pgfault()
failed\n");
            goto failed;
        }
    }

    // 以下是练习2代码
    else // 如果 pa 不为空, 即页表项非空, 尝试换入该页面
    {
        if (swap_init_ok) // 代表初始化成功
        {
            struct Page *page = NULL;
            ret = swap_in(mm, addr, &page); // 换页
            if (ret != 0)                // 如果换页失败
            {
                cprintf("swap_in in do_pgfault failed\n");
                goto failed;
            }

            page_insert(mm->pgdir, page, addr, perm); // 建立映射, 同时通过 perm 设置物
理页权限
            swap_map_swappable(mm, addr, page, 1); // 设置为可交换

            page->pra_vaddr = addr;                // 设置页对应的虚拟地址
        }
        else // 初始化失败
        {
            cprintf("no swap_init_ok but ptep is %x, failed\n",*ptep);
            goto failed;
        }
    }
}

```

```

    }
}

ret = 0;
failed:
    return ret;
}

```

上面的代码中，频繁用到了 `goto`，按照已经学习的知识，使用 `goto` 并不是一个十分优秀的选择，应该减少使用，但是在上述代码中，`goto` 语句切实增强了代码的可读性，方便了阅读；实际上，直接选择 `return ret` 可能会更加方便，不过损失了可读性

## 其他函数的实现

查看 `kern/mm/swap_fifo.c` 中的注释：

FIFO 算法：

[wikipedia]The simplest Page Replacement Algorithm(PRA) is a FIFO algorithm. The first-in, first-out page replacement algorithm is a low-overhead algorithm that requires little book-keeping on the part of the operating system. The idea is obvious from the name - the operating system keeps track of all the pages in memory in a queue, with the most recent arrival at the back, and the earliest arrival in front. When a page needs to be replaced, the page at the front of the queue (the oldest page) is selected. While FIFO is cheap and intuitive, it performs poorly in practical application. Thus, it is rarely used in its unmodified form. This algorithm experiences Belady's anomaly.

```

/* Details of FIFO PRA
 * (1) Prepare: In order to implement FIFO PRA, we should manage all swappable pages,
so we can
 *           link these pages into pra_list_head according the time order. At first
you should
 *           be familiar to the struct list in list.h. struct list is a simple
doubly linked list
 *           implementation. You should know howto USE: list_init,
list_add(list_add_after),
 *           list_add_before, list_del, list_next, list_prev. Another tricky method
is to transform
 *           a general list struct to a special struct (such as struct page). You
can find some MACRO:
 *           le2page (in memlayout.h), (in future labs: le2vma (in vmm.h), le2proc
(in proc.h),etc.
 */

```

需要了解的相关信息：

- 函数
  - `list_init()`
  - `list_add()`
  - `list_add_before`
  - `list_del()`
  - `list_next()`
  - `list_prev()`
  - `le2page()`
- 结构体



- Page
- list (list.h)

### 完成 \_fifo\_map\_swappable() 函数

原函数为：

```
/*
 * (3)_fifo_map_swappable: According FIFO PRA, we should link the most recent arrival
 page at the back of pra_list_head queueue
 */
static int
_fifo_map_swappable(struct mm_struct *mm, uintptr_t addr, struct Page *page, int
swap_in)
{
    list_entry_t *head=(list_entry_t*) mm->sm_priv;
    list_entry_t *entry=&(page->pra_page_link);

    assert(entry != NULL && head != NULL);
    //record the page access situlation
    /*LAB3 EXERCISE 2: YOUR CODE*/
    //(1)link the most recent arrival page at the back of the pra_list_head queueue.
    return 0;
}
```

根据注释提示，修改后为：

```
static int
_fifo_map_swappable(struct mm_struct *mm, uintptr_t addr, struct Page *page, int
swap_in)
{
    list_entry_t *head=(list_entry_t*) mm->sm_priv;
    list_entry_t *entry=&(page->pra_page_link);

    assert(entry != NULL && head != NULL);
    //record the page access situlation
    /*LAB3 EXERCISE 2: YOUR CODE*/
    //(1)link the most recent arrival page at the back of the pra_list_head queueue.
    list_add(head, entry); // 连接
    return 0;
}
```

### 完成 \_fifo\_swap\_out\_victim() 函数

原函数为：

```
/*
 * (4)_fifo_swap_out_victim: According FIFO PRA, we should unlink the earliest
 arrival page in front of pra_list_head queueue,
 *
 * then assign the value of *ptr_page to the addr of this
 page.
 */
static int
_fifo_swap_out_victim(struct mm_struct *mm, struct Page ** ptr_page, int in_tick)
{
    list_entry_t *head=(list_entry_t*) mm->sm_priv;
    assert(head != NULL);
    assert(in_tick==0);
```

```

    /* Select the victim */
    /*LAB3 EXERCISE 2: YOUR CODE*/
    //(1) unlink the earliest arrival page in front of pra_list_head queue
    //(2) assign the value of *ptr_page to the addr of this page
    return 0;
}

```

根据注释提示，修改后为：

```

/*
 * (4)_fifo_swap_out_victim: According FIFO PRA, we should unlink the earliest
 arrival page in front of pra_list_head queue,
 *
 * then assign the value of *ptr_page to the addr of this
 page.
 */
static int
_fifo_swap_out_victim(struct mm_struct *mm, struct Page ** ptr_page, int in_tick)
{
    list_entry_t *head=(list_entry_t*) mm->sm_priv;
    assert(head != NULL);
    assert(in_tick==0);
    /* Select the victim */
    /*LAB3 EXERCISE 2: YOUR CODE*/
    //(1) unlink the earliest arrival page in front of pra_list_head queue
    list_entry_t *to_swap = list_prev(head); // 获取前一个节点（换出的页）
    assert(head != to_swap); // 判断是否是同一个
    //(2) assign the value of *ptr_page to the addr of this page
    struct Page *page = le2page(to_swap, pra_page_link); // 获取该页
    list_del(to_swap); // 删除该页
    assert(page != NULL);
    *ptr_page = page;
    return 0;
}

```

## 测试

运行 `make qemu-nox` 命令之后，显示结果如下，在第二个红框出可以看出是符合基本要求的：

```

PDE(001) fac00000-fb000000 00400000 -rw
|-- PTE(000e0) faf00000-fafe0000 000e0000 urw
|-- PTE(00001) fafeb000-fafec000 00001000 -rw
----- END -----
check_vma_struct() succeeded!
page fault at 0x00000100: K/W [no page found].
check_pgfault() succeeded!
check_vmm() succeeded.
ide 0: 10000(sectors), 'QEMU HARDDISK'.
ide 1: 262144(sectors), 'QEMU HARDDISK'.
SWAP: manager = fifo swap manager
BEGIN check_swap: count 31994, total 31994
setup Page Table for vaddr 0x1000, so alloc a page
setup Page Table vaddr 0~4MB OVER!
set up init env for check_swap begin!
page fault at 0x00001000: K/W [no page found].
page fault at 0x00002000: K/W [no page found].
page fault at 0x00003000: K/W [no page found].
page fault at 0x00004000: K/W [no page found].
set up init env for check_swap over!
write Virt Page c in fifo_check_swap
write Virt Page a in fifo_check_swap
write Virt Page d in fifo_check_swap
write Virt Page b in fifo_check_swap
write Virt Page e in fifo_check_swap
page fault at 0x00005000: K/W [no page found].
swap_out: i 0, store page in vaddr 0x1000 to disk swap entry 2
write Virt Page b in fifo_check_swap
write Virt Page a in fifo_check_swap
page fault at 0x00001000: K/W [no page found].
swap_out: i 0, store page in vaddr 0x2000 to disk swap entry 3
swap_in: load disk swap entry 2 with swap_page in vadr 0x1000
write Virt Page b in fifo_check_swap
page fault at 0x00002000: K/W [no page found].
swap_out: i 0, store page in vaddr 0x3000 to disk swap entry 4
swap_in: load disk swap entry 3 with swap_page in vadr 0x2000
write Virt Page c in fifo_check_swap
page fault at 0x00003000: K/W [no page found].
swap_out: i 0, store page in vaddr 0x4000 to disk swap entry 5
swap_in: load disk swap entry 4 with swap_page in vadr 0x3000
write Virt Page d in fifo_check_swap
page fault at 0x00004000: K/W [no page found].
swap_out: i 0, store page in vaddr 0x5000 to disk swap entry 6
swap_in: load disk swap entry 5 with swap_page in vadr 0x4000
write Virt Page e in fifo_check_swap
page fault at 0x00005000: K/W [no page found].
swap_out: i 0, store page in vaddr 0x1000 to disk swap entry 2
swap_in: load disk swap entry 6 with swap_page in vadr 0x5000
write Virt Page a in fifo_check_swap
page fault at 0x00001000: K/R [no page found].
swap_out: i 0, store page in vaddr 0x2000 to disk swap entry 3
swap_in: load disk swap entry 2 with swap_page in vadr 0x1000
count is 7, total is 7
check_swap() succeeded!
++ setup timer interrupts
100 ticks

```

## 回答问题

- 如果要在 ucore 上实现“extended clock页替换算法”，现有的 swap\_manager 框架是否足以支持在 ucore 中实现此算法？
  - 如果是，请给你的设计方案。

- 如果不是，请给出你的新的扩展和基此扩展的设计方案。
- 并需要回答如下问题：
  - 需要被换出的页的特征是什么？
  - 在 ucore 中如何判断具有这样特征的页？
  - 何时进行换入和换出操作？

## 设计方案

可以实现

1. 需要将页面连接成**环形链表**
  2. 当前指针指向**最早进入的页**
  3. 设置页表项的 **PTE\_D** 位，如果访问过，则为1，否则为0（初始化为0）
  4. 需要进行页替换时，遍历环形链表
- 如果该页表项的 **PTE\_D** 位为1，则置0，访问下一页
  - 如果该页表项的 **PTE\_D** 位为0，则将该页淘汰

## 需要被换出的页特征

- 当前页未被访问（或者脏位在上一次遍历过程中由1被置0）
- 当前页数据与外部储存一致

## 在 ucore 中如何判断具有这样特征的页

判断当前页 **PTE\_D** 位是否为0

- 如果为0，则淘汰该页
- 如果为1，则置0，访问下一页表项，直到访问到第一个 **PTE\_D** 为0的页

## 何时进行换入换出操作

- 需要的页不在页表中
- 页表满

# 实验结果

运行 **make grade** 得到如下结果

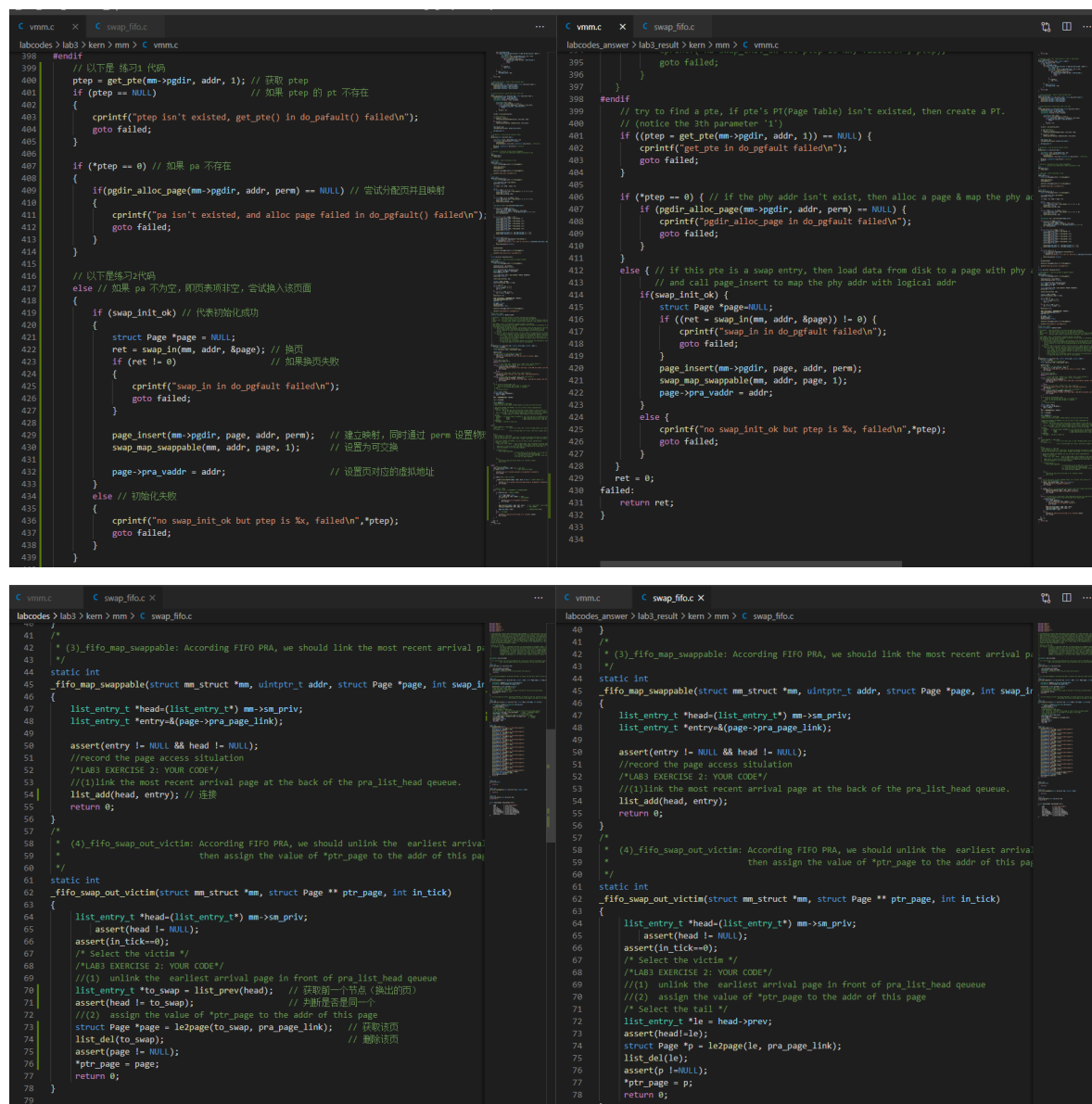
```
mijialong$>make grade
Check SWAP:          (3.6s)
  -check pmm:                OK
  -check page table:         OK
  -check vmm:                OK
  -check swap page fault:    OK
  -check ticks:              OK
Total Score: 45/45
mijialong$>
```

## 实验总结

本次实验的代码量相比于上次，少了许多，但是更加考验对操作系统的理解，并且注释中给出的相关提示，仔细阅读相关代码之后能够快速地理理解需要做的事情，从而完成本次实验

# 完成实验后，请分析ucore\_lab中提供的参考答案，并请在实验报告中说明你的实现与参考答案的区别

实现无区别



列出你认为本实验中重要的知识点，以及与对应的OS原理中的知识点，并简要说明你对二者的含义，关系，差异等方面的理解（也可能出现实验中的知识点没有对应的原理知识点）

实验：

- 页异常处理
- FIFO 页替换算法
- extended clock页替换算法

原理：

- 虚拟储存技术
- 虚拟页式储存
- 缺页异常处理
- 页替换算法

- FIFO（先进先出）算法
- extended clock（拓展时钟替换）算法
- LRU（最近最少使用）算法
- OPT（最有页面替换）算法
- 局部性原理
  - 时间局部性
  - 空间局部性
  - 分支局部性

#### 理解

- 实验中的知识点是原理中的具体应用，需要考虑到具体的操作，会更加的复杂繁琐
- 前者为后者提供具体的内存管理功能的详细操作和底层支持
- 前者涉及到了除开操作系统之外的一些知识，比如说数据结构和算法，使得实现起来稍微麻烦一些

#### 列出你认为os原理中很重要，但在实验中没有对应上的知识点

- OPT 算法