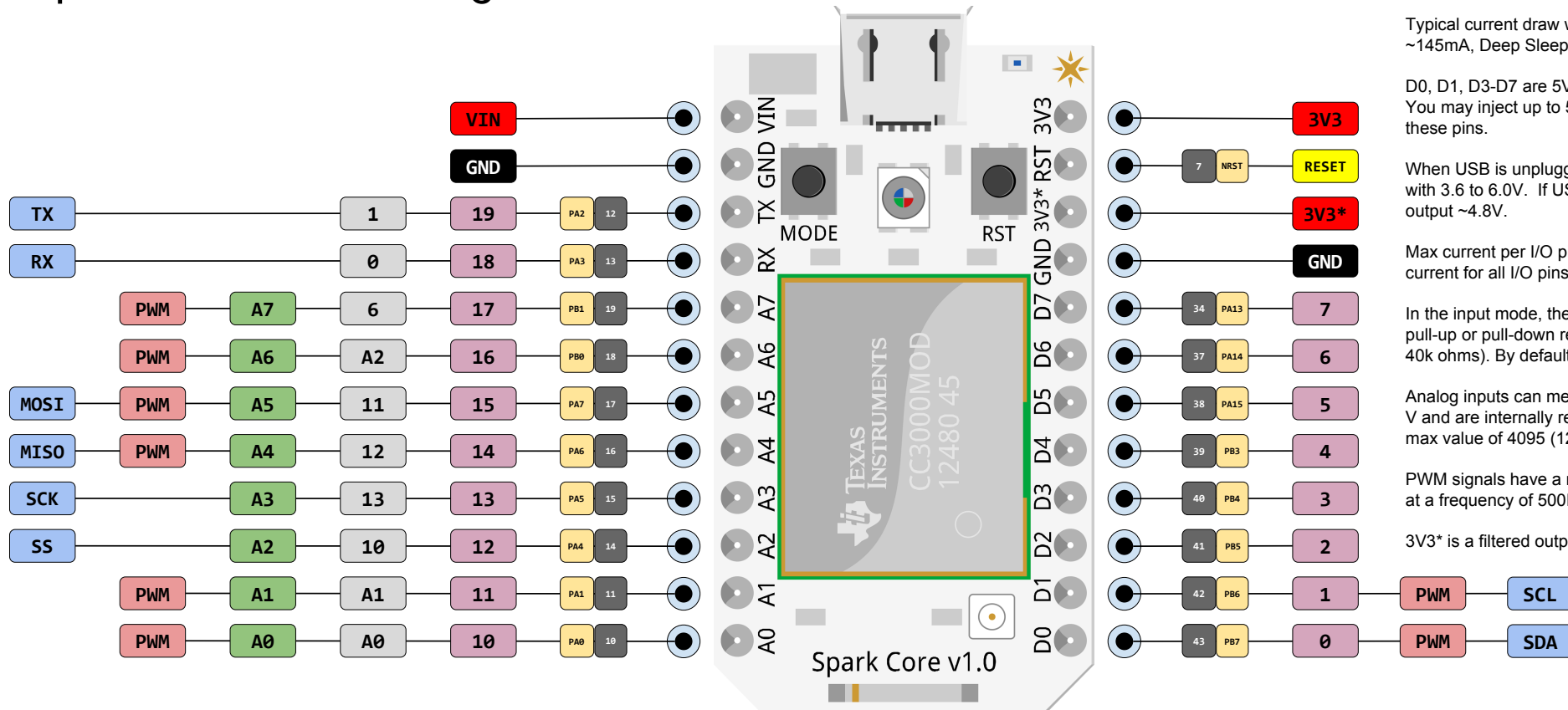


Spark Core Pinout Diagram



Useful notes:

Typical current draw with Wi-Fi enabled is ~145mA, Deep Sleep mode is ~187uA.

D0, D1, D3-D7 are 5V tolerant (D2 is 3.3V max). You may inject up to 5mA of current at 5V into these pins.

When USB is unplugged, VIN can be supplied with 3.6 to 6.0V. If USB is plugged in, VIN will output ~4.8V.

Max current per I/O pin = 25mA. Total supply current for all I/O pins combined = 150mA.

In the input mode, the user can activate internal pull-up or pull-down resistors (typically equal to 40k ohms). By default these are deactivated.

Analog inputs can measure voltages of up to 3.3 V and are internally referenced to 3V3*, with a max value of 4095 (12-bit).

PWM signals have a resolution of 8-bits and run at a frequency of 500Hz.

3V3* is a filtered output used for analog sensors.

REF:

<http://docs.spark.io/#/hardware/core-pin-mapping-v1.xlsx>
<http://docs.spark.io/#/shields>

Inspired by <http://goo.gl/qvIUz>

Created by Jonathan Beri, <http://google.com/+JonathanBeri>

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