

Embedded Engineer

ROBERT SMITH

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Objective

6+ years of experience in Design, Development and Testing of Microcontroller and DSP based projects in the field of automotive & Embedded System Highly proficient in DSP processor based hardware design for the products like Video, Audio Encoders and Decoders. Rich experience in developing the Embedded C, C++ code for the applications like Energy Meters and Weighing scales.

Skills

C, Software Defined Radio, Firmware, VHDL, Verilog, Linux.

Work Experience

Embedded Engineer

ABC Corporation - October 2013 - October 2014

- Provides a framework for Car multimedia, QNX APIs provide unified access to various video capturing devices.
- Drivers use the base video dev kernel module, which represents the corresponding QNX kernel interface for working with video devices.
- Understands V4L2 API, ioctls, and IO methods.
- Understands the QNX APIs Video capabilities Creation of buffers for hardware device at user level on device-specific capabilities.
- Tests the functionality on workbench Environment C/C++, Make, Ctags, Buildroot, QNX RTOS, CAN etc Project 2 Audio and Video Porting on Embedded Linux Target (MPlayer) MPlayer is an open-source media player.
- Supports a wide variety of media formats and can also save all streamed content to a file.
- Creates kernel image for the target architecture.

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Delta Corporation - 2009 - 2013

- Project 1 Video Encoder for Infotainment System This project develop video surveillance product to compress the Video signal from camera into MPEG4 format, perform video analytics on it and transmission over Ethernet.
- It contains two DSP processors that are identified as TMS320DM642 and TMS320DM648 and interfaces involving DDR2 SDRAM, SDR SDRAM, flash, Ethernet Gige on SGMII to Copper, 10/100, Video A to D converter, Ethernet switch, and SGMII - MII interfacing.
- Responsibilities Design and architect the block diagram based on the specifications given by the customer.
- Selection of components based on client requirement and cost of the design.
- Draw the schematic for the design using cadence allegro schematic entry tool.
- Implemented the logic to save the data into data base Implemented the logic to filter the data using rules & provides filtering algorithm based on supported Verify the schematic and sending Gerber details.

- Test the board for functionality and Unit testing Environment C/C++, Code Composer Studio IDE, Cadence Allegro schematic entry, JTAG.

Education

Bachelors in Electrical and Electronics Engineering - (Anna University)