

# Robert Smith

## Senior Embedded Software Engineer/Director

### **CONTACT DETAILS**

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### **PERSONAL STATEMENT**

8 years of Embedded System experience with emphasis on development of application in C, embedded software and pre and post silicon validation. Use various Chip families like MSP430, LPC2148, [ ] 8051, Cortex M3. Worked on Operating system like Embedded Linux, Desktop Linux, and Windows. Experience in bug-fixing with peripherals such as LCD, GPIO, PWM, A/D, D/A, RTC, EEPROM etc.

### **WORK EXPERIENCE**

#### **Senior Embedded Software Engineer/Director**

**ABC Corporation - July 2011 - October 2015**

##### *Responsibilities:*

- Involved in capturing Test requirement and preparing Test Design and Test Plan.
- Involved in chip validation software development and testing - Involve in reading AVB protocols specifications - Read the GMAC specification of the chip and identify validation requirements.
- Changed in Open AVB stack - Debug USB class Driver and USB enumeration code.
- Worked with offshore team to modify AC charging station software.
- Developed real-time low level modules for use as general purpose plug-ins for FPGAs.
- Responsibilities span the full lifecycle of product development from design through integration, test, and field support.
- Worked and learned in an engineering organization with the highest software engineering process maturity level as recognized by the Software Engineering Institute.

#### **Senior Embedded Software Engineer**

**Delta Corporation - 2010 - 2011**

##### *Responsibilities:*

- contractor and offsite consultant) Project External Fiber End Face Image Grabber and Labeling Instrument Build research platform on ARM based SoC, .
- HID and LCD driver for interactive control Programmed software package for controlling image capture, storage management, human-device interact .
- Led effort to obtain UL 1998 certification for AC safety circuit.
- Primary engineering customer contact for OEM vehicle communication interface.
- Use GNU style tools such as C and C++ compilers and Make.
- Use profilers on Win32 development machines to enhance real-time embedded modules targeted for FPGA architectures.
- This is Dummy Description data, Replace with job description relevant to your current role.

### **SKILLS**

MS Office

### **LANGUAGES**

English (Native)  
French (Professional)  
Spanish (Professional)

### **INTERESTS**

Climbing  
Snowboarding  
Cooking  
Reading

### **REFERENCES**

Reference - 1 (Company Name)  
Reference - 2 (Company Name)

## Education

B.E. in Biomedical & Instrumentation engg - (Saurashtra University)