Robert Smith

Senior Member Technical Staff

Phone (123) 456 78 99
Email: info@qwikresume.com
Website : www.qwikresume.com
LinkedIn: linkedin.com/qwikresume
Address: 1737 Marshville Road, Alabama

SUMMARY

Software engineering professional with over 15 years experience in all phases of total Software Development lifecycle process. Experience in Software Architecture, Design, Development, Software Systems Engineering, Requirements development and Systems Integration and test. Key strengths include development of software systems architecture and processes, engineering to define and develop processes, systems and software, Coding, implementation and software for commercial, passenger entertainment and aerospace systems.

SKILLS

Highly experienced in board level, and FPGA design.

WORK EXPERIENCE

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ABC Corporation - May 1996 – January 2002

- Helped customer create own design and support.
- Developed and implemented operations models for both small- and large-scale isotope production facilities by studying product requirements, researching, designing, modifying, and testing methods and equipment
- Developed process maps and procedures to be utilized in all aspects of the Stable Isotope Portfolio
- Assured product and process quality by designing testing methods and developing process maps; test finished product and process capabilities; establish standards and procedures; confirm production and manufacturing processes
- Developed and provided leadership to manufacturing efforts in a new ORNL Centrifuge Manufacturing Capability coming online.
- Participated on project teams (currently executing an anticipated > $300M in work scope with great potential for additional future projects)
- Provided decision-making information and advise line management on budget, staffing, and facilities needs to establish and maintain safe, efficient, and cost-effective operations and processes

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Delta Corporation - 1995 – 1996

- Responsible for the support for the development of the required market strategy and operational plans for Bellcores Network Management Architecture (.
- Supported the development of the Market Plan for the NMA Bellcores network management product.
- The plan was acted upon resulting in Puerto Rico and TelMex (Mexico) as NMA customers $34M over 5 years.
- Led Digital Design Flow, Tapeout Methodology, and Memory BIST projects.
- Applied Dracula DRC and LVS rules for 0.25 micron and 0.18 micron processes as final step before manufacturing.
- Oversaw ESD I/O Testchip layout and Library Vendors Testchips tapeout.
- Upgraded software routines for the Scenario Preprocessor for PATRIOT Operator Tactical Trainer (OTT) Weapons System Analyze requirements, assess .

EDUCATION

Project Management Fundamentals - (Dale Carnegie)