

Robert Smith

Senior Embedded Software Engineer/Assistant

CONTACT DETAILS

1737 Marshville Road,
Alabama
(123)-456-7899
info@qwikresume.com
www.qwikresume.com

PERSONAL STATEMENT

Experience in the field of embedded systems Development, Firmware and device drivers development for embedded products, Testing, Verification & Validation. Experience in design, implement and debugging embedded software in Linux using C, C++. Experience in C, C++ code Development and code Debugging, RTOS libraries for embedded Linux in C.

WORK EXPERIENCE

Senior Embedded Software Engineer/Assistant

Caterpillar - April 2014 - 2020

Responsibilities:

- Detailed requirements and software for the Javelin Missile Built-In Test (BIT) for the squibs, serial communications, field-of-view (FOV), .
- Integrated the BIT software in C and assembly into the missile system using C5510 DSP and Code Composer Studio emulator.
- Utilized an RS-232 serial interface for the test fixture for test and emulation of the BIT functions.
- Designed and developed firmware for the external watchdog of the Aftertreatment Control module which exactly replicates the functions of Cummins .
- Developed firmware for the particulate matter sensor interface module (PMSIM) which is an 8-bit microcontroller viz.
- Maintained of OS layer software of the Engine Control Module, particularly related to Bosch pump motor and sensor module which communicates.
- Develop and maintain relevant documentation (for e.g. specifications, designs, test plans and reports, etc.) required for medical product Design History Files.

Senior Embedded Software Engineer

Delta Corporation - 2012 - 2014

Responsibilities:

- Project Development and Testing of Reverse Aid System Developed a Reverse Aid system to assist the driver to park.
- The RA consists of an Imager, microcontroller & FPGA.
- The ECU for this systems is connected to the CAN/LIN/J1939 network.
- Based on the messages (Steering wheel Angle message, Ultrasonic message, etc.) received from CAN/LIN/J1939 bus, microcontroller provides the corresponding overlay image to FPGA.
- The FPGA superimposes the overlay on an Imager, captured image/NTSC video signal.
- Imager is configured by Micro to capture the image via I2C.
- Overlay will supply via SPI.

SKILLS

C++, C, Linux, RTOS,
Micro Controllers,
Protocols.

LANGUAGES

English (Native)
French (Professional)
Spanish (Professional)

INTERESTS

Climbing
Snowboarding
Cooking
Reading

REFERENCES

Reference - 1 (Company Name)
Reference - 2 (Company Name)

Education

Bachelor of Technology in Electronics and Communication
Engineering - (JNTU)