

System Test Engineer

ROBERT SMITH

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Objective

5+ years of experience in LTE and UMTS system/software testing, and verification in the wireless industry. Experienced with creating test plans, quality analysis, functional and performance test of all modem, coordinating testing efforts both in lab and in field. Experienced to closely understand performance for different competitor modems like Samsung, Marvel, MediaTek, Intel, and NVidia.

Skills

Product Marketing, Product Management, Leadership, Strategic Partnerships, Pricing, SAAS, Enterprise Software, Keynote.

Work Experience

System Test Engineer

Qualcomm Incorporated - March 2012 - 2020

- Responsible for to perform performance test, extensive log analysis and develop/implement ideas to improve performance for modem in different RF scenario on different Modems.
- Responsible to do Performance test and quality analysis for UMTS on Qualcomm and different competitive modem.
- Performed log analysis to find critical issues for LTE/WCDMA.
- Responsible to automate testing and post processing procedure to extract KPI using Perl.
- Analyzed different KPI like Mac throughput, CQI, BLER, Res BLER, and Scheduling.
- Analyzed how different modem behaves in different RF scenario and develop ideas to improve performance on Qualcomm modems.
- Initiated Ideas like CQI outer loop, Rate indicator, speed detection, CQI boost, Sense low and high interference, and Adaptive BLER on nodeB to improve performance for modem, in every other RF scenario.

System Test Engineer

Delta Corporation - 2009 - 2012

- Responsible to of different events to show bench marking advantage of Qualcomm modem compare with different modems Events like Modem blogger (SFO), Computex 2015(Taiwan).
- Oversaw system level testing operations within the scope of several industry specific programs.
- Duties included developing tests and test procedures; managing staffing and equipment needs; product yield, failure and productivity analysis; .
- Served as team leader in charge of group software development as well as lead test engineer on several programs.
- Served as the Engineering trouble shooter responsible for the first "I" line wafer stepper in the semi-conductor field.
- Was responsible for building, redesigning, installation, demonstration, coordinating shipping, on-site training and final testing of million dollar+ .
- See description above of ASET.

Education

M. S. in Electrical & Computer Engineering - (State university of New York - New Paltz, NY)