Module	CPU_SingleCycle	
Dependencies	SE18_32, SPLICE_SL2, MUX2_5, MUX2_32, DM, SE16_32, SameBit, MUX2, IM, Flopr_32, MUX3_32, INC4_32, SPLICE_PCJ, Adder_32, OneBitAdder, MUX3, Flopr, DSwitch, CTRL, ALU32, RF, Flopenr_32, MUX32_32, MUX32, Decoder_32, Flopenr, DESwitch, ALUBits1, ALUBits0To30, MUX4	
Inputs	Name	Size (Bits)
	clk	1
	reset	1
Outputs		
Description	Single-cycle MIPS CPU	