

<b>Module</b>	<b>DM</b>	
<b>Dependencies</b>	<b>None</b>	
<b>Inputs</b>	<b>Name</b>	<b>Size (Bits)</b>
	a	32
	wd	32
	we	1
<b>Outputs</b>	<b>Name</b>	<b>Size (Bits)</b>
	rd	32
<b>Description</b>	Random access data memory. rd = contents of address a. Value at address a is set to wd if we is equal to 1.	