Module	RF	
Dependencies	**Flopr_32, Flopr, DSwitch, DESwitch, MUX32_32, MUX32, Decoder_32**	
Inputs	**Name**	**Size (Bits)**
	clk	1
	reset	1
	we	1
	r1a	5
	r2a	5
	wa	5
	wd	32
Outputs	**Name**	**Size (Bits)**
	r1d	32
	r2d	32

Description	MIPS register file, containing all 32-bit MIPS registers
	When reset=1, all registers are set to zero
	Otherwise:
	  - r1d is set to the contents of register #r1a
	  - r2d is set to the contents of register #r2a
	  - if (we == 1), set register #wa equal to wd	