

# Module

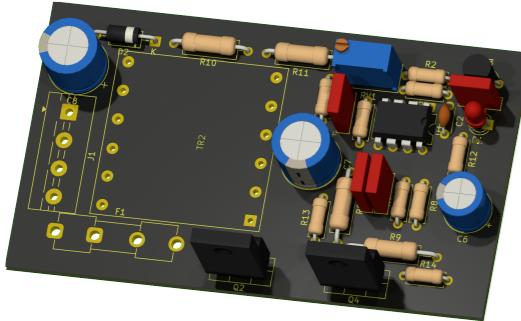
## Variant: DRAFT

Rev + (Unreleased)

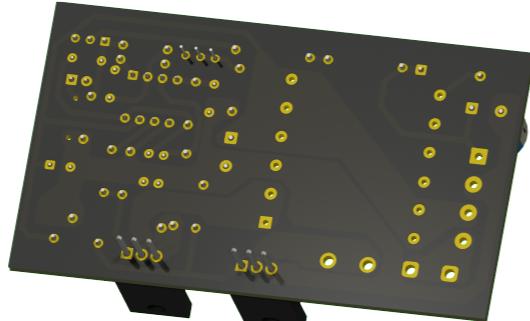
2025-12-12

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### TOP VIEW



### BOTTOM VIEW



### NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 12-Dec-2025

### DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

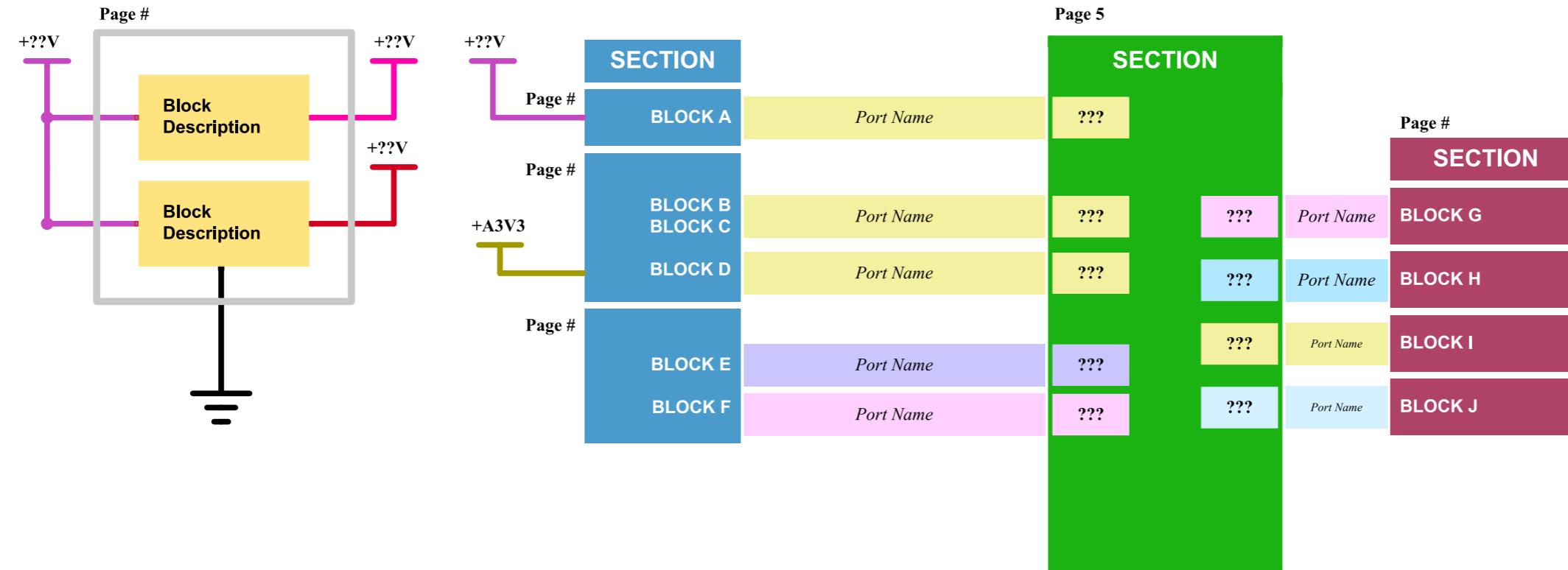
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

	Comments:	Company:	Variant:	Git Hash:
	FR		DRAFT	3e65155
	Board Name:	Module	Project Name:	Alixp Step up module 12 to 450 V
	Sheet Title:	File Name:	Designer:	Date: 2025-01-12
		StepUp_module_12to450V.kicad_sch	FR	Revision: + (Unreleased)
	Sheet Path:	/	Reviewer:	Size: A3
				Sheet: 1 of 7

## [2] Block Diagram

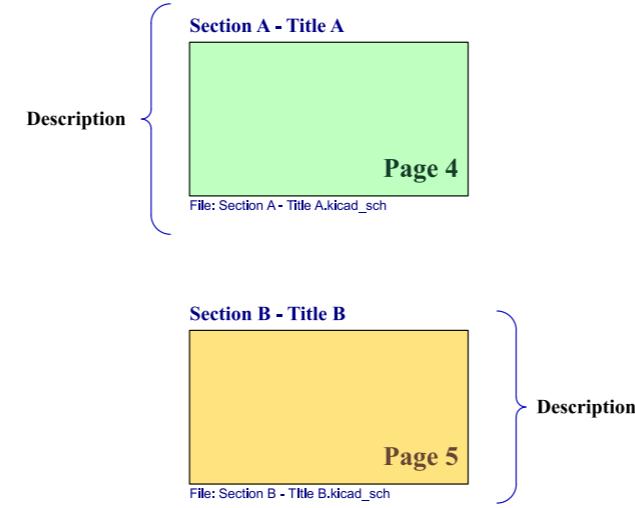


### Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

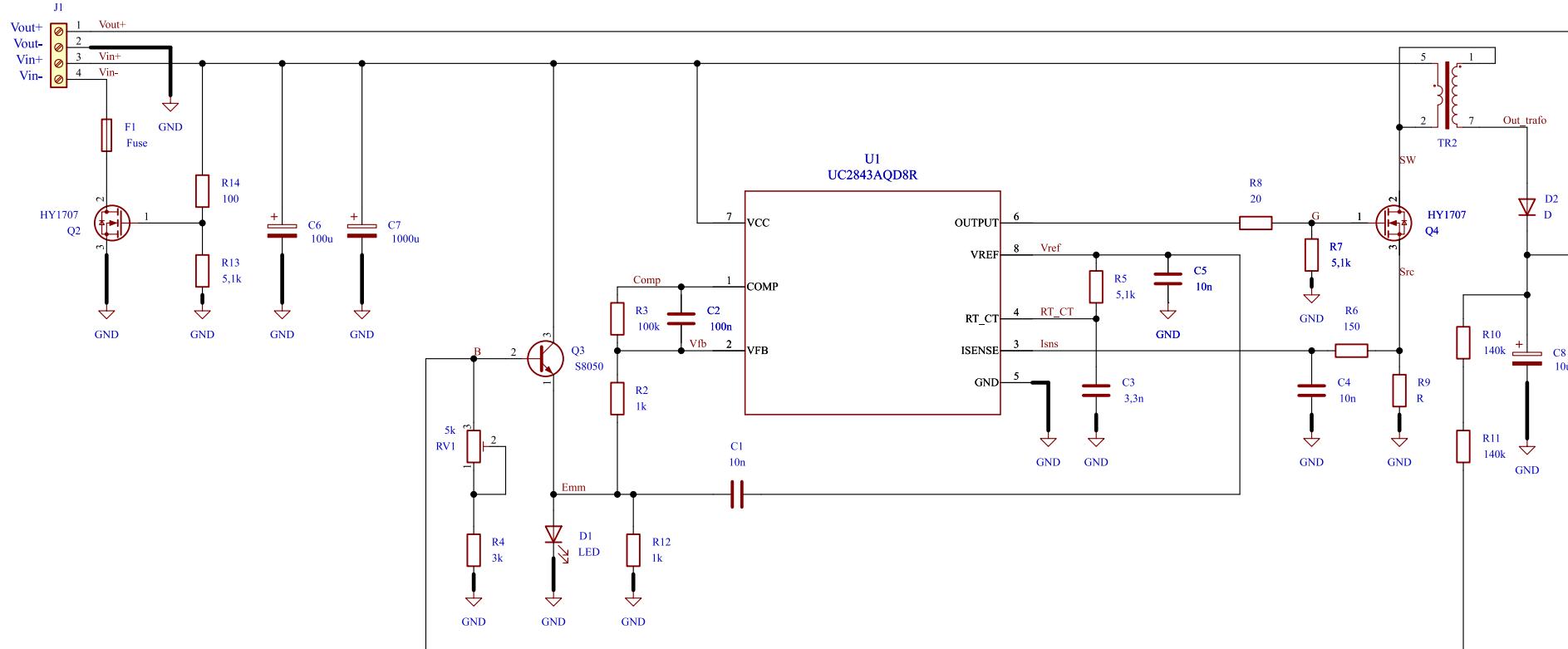
	Comments: FR	Company: FR	Variant: DRAFT	Git Hash: 3e65155
		Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>	
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Block Diagram/	Reviewer:	Size: <b>A3</b>	Revision: Sheet: <b>2 of 7</b>

# [3] Project Architecture



	Comments:	Company:	Variant:	Git Hash:
	FR		DRAFT	3e65155
	Board Name: <b>Module</b>		Project Name:	
			Alixp Step up module 12 to 450 V	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: <b>A3</b> Sheet: <b>3 of 7</b>

# [4] Sheet Title A



Comments:  
AliExpress module

Company:  
FR

Variant:  
DRAFT

Git Hash:  
3e65155

Board Name:  
**Module**

Project Name:  
**Alixp Step up module 12 to 450 V**

Sheet Title:

Sheet Title A

File Name:

Section A - Title A.kicad\_sch

Designer:

FR

Date:

2025-01-12

Revision:

+ (Unreleased)

Sheet Path:

/Project Architecture/Section A - Title A/

Reviewer:

Size:

**A4**

Sheet:

**4** of 7

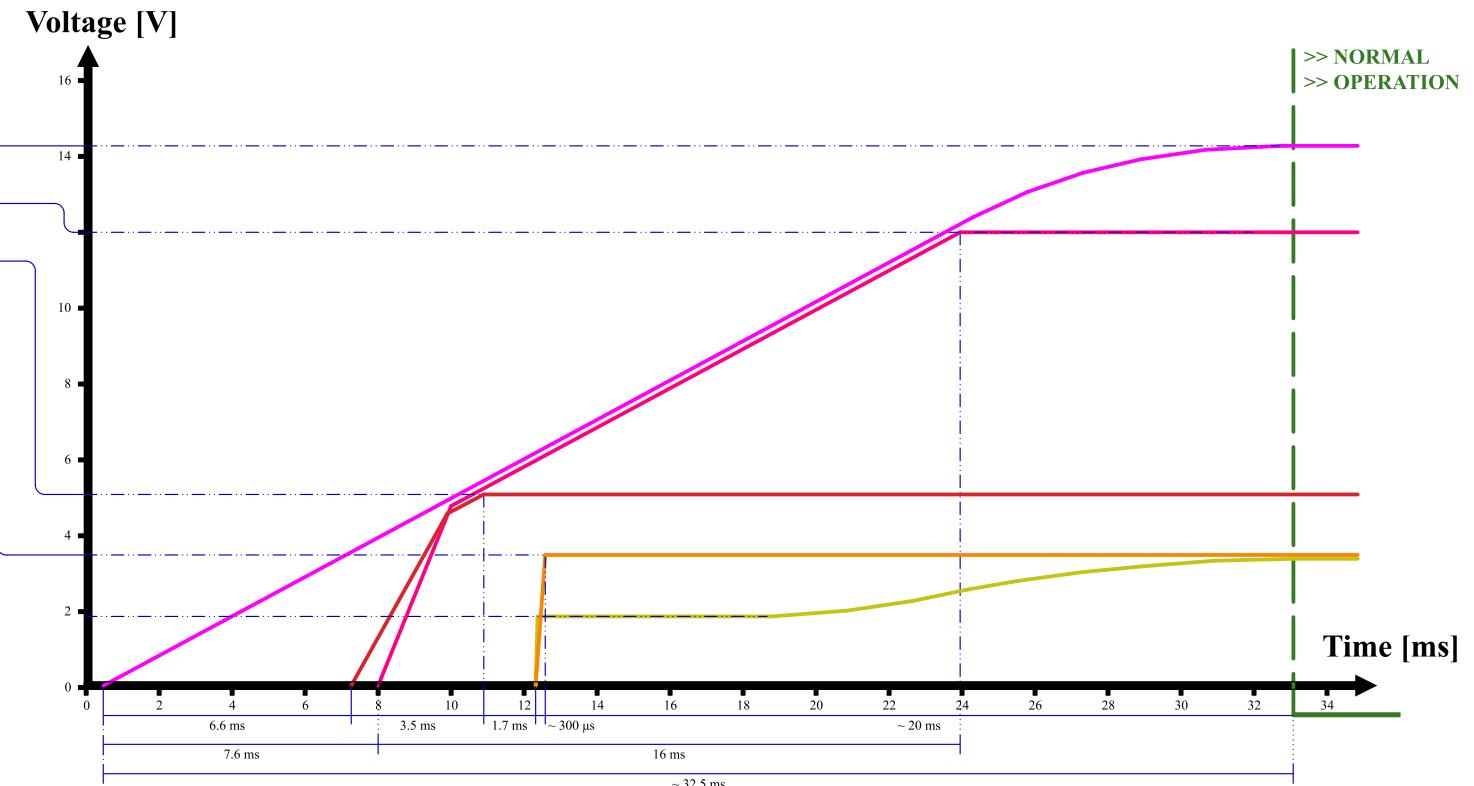
# [5] Sheet Title B



	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: FR	Variant: DRAFT	Git Hash: 3e65155
	Board Name: <b>Module</b>		Project Name: <b>Alixp Step up module 12 to 450 V</b>	
	Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/Section B - Title B/	Reviewer:	Size: <b>A3</b>	Sheet: <b>5 of 7</b>

## [6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



			Comments:	Company:	Variant:	Git Hash:	
			FR	FR			
			Board Name:	Project Name:			
			Module	Alixp Step up module 12 to 450 V			
Sheet Title:		File Name:	Designer:	Date:	Revision:		
Power - Sequencing		Power - Sequencing.kicad_sch	FR	2025-01-12	+ (Unreleased)		
Sheet Path:		Reviewer:		Size:	Sheet:		
/Power - Sequencing/		A4		6	of 7		

## [7] Revision History

	Comments:	Company: <b>FR</b>	Variant: DRAFT	Git Hash: 3e65155	
		Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>		
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)	Revision: D
	Sheet Path: /Revision History/		Reviewer:	Size: <b>A4</b>	Sheet: <b>7 of 7</b>