

A  
1 | 2 | 3 | 4 | 5 | 6 | 7 | 8

# Module

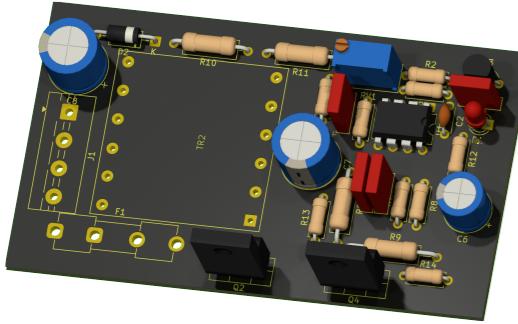
# Rev + (Unreleased)

## Variant: PRELIMINARY

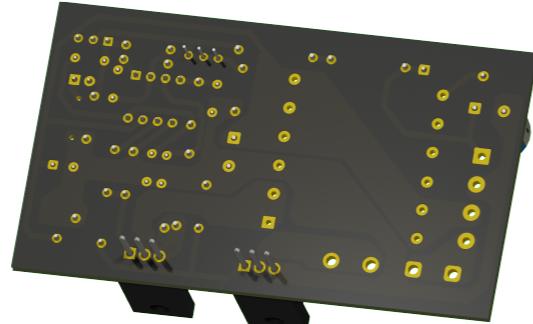
2025-12-12

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### TOP VIEW



### BOTTOM VIEW



### NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 12-Dec-2025

### DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

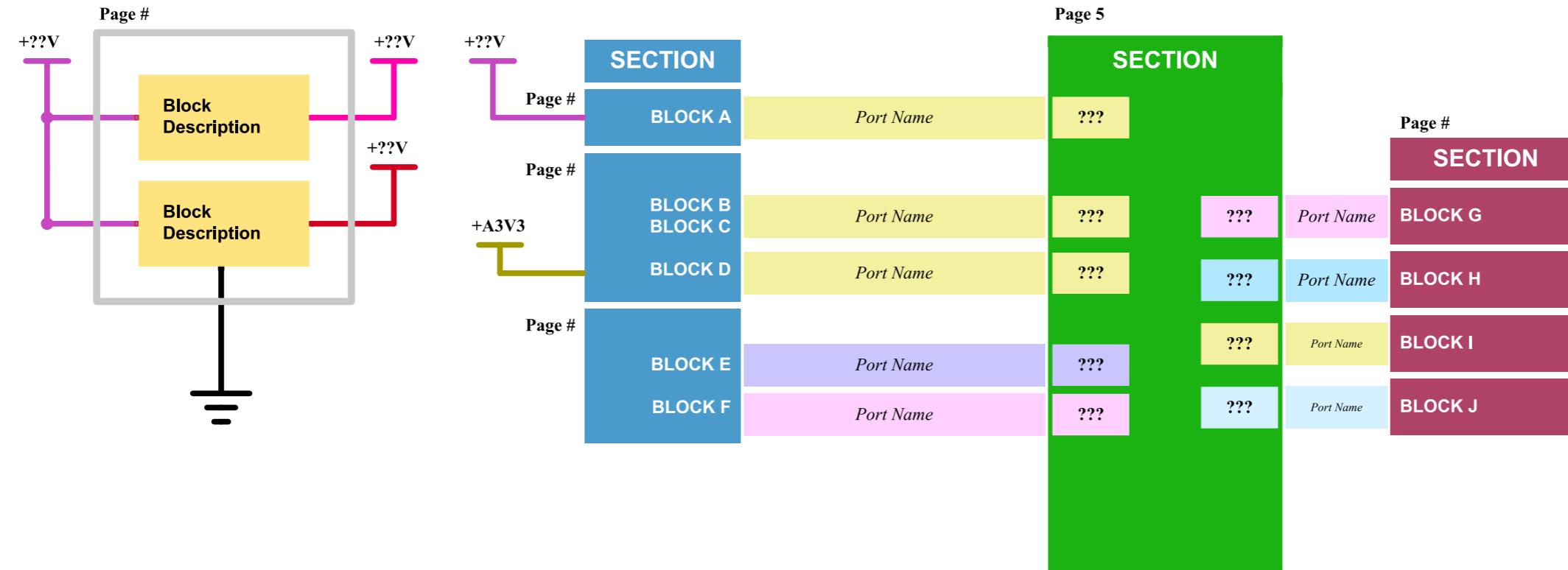
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

	Comments:	Company: <b>FR</b>	Variant: <b>PRELIMINARY</b>	Git Hash: <b>3e65155</b>
	Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>		
	Sheet Title: File Name: <b>StepUp_module_12to450V.kicad_sch</b>	Designer: <b>FR</b>	Date: <b>2025-01-12</b>	Revision: <b>+ (Unreleased)</b>
	Sheet Path: <b>/</b>	Reviewer:	Size: <b>A3</b>	Sheet: <b>1 of 7</b>

## [2] Block Diagram

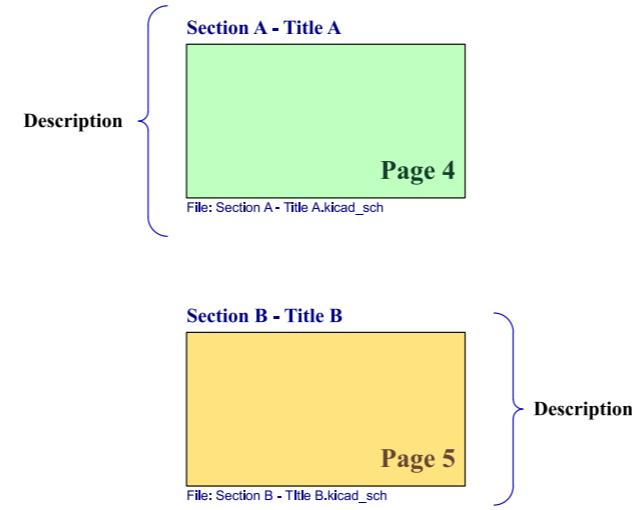


### Target specifications:

<b>Input voltage:</b>	<b>?? - ?? V</b>
<b>Spec 2</b>	<b>??</b>
<b>Spec 3</b>	<b>??</b>
<b>Spec 4</b>	<b>??</b>

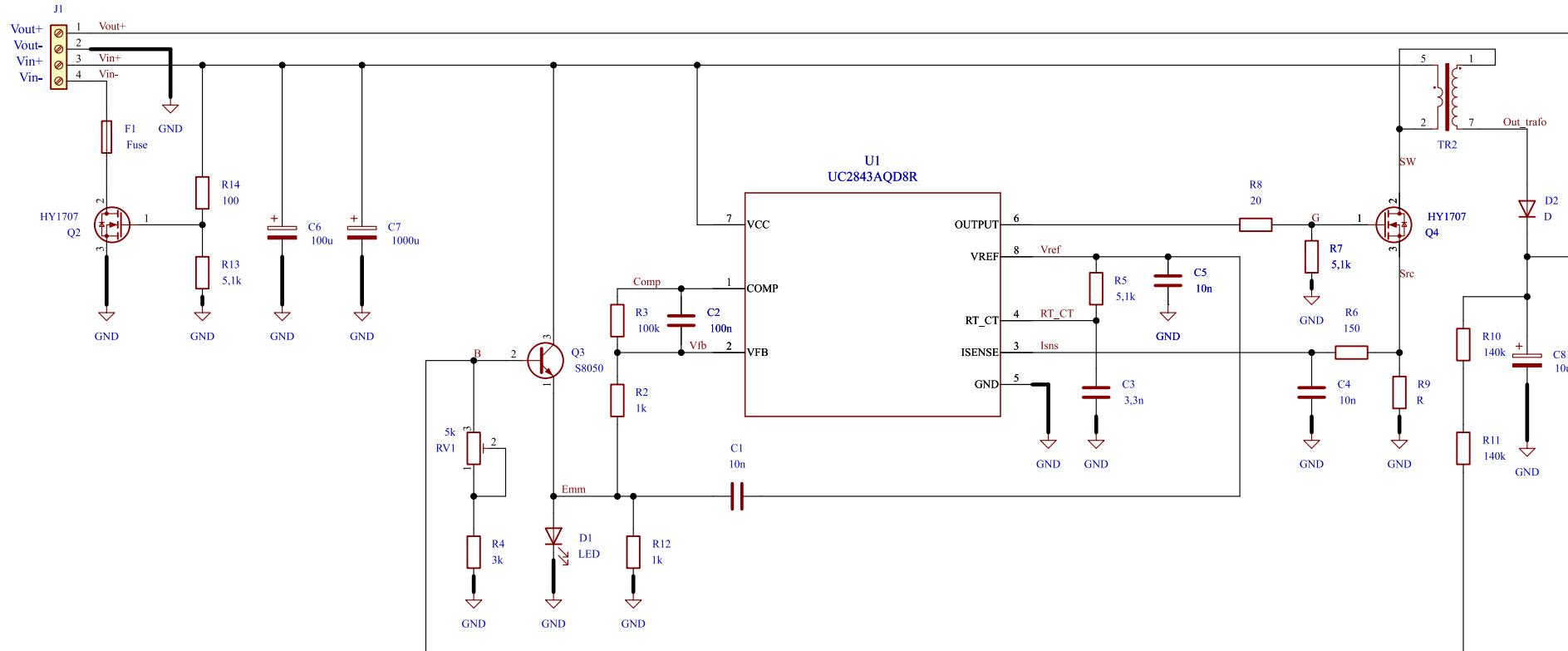
	<b>Comments:</b> FR	<b>Company:</b> FR	<b>Variant:</b> PRELIMINARY	<b>Git Hash:</b> 3e65155
		<b>Board Name:</b> <b>Module</b>	<b>Project Name:</b> <b>Alixp Step up module 12 to 450 V</b>	
	<b>Sheet Title:</b> Block Diagram	<b>File Name:</b> Block Diagram.kicad_sch	<b>Designer:</b> FR	<b>Date:</b> 2025-01-12 + (Unreleased)
	<b>Sheet Path:</b> /Block Diagram/	<b>Reviewer:</b>	<b>Size:</b> <b>A3</b>	<b>Sheet:</b> <b>2 of 7</b>

# [3] Project Architecture



	Comments: <b>FR</b>	Company: <b>FR</b>	Variant: <b>PRELIMINARY</b>	Git Hash: <b>3e65155</b>
	Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>		
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: FR	Date: 2025-01-12
	Sheet Path: /Project Architecture/		Reviewer:	Revision: + (Unreleased)
			Size: <b>A3</b>	Sheet: <b>3 of 7</b>

# [4] Sheet Title A



		Comments: AliExpress module	Company: FR	Variant: PRELIMINARY	Git Hash: 3e65155
		Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>		
		Sheet Title: Sheet Title A	File Name: Section A - Title A.kicad_sch	Designer: FR	Date: 2025-01-12
		Sheet Path: /Project Architecture/Section A - Title A/		Reviewer: A4	Revision: + (Unreleased)

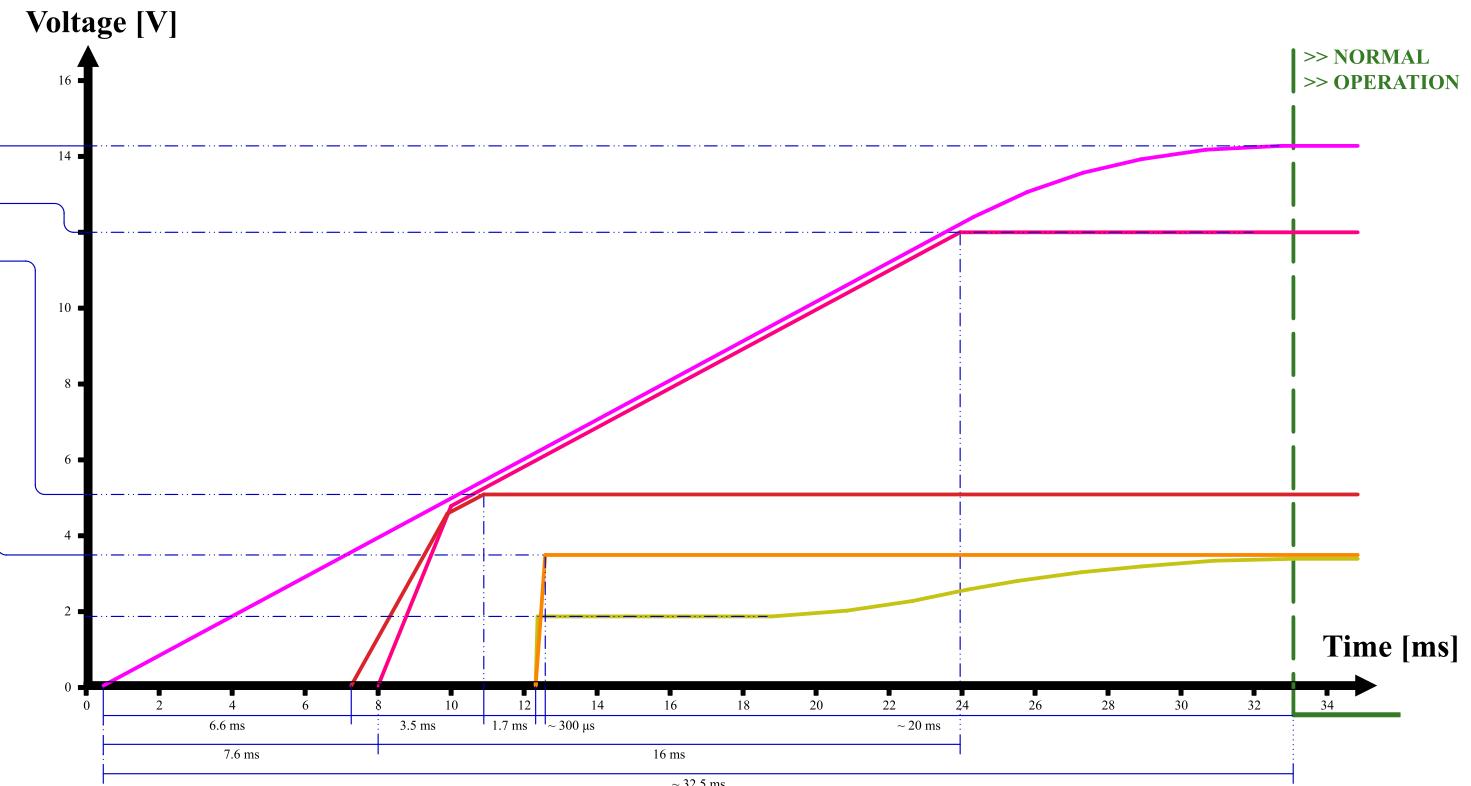
# [5] Sheet Title B



	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: FR	Variant: PRELIMINARY	Git Hash: 3e65155
	Board Name: <b>Module</b>		Project Name: <b>Alixp Step up module 12 to 450 V</b>	
	Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/Section B - Title B/		Reviewer:	Size: <b>A3</b> Sheet: <b>5 of 7</b>

## [6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



			Comments:	Company:	Variant:	Git Hash:	
			FR	FR			
			Board Name:	Project Name:			
			Module	Alixp Step up module 12 to 450 V			
Sheet Title:		File Name:	Designer:	Date:	Revision:		
Power - Sequencing		Power - Sequencing.kicad_sch	FR	2025-01-12	+ (Unreleased)		
Sheet Path:		Reviewer:		Size:	Sheet:		
/Power - Sequencing/		A4		6	of 7		

## [7] Revision History

	Comments:	Company: <b>FR</b>	Variant: PRELIMINARY	Git Hash: 3e65155	
		Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>		
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)	Revision: D
	Sheet Path: /Revision History/		Reviewer:	Size: <b>A4</b>	Sheet: <b>7 of 7</b>