

A  
1 2 3 4 5 6 7 8

# Module

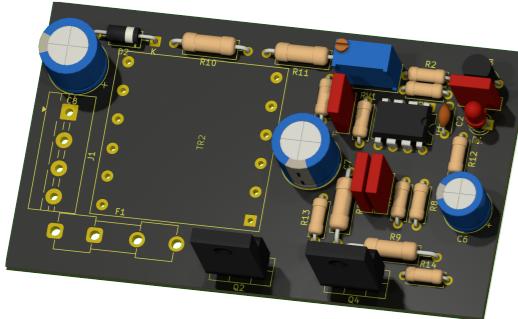
# Rev + (Unreleased)

## Variant: PRELIMINARY

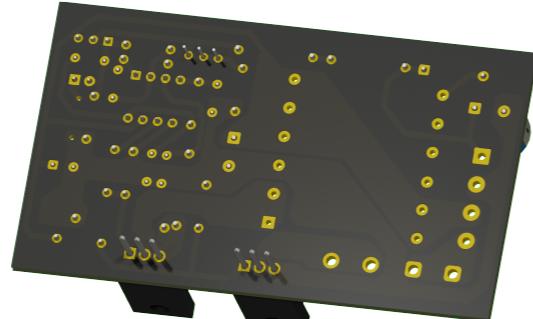
2025-12-13

Page	Index	Page	Index	Page	Index	Page	Index
1	Cover Page	11	.....	21	.....	31	.....
2	Block Diagram	12	.....	22	.....	32	.....
3	Project Architecture	13	.....	23	.....	33	.....
4	Section A - Title A	14	.....	24	.....	34	.....
5	Section B - Title B	15	.....	25	.....	35	.....
6	Power - Sequencing	16	.....	26	.....	36	.....
7	Revision History	17	.....	27	.....	37	.....
8	.....	18	.....	28	.....	38	.....
9	.....	19	.....	29	.....	39	.....
10	.....	20	.....	30	.....	40	.....

### TOP VIEW



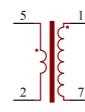
### BOTTOM VIEW



### NOTES

Comment

Not fitted components are marked as



DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 13-Dec-2025

### DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

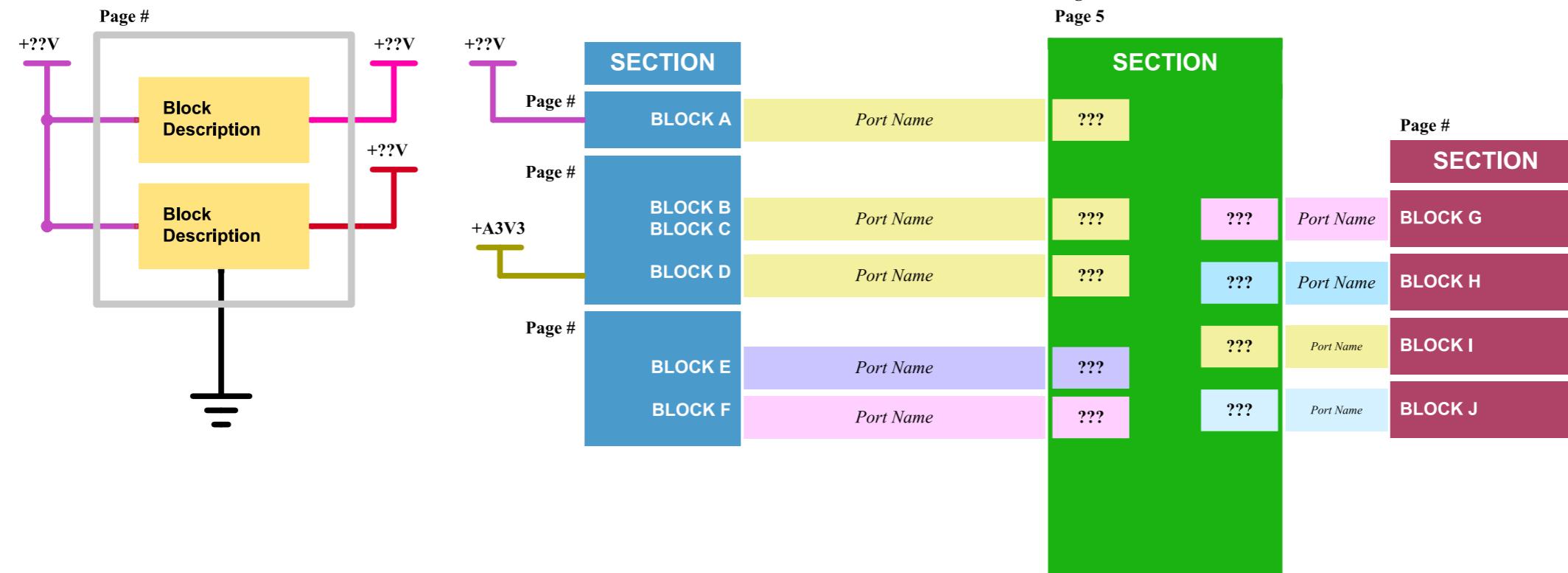
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

	Comments:	Company:	Variant:	Git Hash:
	FR	Module	PRELIMINARY	1fb9677
	Board Name:	Project Name:		
	Module	Alixp Step up module 12 to 450 V		
	Sheet Title:	File Name:	Date:	Revision:
		StepUp_module_12to450V.kicad_sch	FR	2025-01-12 + (Unreleased)
	Sheet Path:	Reviewer:	Size:	Sheet:
	/		A3	1 of 7

## [2] Block Diagram

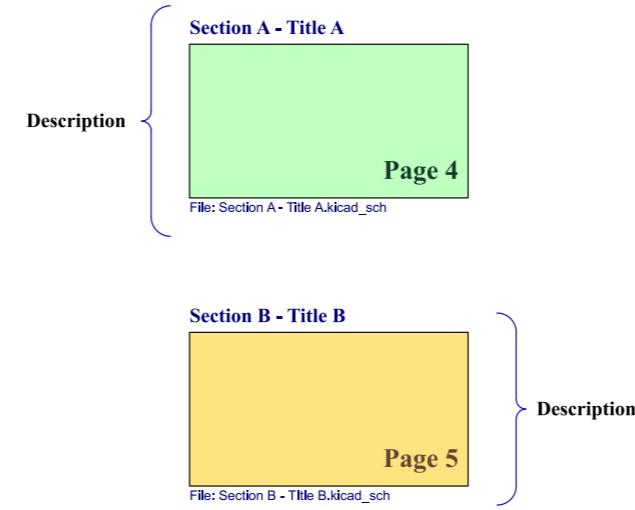


### Target specifications:

<b>Input voltage:</b>	<b>?? - ?? V</b>
<b>Spec 2</b>	<b>??</b>
<b>Spec 3</b>	<b>??</b>
<b>Spec 4</b>	<b>??</b>

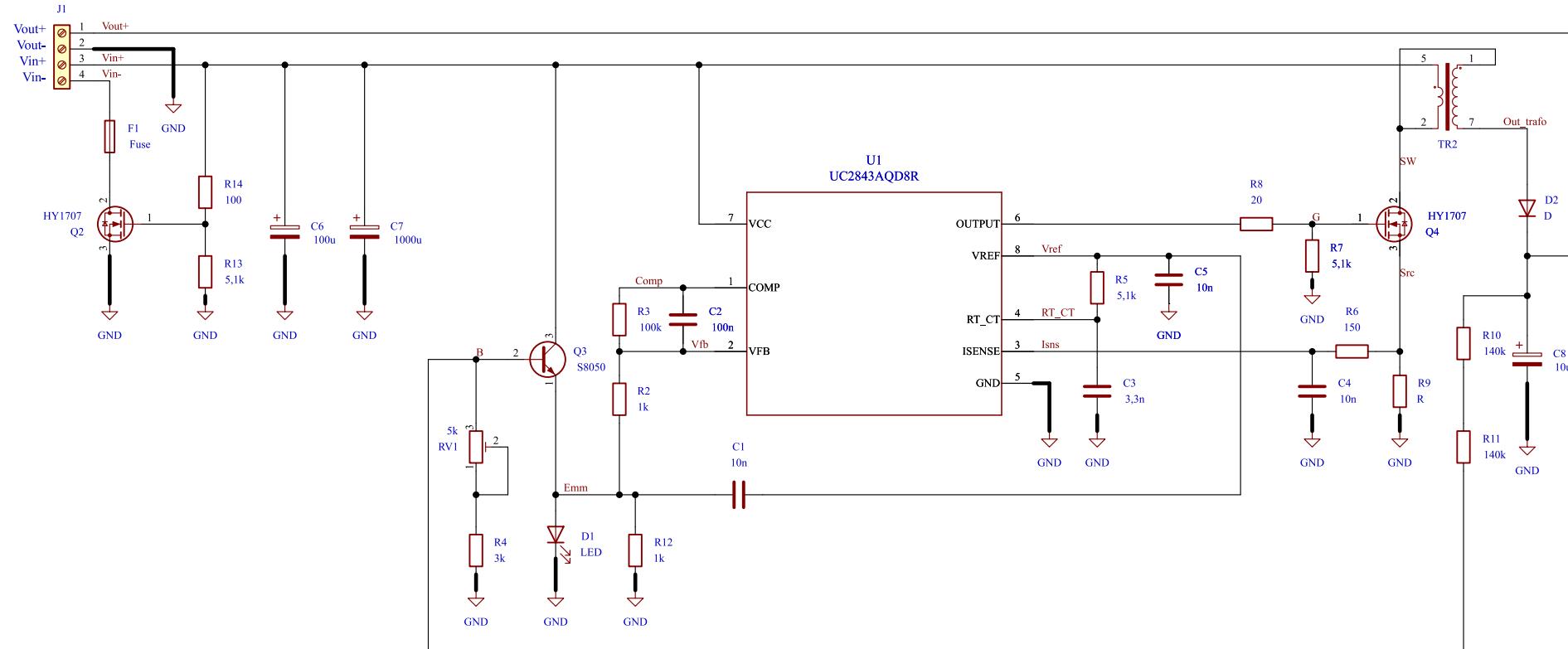
	<b>Comments:</b> FR	<b>Company:</b> FR	<b>Variant:</b> PRELIMINARY	<b>Git Hash:</b> 1fb9677
		<b>Board Name:</b> <b>Module</b>	<b>Project Name:</b> <b>Alixp Step up module 12 to 450 V</b>	
	<b>Sheet Title:</b> Block Diagram	<b>File Name:</b> Block Diagram.kicad_sch	<b>Designer:</b> FR	<b>Date:</b> 2025-01-12 + (Unreleased)
	<b>Sheet Path:</b> /Block Diagram/	<b>Reviewer:</b>	<b>Size:</b> <b>A3</b>	<b>Sheet:</b> <b>2 of 7</b>

# [3] Project Architecture



	Comments:	Company:	Variant:	Git Hash:
	FR		PRELIMINARY	1fe9677
	Board Name: <b>Module</b>		Project Name:	
			Alixp Step up module 12 to 450 V	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: FR	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: <b>A3</b> Sheet: <b>3 of 7</b>

# [4] Sheet Title A



Comments:  
AliExpress module

Company:  
FR

Variant:  
PRELIMINARY  
Git Hash:  
1fe9677

Board Name:  
**Module**

Project Name:  
**Alixp Step up module 12 to 450 V**

Sheet Title:

Sheet Title A

File Name:

Section A - Title A.kicad\_sch

Designer:

FR

Date:

2025-01-12  
+ (Unreleased)

Revision:

Sheet Path:  
/Project Architecture/Section A - Title A/

Reviewer:

Size:

A4

Sheet:  
**4** of **7**

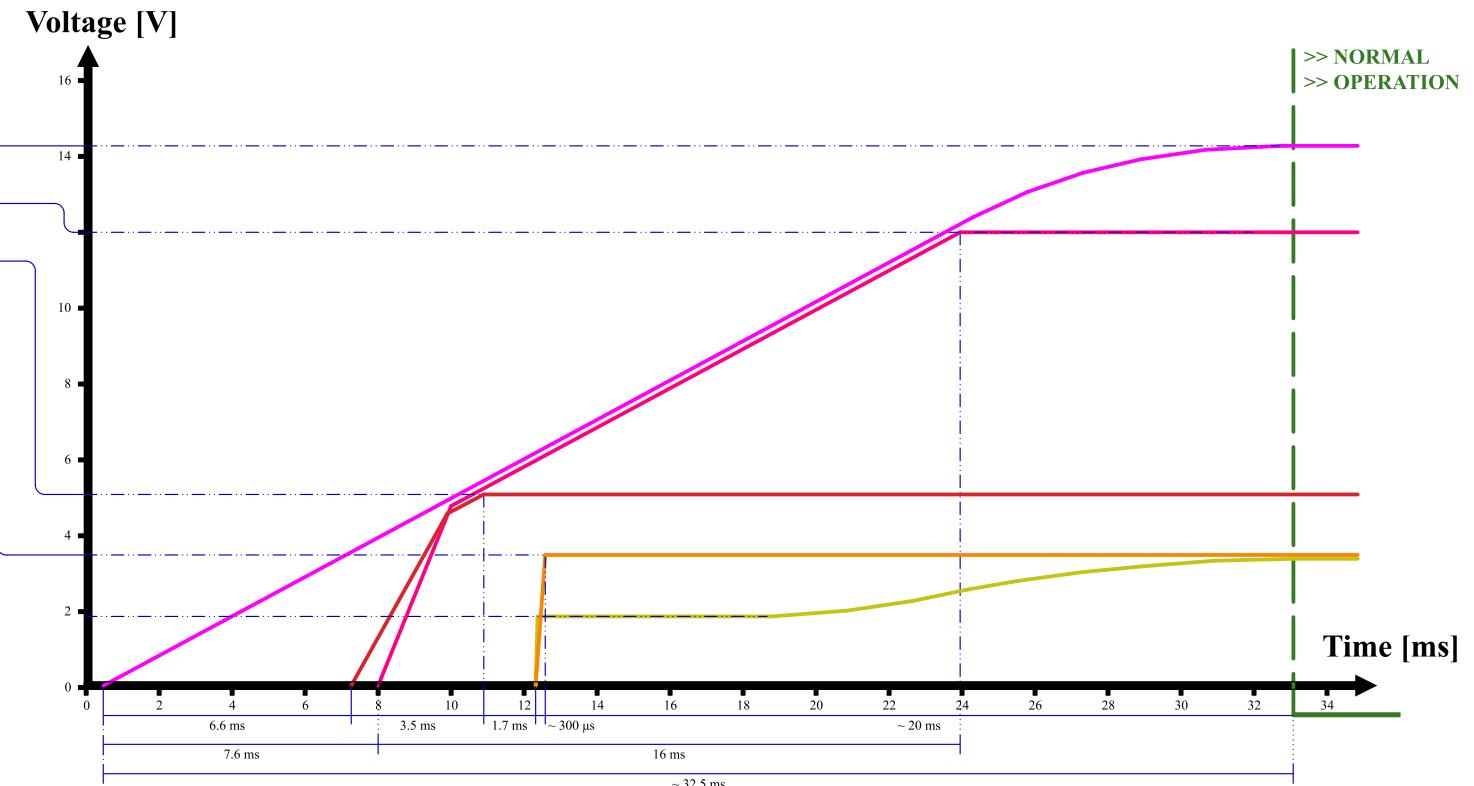
# [5] Sheet Title B



	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: FR	Variant: PRELIMINARY	Git Hash: 1fe9677
	Board Name: <b>Module</b>		Project Name: <b>Alixp Step up module 12 to 450 V</b>	
	Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/Section B - Title B/	Reviewer:	Size: <b>A3</b>	Sheet: <b>5 of 7</b>

## [6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



			Comments:	Company:	Variant: PRELIMINARY	Git Hash: 1fe9677		
			Board Name:					
			Module					
			Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: FR	Date: 2025-01-12		
Sheet Path: <a href="#">/Power - Sequencing/</a>				Reviewer:	Size: <b>A4</b>	Revision: + (Unreleased)		
					Sheet: <b>6</b> of 7			

## [7] Revision History

	Comments:	Company: <b>FR</b>	Variant: PRELIMINARY	Git Hash: 1fe9677	D
		Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>		
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: FR	Date: 2025-01-12 + (Unreleased)	
	Sheet Path: /Revision History/		Reviewer:	Size: <b>A4</b>	Sheet: <b>7 of 7</b>