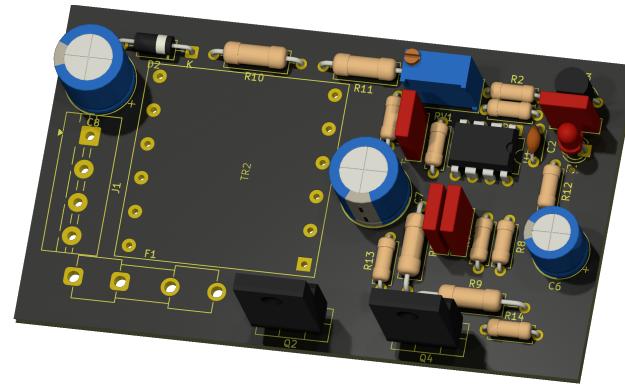


# Module Assembly Document

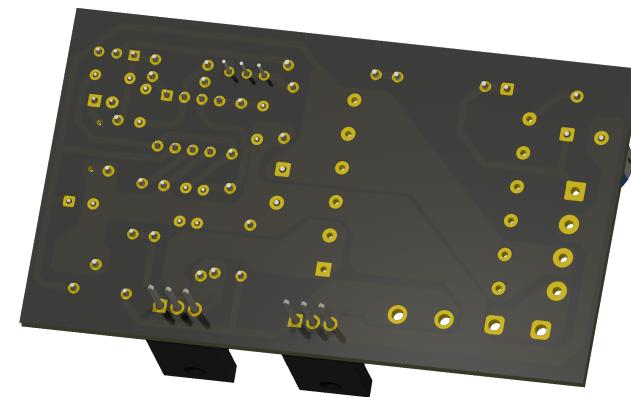
A

**Top View**



B

**Bottom View**



C

D

	Comments:  FR	Company:  FR	Variant:  PRELIMINARY	Git Hash:  7d63e9b
	Board Name:  <b>Module</b>			Project Name:  <b>Alixp Step up module 12 to 450 V</b>
	Sheet Title: Top/Bottom View	File Name: StepUp_module_12to450V.kicad_pcb	Designer: FR	Date: 2024-04-13 Revision: + (Unreleased)
	Sheet Path:		Reviewer:	Size: <b>A4</b> Sheet: <b>1 of 3</b>

## **Module Assembly Document**

A

A

## Components

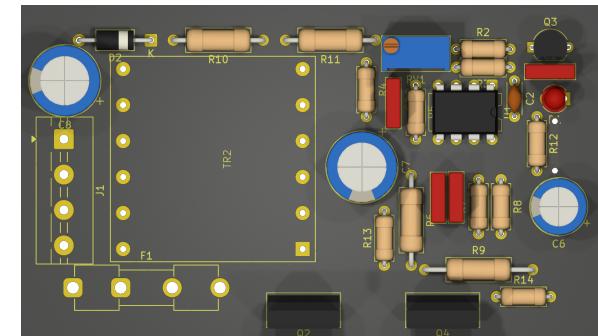
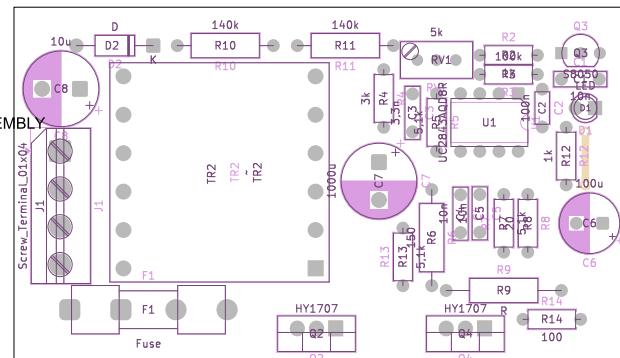
Type	Front Side	Back Side	Total
THT	30	0	30
SMD	1	0	1
<b>Total</b>	<b>31</b>	<b>0</b>	<b>31</b>

## ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

- 1) DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
  - 2) DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
  - 3) IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSOCIATED FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
  - 4) CONFORMAL COATING IS NOT REQUIRED.
  - 5) DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.

B

B



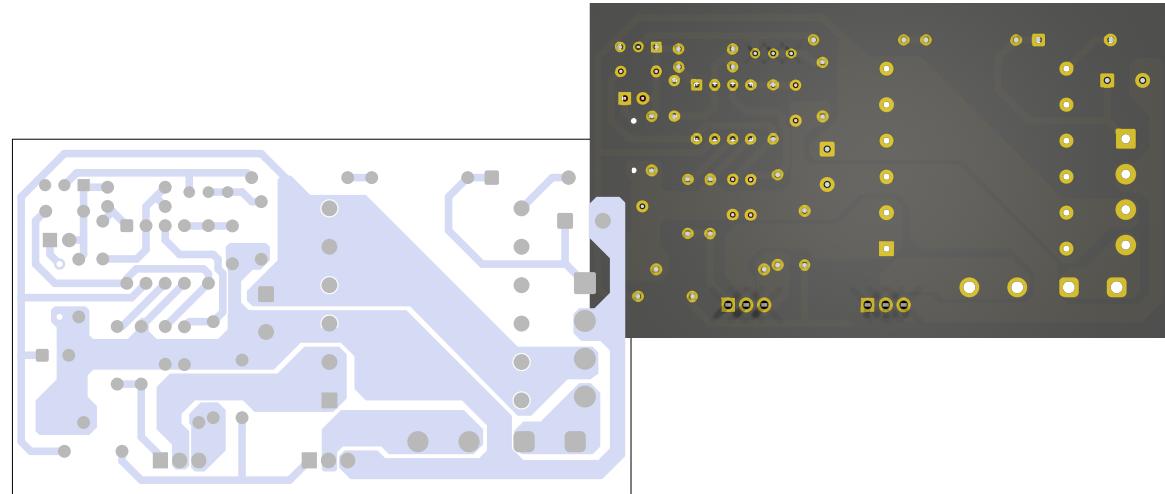
6

5

	Comments:	Company: <b>FR</b>	Variant: PRELIMINARY	Git Hash: 7d63e9b
		Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 45V</b>	
	Sheet Title: Top Assembly (Scale 1:1)	File Name: StepUp_module_12to450V.kicad_pcb	Designer: FR	Date: 2024-04-13 Revision: + (Unreleased)
	Sheet Path:		Reviewer:	Size: <b>A4</b> Sheet: <b>2 of 3</b>

# Module Assembly Document

**Bottom Assembly (Scale 1:1)**



		Comments:  	Company: <b>FR</b>	Variant: <b>PRELIMINARY</b>	Git Hash: <b>7d63e9b</b>	
		Board Name: <b>Module</b>	Project Name: <b>Alixp Step up module 12 to 450 V</b>			
		Sheet Title: Bottom Assembly (Scale 1:1)	File Name: StepUp_module_12to450V.kicad_pcb	Designer: FR	Date: 2024-04-13	Revision: + (Unreleased)
		Sheet Path:		Reviewer:	Size: <b>A4</b>	Sheet: <b>3 of 3</b>