

Module

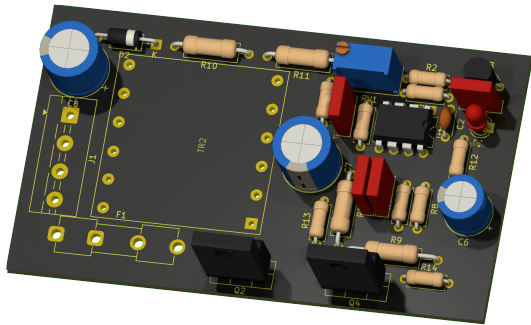
Rev + (Unreleased)

Variant: DRAFT

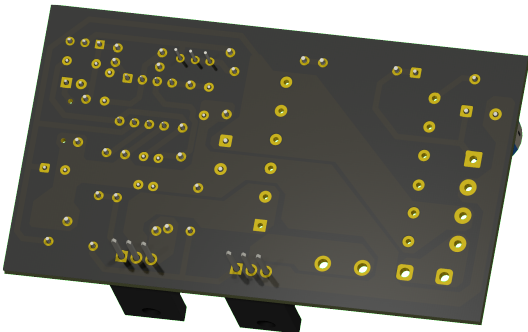
2025-12-12

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as X

DRAFT - Very early stage of schematic, ignore details.
PRELIMINARY - Close to final schematic.
CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.
RELEASED - A board with this schematic has been sent to production.

Date: 12-Dec-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for
informational design
notes.

DESIGN NOTE:
Example text for
debug notes.

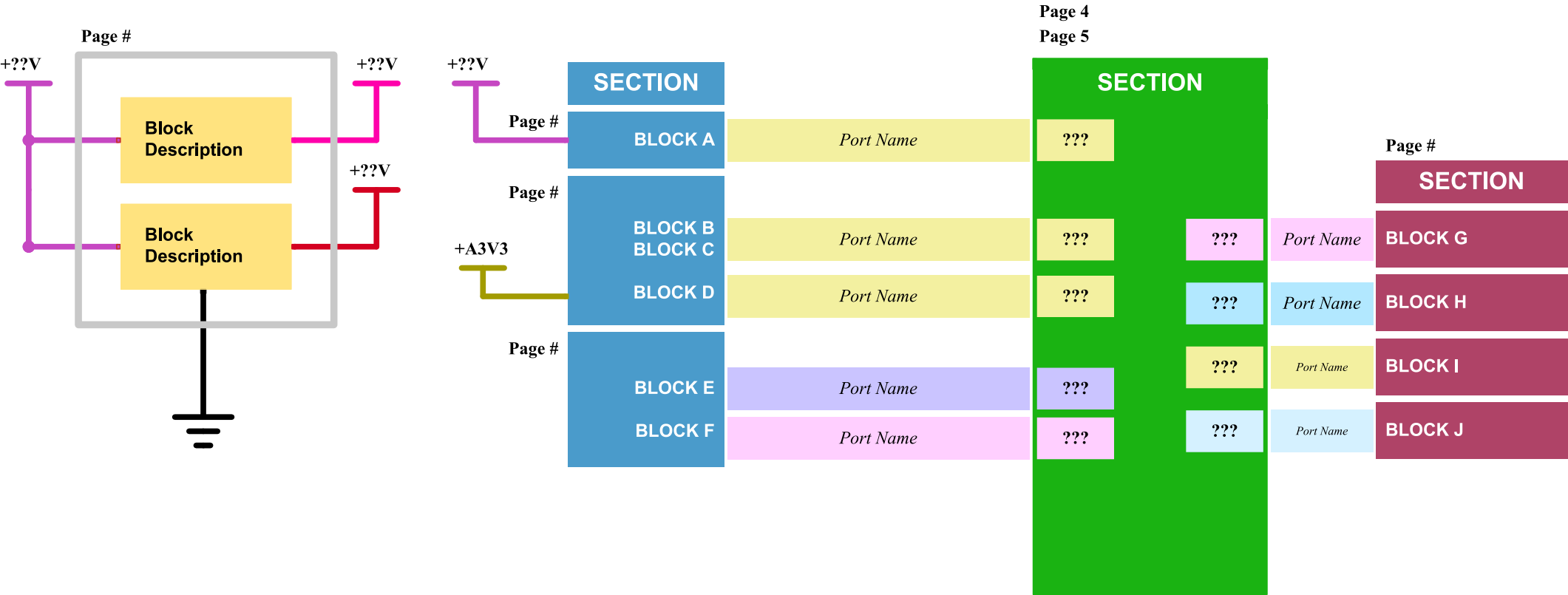
DESIGN NOTE:
Example text for
cautionary design
notes.

DESIGN NOTE:
Example text for
critical design notes.

LAYOUT NOTE:
Example text for
critical layout
guidelines.

	Comments:	Company: FR		Variant: DRAFT	Git Hash: 3e65155
		Board Name: Module		Project Name: Alixp Step up module 12 to 450 V	
	Sheet Title:	File Name: StepUp_module_12to450V.kicad_sch	Designer: FR	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /		Reviewer:	Size: A3	Sheet: 1 of 7

[2] Block Diagram



Target specifications:	
Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

	Comments:	Company:	Variant:	Git Hash:	
		FR	DRAFT	3c65155	
		Board Name:	Project Name:		
		Module	Alixp Step up module 12 to 450 V		
	Sheet Title:	File Name:	Designer:	Date:	Revision:
Block Diagram	Block Diagram.kicad_sch	FR	2025-01-12	+ (Unreleased)	
Sheet Path:		Reviewer:	Size:	Sheet:	
/Block Diagram/			A3	2 of 7	

F	[3] Project Architecture							
E								
D								
C								
B								
A								

Section A - Title A

Page 4

File: Section A - Title A.kicad_sch

Description

Section B - Title B



Page 5

File: Section B - Title B.kicad_sch

Description

	Comments:	Company:		Variant:	Git Hash:
		FR		DRAFT	3e65155
	Sheet Title:	Board Name:		Project Name:	
		Project Architecture		Alixp Step up module 12 to 15	
	Sheet Path:	File Name:	Designer:	Date:	Revision:
	/Project Architecture/	Project Architecture.kicad_sch	FR	2025-01-12	+ (Unreleased)
			Reviewer:	Size:	Sheet:
				A3	3 of 7

[illegible][illegible][illegible]

A	<h1>[3] Project Architecture</h1>										
B											
C				<div> <div>Description</div> <div> <div>Section A - Title A</div>  <div>Page 4</div> <div>File: Section A - Title A.kicad_sch</div> </div> </div>							
D				<div> <div></div> <div> <div>Section B - Title B</div>  <div>Page 5</div> <div>File: Section B - Title B.kicad_sch</div> </div> <div>Description</div> </div>							
E											
F											
						Comments:	Company: FR		Variant: DRAFT	Git Hash: 3e65155	
							Board Name: Module		Project Name: Alixp Step up module 12 to 24VDC		
						Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: FR	Date: 2025-01-12	Revision: + (Unreleased)	
						Sheet Path: /Project Architecture/		Reviewer:	Size: A3	Sheet: 3 of 7	

[3] Project Architecture

Description { Section A - Title A
Page 4
File: Section A - Title A.kicad_sch

{ Section B - Title B
Page 5
File: Section B - Title B.kicad_sch } Description

	Comments:	Company: FR	Variant: DRAFT	Git Hash: 3e65155
		Board Name: Module	Project Name: Alixp Step up module 12 to 7V	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: FR	Date: 2025-01-12
	Sheet Path: /Project Architecture/		Reviewer:	Revision: + (Unreleased)
			Size: A3	Sheet: 3 of 7

[illegible][illegible]

F	[3] Project Architecture							
E								
D								
C								
B								
A								

Section A - Title A

Page 4

File: Section A - Title A.kicad_sch

Description

Section B - Title B

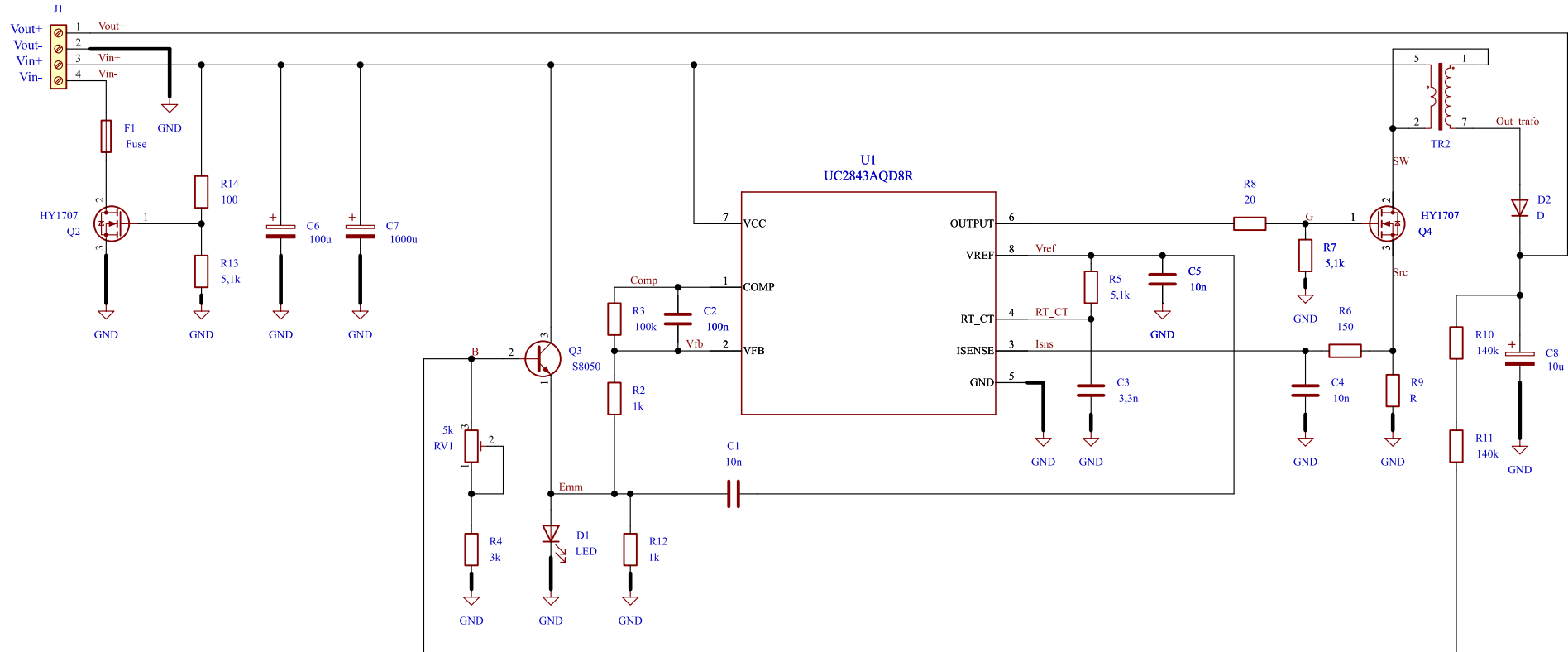
Page 5

File: Section B - Title B.kicad_sch

Description

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			FR		DRAFT	3e65155
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	Module					
	Sheet Title:	File Name:	Designer:	Date:	Revision:	
	Project Architecture	Project Architecture.kicad_sch	FR	2025-01-12	+ (Unreleased)	
	Sheet Path:	Reviewer:	Size:	Sheet:		
	/Project Architecture/		A3	3 of 7		

[4] Sheet Title A



	Comments: AliExpress module		Company: FR		Variant: DRAFT	Git Hash: 3e65155
			Board Name: Module		Project Name: Alixp Step up module 12 to 450 V	
	Sheet Title: Sheet Title A		File Name: Section A - Title A.kicad_sch	Designer: FR	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/Section A - Title A/			Reviewer:	Size: A4	Sheet: 4 of 7

[5] Sheet Title B

DESIGN NOTE:
blabla

DESIGN NOTE:
blabla

LAYOUT NOTE:
blabla

Block Description

Block Description

Block Description

Block Description

Block Description

Block Description

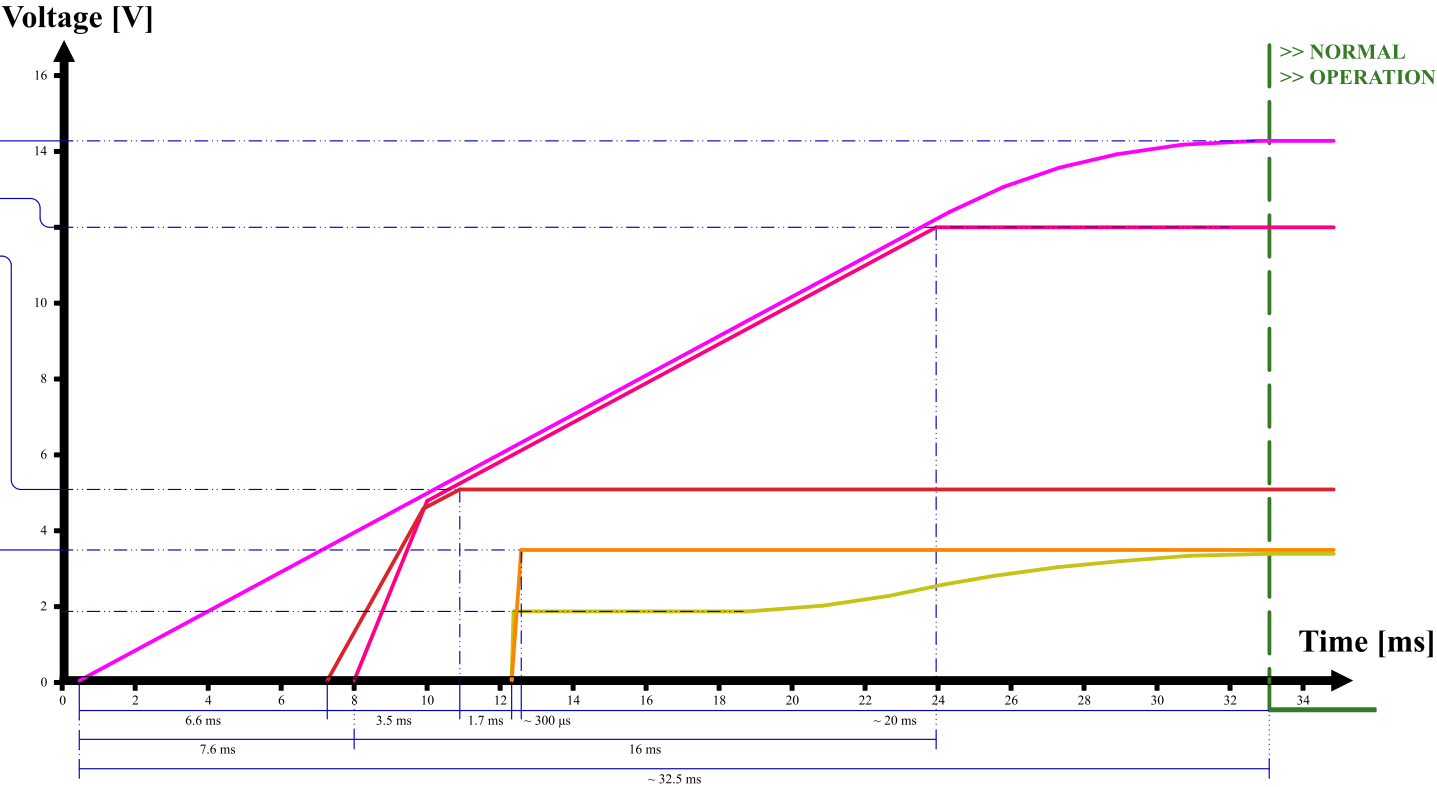
Block Description

Block Description

Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: FR		Variant: DRAFT	Git Hash: 3e65155
	Board Name: Module		Project Name: Alixp Step up module 12 to 450 V	
Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: FR	Date: 2025-01-12	Revision: + (Unreleased)
Sheet Path: /Project Architecture/Section B - Title B/		Reviewer:	Size: A3	Sheet: 5 of 7

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company: FR	Variant: DRAFT	Git Hash: 3e65155
		Board Name: Module	Project Name: Alixp Step up module 12 to 450 V	
	Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: FR	Date: 2025-01-12
	Sheet Path: /Power - Sequencing/		Reviewer:	Revision: + (Unreleased)
			Size: A4	Sheet: 6 of 7

[7] Revision History

	Comments:	Company: FR		Variant: DRAFT	Git Hash: 3e65155	
		Board Name: Module		Project Name: Alixp Step up module 12 to 450 V		
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: FR	Date: 2025-01-12	Revision: + (Unreleased)	
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 7 of 7	