Title: **Modelling of Open Circuit Fault Analysis of 11 bus system using the Open IPSL**

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Model Abstract:

The simulation represents Open Circuit Fault Analysis of 11 bus system. Open circuit faults have been simulated on standard IEEE 11 bus system. State of the system before and after the fault has been estimated in terms of bus voltages and line losses by solving load flow. Open circuit conditions have been simulated on the lines one by one and their effect on the bus voltages has been observed and most critical lines are sorted out. The power system model consists of 5 generators, 11 buses, 6 loads, 18 lines. The system is on a 100 MVA base. The model submitted is implemented in Modelica language using Open IPSL package. Simulation obtained shows voltage profiles at various buses.