the circuit of given figure can be used for BCD D/A converter. The binar y inputs corresponding to LSB are applied to b3,b2,b1,b0 and those corresponding to the next digit at b7,b6,b5,b4. the value of r is c hosen so as to make the input current of OP-AMP corresponding to LS D as 1/10th of that of current due to MSD, and is given by

$$((V(R)*(8/7*R))/(R*(r+8*R/7)+r*8/7*R))=V(R)/(10*R)$$

r=4.8R