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This is a project log entry for [FPGA Computer/Eval board](#)

Defect list

I guess I should start maintaining a defect list:

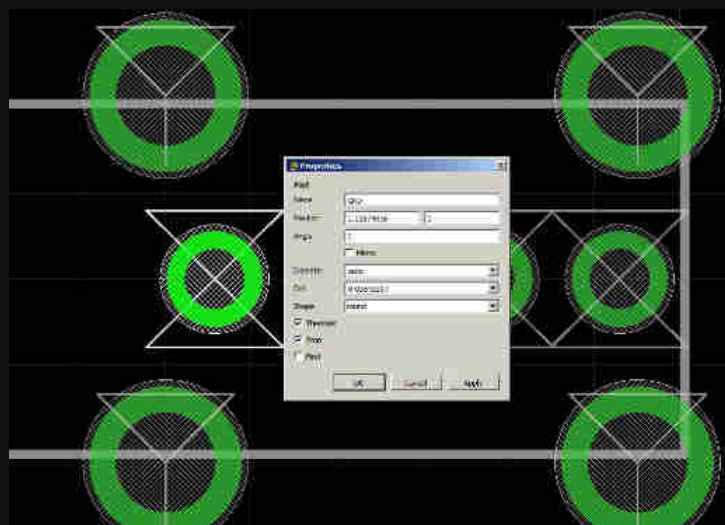
Library: con-usb-2.lbr by Erik Christiansson; also con-usb-3.lbr by same author

Level: Serious, require localized copper change

Problem has not been resolved in con-usb-3.lbr at Cadsoft ftp.

Acon UAR80-4* datasheet clearly shown pin 1 location (USB Bus Power), but instead it is connected to ground. Seems like all the pin numbering is wrong, so all pins are affected.

Foorprint:



USB 2.0 standard:

Table 6-1 provides the standardized contact terminating assignments by number and electrical value for Series "A" and Series "B" connectors.

Contact Number	Signal Name	Typical Wiring Assignment
1	VBUS	Red
2	D-	White
3	D+	Green
4	GND	Black
Shell	Shield	Drain Wire

From a mechanical strength/appearance point of view, I should keep the connector as is, USB signals are best reversed at RN19 to preserve the routing. Power/ground can be reverse by cutting trace and thermals next to connector pin and jumper by magnetic wire.

