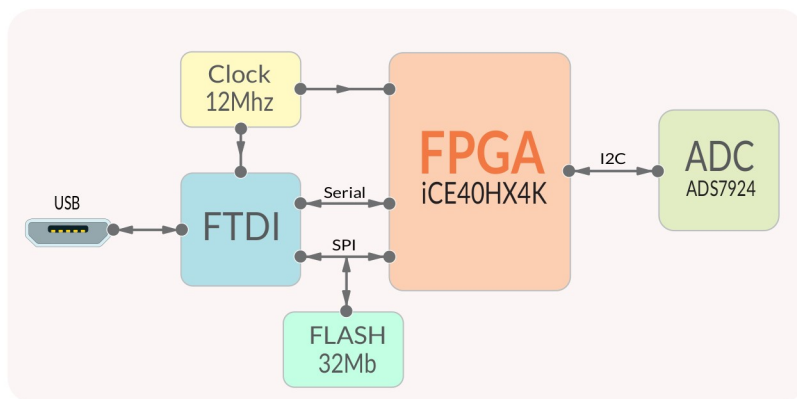


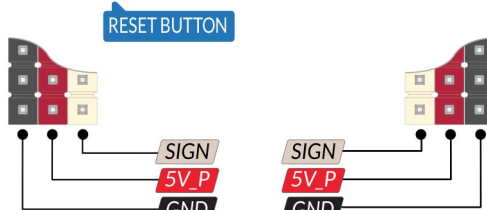
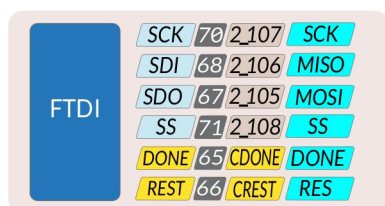
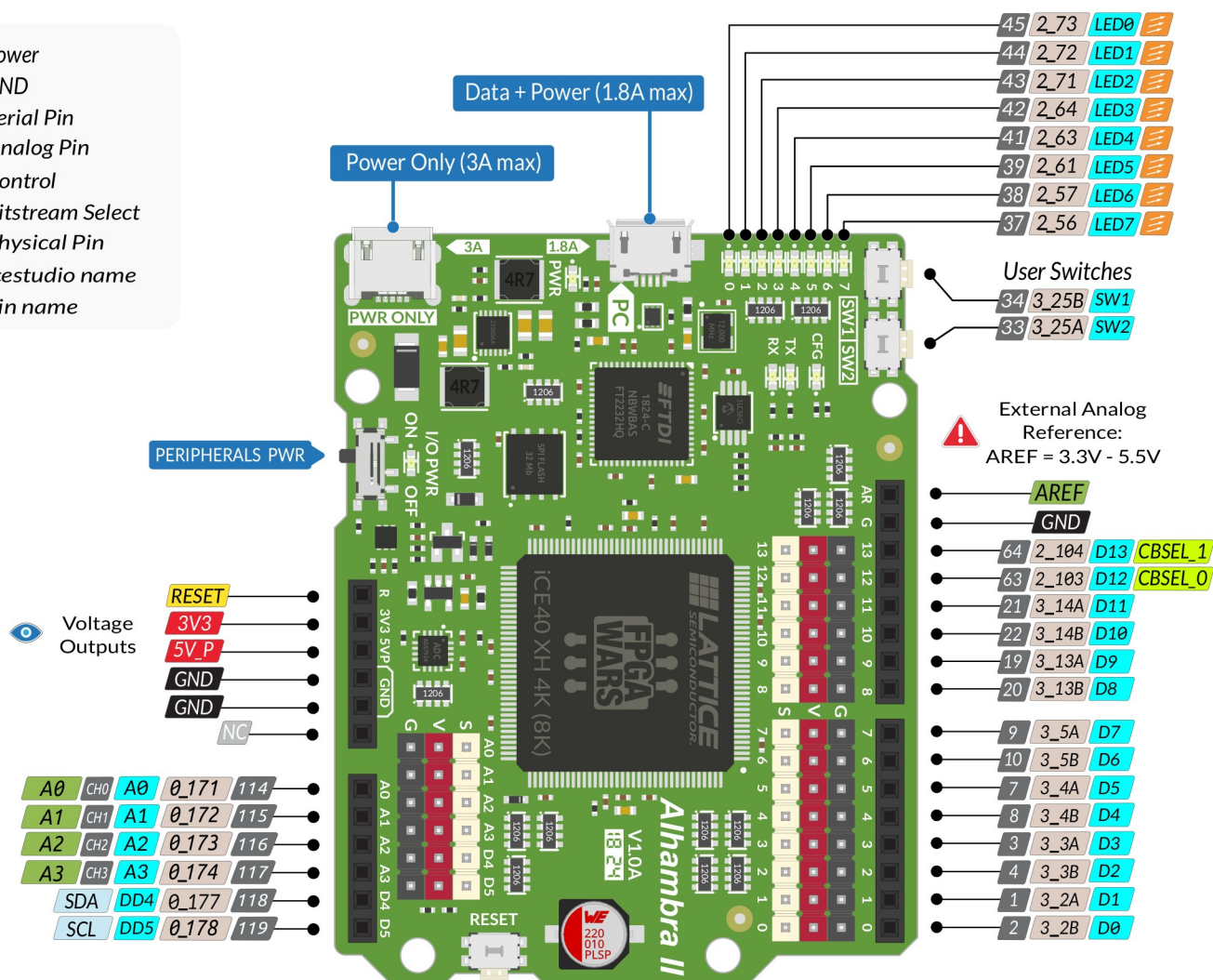
# Alhambra II V1.0A

## Open FPGA Board

- ⚠ Maximum Supply Voltage: 5.5V
- 👁 Operating Supply Voltage: 3.5 - 5.5V
- ✅ 3.3V GPIO, 5V Compatible
- 👁 Each GPIO Includes a 200 ohm Serial Resistor
- ✅ ADC Internal to External Ref. Automatic Switching

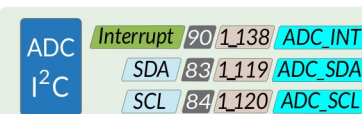


- Power
- GND
- Serial Pin
- Analog Pin
- Control
- Bitstream Select
- Physical Pin
- Icestudio name
- Pin name



👁 Max 4.8A for all 5V\_P Pins

**CLK** Clock 12Mhz 49 2\_81



@EladioDM  
@MariaDM65

30 DEC 2018  
ver 1.0 rev 2

