① ALFA 16 bit

- 1 bit x SEGNO
- 8 bit x ESPO ECC 128
- 7 bit x MANT

- 64,5 IN ALFA

64,5 → 64 32 16 8 4 2 1 0,5
1 0 0 0 0 0 0 , 1

1000000,1

- SEGNO: 1
- ESP: 6 + 128 = 10000110
- MANT: 0000001

0110 +
10000000
10000110

② $E_A = 0$

③ GGF1 ALFA IN ECC 512 10 bit

G G F 1
0|100 0100 1|111 0001
7 3 0123 7

$2^3 + 2^0 = 9$

$2^9\left(2^{-1}+2^{-2}+2^{-3}+2^{-3}\right) = 2^8+2^7+2^6+2^2-2^9 = 1111000100$

④ G160 ALFA IN DEC

G 1 6 0
0|100 0001 0|110 0000

- SEGNO: +
- ESP: 100 00010 -
  10000000 =
  00000010

$2^1 = 2$

$2\left(2^0+2^{-1}+2^{-2}\right) = 2^2+2^1+2^0 = 7$

⑤ RAID 1 TB STRIP 512 KB

512 GB

⑥ 2 TB

⑦ 256 GB

⑧ NUM STRIP RAID LVL 0 32 DISCHI

1024 : 32 = 32 GB

32 GB = 2^{35}

GB = 2^{30}

$\frac{32\ GB}{512\ KB} = \frac{2^{35}\ B}{2^{19}\ B} = 2^{16} = 65536$

⑨ A ⑩ F ⑪ B

⑫

A	B	C	A AND B	A AND B AND C (U1)	U1 NOR A (U2)	U2 OR B
0	0	0	0	0	1	1
0	0	1	0	0	1	1
0	1	0	0	0	1	1
0	1	1	0	0	1	1
1	0	0	0	0	0	0
1	0	1	0	0	0	0
1	1	0	1	0	0	1
1	1	1	1	1	0	1

B

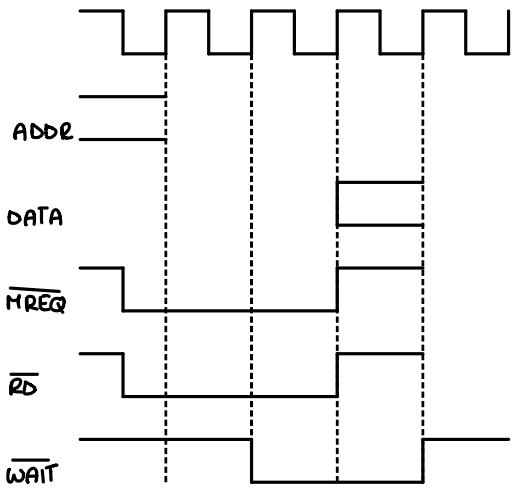
⑬

A	B	Y
0	0	0
0	1	1
1	0	1
1	1	1

(NOT A AND B) OR (A AND NOT B) OR (A AND B)

⑮ BUS SINCRONO 200 MHz
LINEE SEPARATE
T RISP 15 msec DA INDIR STABILI

1000/200 = 5 msec



⑭ 10 msec
⑯ 17,5 msec
⑰ 20 msec
⑱ 30 msec

① -64,5

ALFA 16 bit
- 1 bit x SEGNO
- 8 bit x ESPO ECC 128
- 7 bit x MANT

- SEGNO 1

64,5 → 1000000,1

- ESP = 6 → 128 = 10000110

```
  0110 +
10000000
--------
10000110 =
```

- MANT: 0000001

② EA=0

③ 64F1 ALFA in ECC 512 10 bit

```
 G    G    F    1
0100 0100 1111 0001
```

~~0000~~1001 = 9

```
10001001 -
10000000 =
--------
0000 1001
     9
```

$2^9(2^{-1}+2^{-2}+2^{-3}+2^{-7})$

$2^8+2^7+2^6+2^2-2^{-9}$

1111000100

④ G160 HEX IN ALFA

```
 G    1    6    0
0100 0001 0110 0000
```

2^2

$2^2(2^{-1}+2^{-2}+2^0) = 2^1+2^0+2^2 = 7$

⑤ RAID 1 TB
STRIP 512 KB

512 GB

⑥ 2 TB

⑧ $32/512 \cdot 2^{20} = 65536$

⑦ 256 GB

⑫

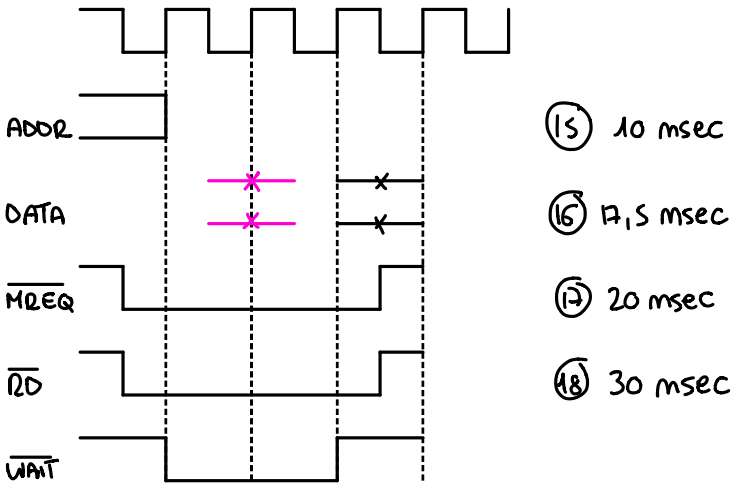
A	B	C	U1: A AND B AND C	U2: U1 NOR A	U2: U2 OR B
0	0	0	0	1	1
0	0	1	0	1	1
0	1	0	0	1	1
0	1	1	0	1	1
1	0	0	0	0	0
1	0	1	0	0	0
1	1	0	0	0	1
1	1	1	1	0	1

(NOT A AND NOT B AND NOT C) OR (NOT A AND NOT B AND C) OR (NOT A AND B AND NOT C) OR (NOT A AND B AND C) OR (A AND B AND NOT C) OR (A AND B AND C)

A	B	C	A AND B AND C (U1)	NOT A OR C (U2)	U1 OR U2
0	0	0	0	1	1
0	0	1	0	1	1
0	1	0	0	1	1
0	1	1	0	1	1
1	0	0	0	0	0
1	0	1	0	1	1
1	1	0	0	0	0
1	1	1	1	1	1

⑮ BUS SINCRONO 200 MHz LINEE SEPARATE
15 msec lmdin stabili

1000/200 = 5 msec



⑮ 10 msec
⑯ 17,5 msec
⑰ 20 msec
⑱ 30 msec