```
$MODLP52
 1
 2
    org 0000H
 3
       ljmp
             setup
 5 ; Timer/Counter 0 overflow interrupt vector
 6
   org 0x000B
 7
        ljmp Timer0_ISR
 8
9
   ; imports
10 $include(math32.inc)
    $include(macros.inc)
11
12
    ;$include(LCD_4BIT.inc)
13
14 CLK equ 22118400
15 BAUD equ 115200
16 T1LOAD equ (0x100-CLK/(16*BAUD))
17
18 ; for the alarm
19 TOLOAD equ ((65536-(CLK/4096)))
20
21 ; pins for ADCs
22 ADC_CE equ
                      P2.0
23 ADC_MOSI equ
24 ADC_MISO equ
25 ADC_SCLK equ
                   P2.1
P2.2
P2.3
26
27
   ; pins for shift register
   LED_DATA equ P2.4
28
29 LED_LATCH equ
                     P2.5
                   P2.6
P2.7
30 LED_CLK equ
31 LED_CLR equ
32
33 DSEG at 30H
y:
37
            ds
                     4
38
39 BSEG
40 mf: dbit 1
41
42 CSEG
43
44 ;----;
45 ; Routine to initialize the ISR ;
46 ; for timer 0
47
   48 timer0 init:
; clear bits for the timer
     mov a, TMOD
anl a, #0xF0
orl a, #0x01
50
51
52
        mov TMOD, a
53
54
   ; set reload value

mov TH0, #high(

mov TL0, #low(TO
55
       mov TH0, #high(T0LOAD)
mov TL0, #low(T0LOAD)
56
57
58
      ; enable interrupts
59
60
        setb ET0
               TR0
61
        setb
62
        ret
63
```

```
64
     ; ISR for timer 0. Set to execute;
 65
      ; every 1/4096Hz to generate a ;
 66
 67
      ; 2048 Hz square wave at pin P3.7 ;
 68
 69
      timer0_ISR:
 70
          ; operating in mode 1, reload the timer
 71
          clr
                   TR0
 72
          mov
                   TH0,
                           #high(T0LOAD)
 73
                  TL0,
                           #low(TOLOAD)
          mov
 74
          setb
                   TR0
 75
                   P3.7
          cpl
 76
          reti
 77
 78
      ; configure serial port and baudrate using timer 1
 79
      InitSerialPort:
 80
          ; debounce reset button
 81
          mov
                  R1,
                           #222
 82
          mov
                   R0,
                           #166
 83
          djnz
                  R0,
 84
                           $-4
          djnz
                   R1,
 85
          ; set timer
 86
          clr
                  TR1
 87
          anl
                  TMOD,
                           #0x0f
 88
                  TMOD,
                           #0x20
          orl
 89
                  PCON,
          orl
                           #0x80
 90
                  TH1,
                           #T1LOAD
          mov
 91
          mov
                  TL1,
                           #T1LOAD
 92
          setb
                  TR1
 93
                   SCON,
                           #0x52
          mov
 94
          ret
 95
 96
      ; send character using serial port
 97
      putChar:
 98
          jnb
                   TI,
                           putchar
 99
                   ΤI
          clr
                   SBUF,
100
          mov
                           a
101
          ret
102
103
     ; send a string using serial port
104
      putString:
105
          clr
                   a
106
                   a, @a+dptr
          movc
107
          jΖ
                  putString_return
108
                  putChar
          lcall
109
                  dptr
          inc
110
                  putString
          sjmp
111
      putString_return:
112
          ret
113
114
     ; initialize SPI
115
     SPIinit:
                  ADC_MISO
116
          setb
117
          clr
                   ADC_SCLK
118
          ret
119
120
      ; send byte in R0, receive byte in R1
      SPIcomm:
121
122
          push
                   ACC
123
                           #0
                   R1,
          mov
124
                   R2,
                           #8
          mov
125
      SPIcomm loop:
126
                           R0
          mov
                   a,
```

```
127
          rlc
                   R0,
128
          mov
                           a
                   ADC_MOSI,
129
          \mathbf{mov}
130
          setb
                   ADC_SCLK
                           ADC_MISO
131
          mov
                   С,
132
                           R1
          mov
                   a,
133
          rlc
                   a
134
                   R1,
          mov
                           a
                   ADC_SCLK
135
          clr
136
                           SPIcomm_loop
          djnz
                   R2,
137
                   ACC
          pop
138
          ret
139
140
     ; main program
141
     setup:
142
                   SP,
                           #7FH
          mov
143
          mov
                   PMOD,
                           #0
144
145
          ; initialize MCP3008
146
                   ADC_CE
          setb
147
          lcall
                   SPIInit
148
          lcall
                 InitSerialPort
149
          ; shift register
150
151
                   LED_DATA
          clr
152
                   LED LATCH
          clr
153
                   LED_CLK
          clr
154
          clr
                   LED_CLR
155
          sleep(#2)
156
          setb
                   LED_CLR
157
          ; timer initialization
158
159
          lcall
                   timer0_init
160
161
          ; enable global interrupts
162
          setb
                   EΑ
163
164
      ; loops forever
165
     loop:
166
          clr
                   ADC_CE
167
          ; starting bit is 1
                   R0,
168
          mov
                                #0x01
169
          lcall
                   SPIcomm
170
171
          ; read channel 0 & save to result, only care about lower 2 bits
           ; read xxxxxxRR xxxxxxxx
172
173
                                #0x80
                   R0,
          mov
174
          lcall
                   SPIcomm
175
                                R1
          mov
                   a,
176
          anl
                                #0x03
                   a,
177
          mov
                   result+1,
178
          ; read rest of 8-bits
179
180
          ; read xxxxxxxx RRRRRRRR
181
                   R0,
                                #0x55
                                            ; doesn't matter
          mov
182
          lcall
                   SPIcomm
183
                   result,
                                R1
          mov
                   ADC_CE
184
          setb
185
          sleep(#50)
186
          ; convert result into BCD
187
188
          mov
                   х,
                           result
                   x+1,
189
          mov
                           result+1
```

```
190
                  x+2,
                           #0x00
          mov
                  x+3,
191
                           #0x00
          mov
192
                  hex2bcd
          lcall
193
          mov
                  result,
                               bcd
194
                  result+1,
                               bcd+1
          mov
195
196
          ; ignore 0 values because it's not right
197
                  a, result
          mov
198
          jnz
                  loop_putBCD
199
          mov
                  a, result+1
200
          inz
                  loop_putBCD
201
          ljmp
                  loop
202
203
          ; toggle things into shift register
204
205
      loop_putBCD:
          ; print BCD for ADC value
206
207
          putBCD(result+1)
208
          putBCD(result)
209
          mov
                  a, \#' \ r'
                  putChar
210
          lcall
211
                  a, #'\n'
          mov
212
                  putChar
          lcall
213
          ; compute Vout
214
215
          ; x is already loaded
216
          Load_y(10)
217
          lcall sub32
218
          Load_y(49500)
                                            ; * 5000mV reference
219
          lcall mul32
          Load_y(1023)
220
                                           ; / 1023 ratio
          lcall div32
221
222
          Load_y(27300)
                                            ; - 2730mV voltage to convert to celcius
223
          lcall sub32
224
          ; clear sound
225
226
          clr TR0
227
228
          ; output to LED
229
          ; < 10
230
          Load_y(1000)
                  x\_gteq\_y
231
          lcall
232
                          low1
          jb
                  mf,
233
          barLED(#1)
234
                 loop_end
          ljmp
235
      low1: ; 10 < t < 20
236
          Load_y(2000)
237
          lcall
                  x_gteq_y
                  mf,
238
          jb
                          low2
          barLED(#2)
239
240
          ljmp
                  loop_end
      low2: ; 20 < t < 25
241
242
          Load_y(2500)
243
          lcall
                  x_gteq_y
244
                 mf,
                         low3
          jb
245
          barLED(#3)
246
          ljmp
                 loop_end
247
      low3: ; 25 < t < 35
248
          Load_y(3500)
249
          lcall
                  x_gteq_y
250
                 mf,
                         low4
          jb
251
          barLED(#4)
252
          ljmp
                  loop_end
```

```
253
      low4: ; 35 < t < 45
254
          Load_y(4500)
255
          lcall
                x_gteq_y
256
          jb
                 mf,
                       med1
257
          barLED(#5)
258
          ljmp
               loop_end
259
      med1: ; 45 < t < 60
260
          Load_y(6000)
261
          lcall x_gteq_y
262
          jb
                 mf,
                         med2
263
          barLED(#6)
264
          ljmp
                 loop_end
      med2: ; 60 < t < 70
265
266
          Load_y(7000)
267
          lcall
                  x_gteq_y
268
                  mf,
          jb
                       high1
269
          setb
                  TR0
          barLED(#7)
270
271
          ljmp
                 loop_end
      high1: ; 70 < t < 80
272
273
          Load_y(8000)
274
          lcall
                  x_gteq_y
275
                  mf,
          jb
                         high2
276
          setb
                  TR0
277
          barLED(#8)
278
          ljmp
                  loop_end
279
      high2:
280
          setb
                  TR0
281
          ljmp
                  loop_end
282
      loop_end:
283
          ; print results to SPI
284
          lcall hex2bcd
285
          putBCD(bcd+1)
          putBCD(bcd)
286
287
288
          ; print terminating string
289
          mov
                  a, \#' \ r'
290
                  putChar
          lcall
                  a, #'\n'
291
          mov
292
                  putChar
          lcall
293
          ljmp
                  loop
294
      END
295
```