

# STUDENT ASSIGNMENT COVER SHEET



Wellington Institute of Technology  
Te Whare Wānanga o te Awakairangi

PLEASE COMPLETE ALL SECTIONS AND ATTACH TO YOUR ASSIGNMENT

SCHOOL

Engineering

First Name: Flynn  
Last Name: Stilwell

Student ID  
2180710

Programme: BEngTech - Electrical

Course: MG7013 Embedded Systems

Group

Assignment / Assessment: Logic Analyzer + Minimal Oscilloscope + Signal Generator

Due Date 10/07/2020

Lecturer / Tutor: Frank Beinersdorf

Declaration:  
This assignment  
is my own work.

Student's Signature: Flynn Stilwell

Date 08/07/2020

Received on behalf of the School by: \_\_\_\_\_ on:

(date stamp)

This part of the Assignment Cover Sheet is retained by the School as proof that your assignment was handed in).

Received on behalf of the School by: \_\_\_\_\_ on:

(date stamp)

Student please complete this section

First Name: [Enter Name]  
Last Name: [Enter Name]

Student ID  
[Enter ID]

Programme / Course: BEngTech - [Enter specialization]/[Enter course]

Assignment / Assessment: [Enter assignment]

Due Date [Enter date]