Fabrication notes for pad\_64\_in

(Square Anodes version 2)

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**Board stack info:** 6-layer board, 6 mm board,

The layers are organized as

1. Cu\_F ---- Signal (L1)
2. Cu\_ln1 ----Ground (L2)
3. Cu\_ln2 (empty plane) ---- Ground (L3)
4. Cu\_ln3 ---- Ground (L4)
5. Cu\_ln4 ---- Signal (L5)
6. Cu\_B ---- Ground (L6)

We have traces on Cu\_F and Cu\_ln4, they need to be 50 ohms impedance, including vias that connect the traces between these planes.

For traces on Cu\_F, now you can couple it to Cu\_ln1 to make it 50 ohms. Please make the distances between Cu\_F and Cu\_ln3 as far as possible.

I have adjusted the width of the vias, now they are >0.7mm.