

Sheet: /POWER/  
File: POWER.kicad\_sch

**Title:**

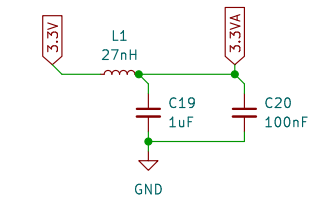
Size: A4

Date:

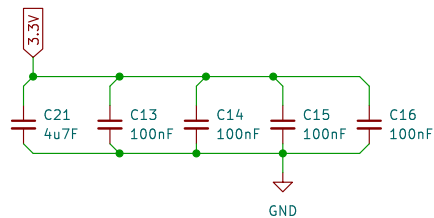
KiCad E.D.A. kicad 7.0.5

**Rev:**

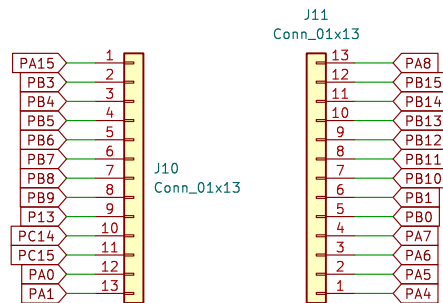
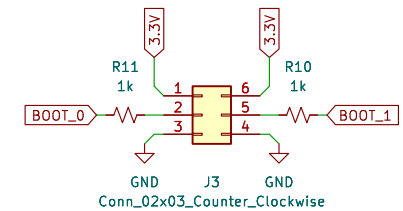
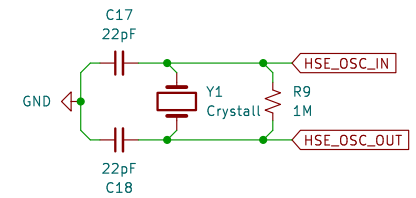
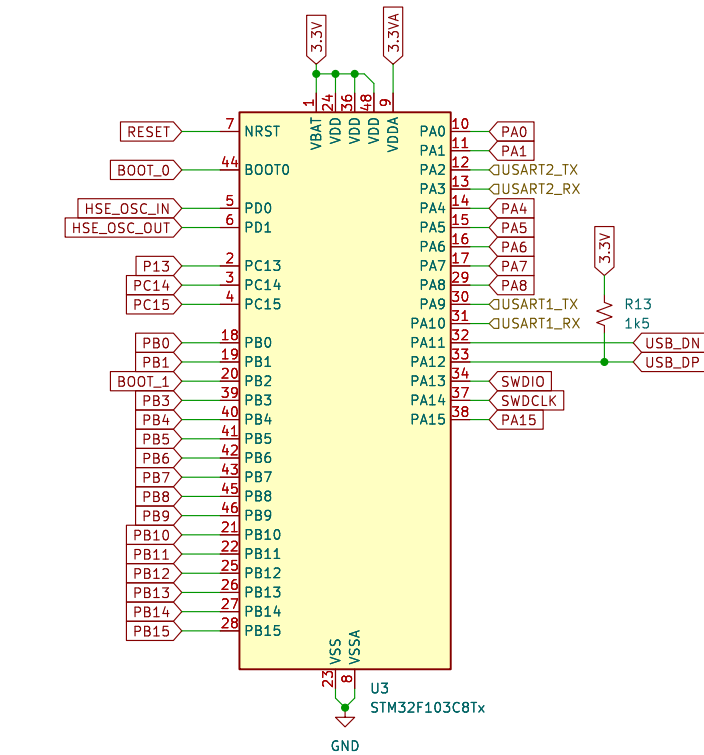
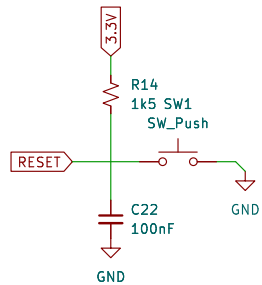
Id: 2/5



'Light' low-pass filter for Analog VDD



Decoupling Cap(\*100nF per VDD pin)



Sheet: /MCU/  
File: MCU.kicad\_sch

**Title:**

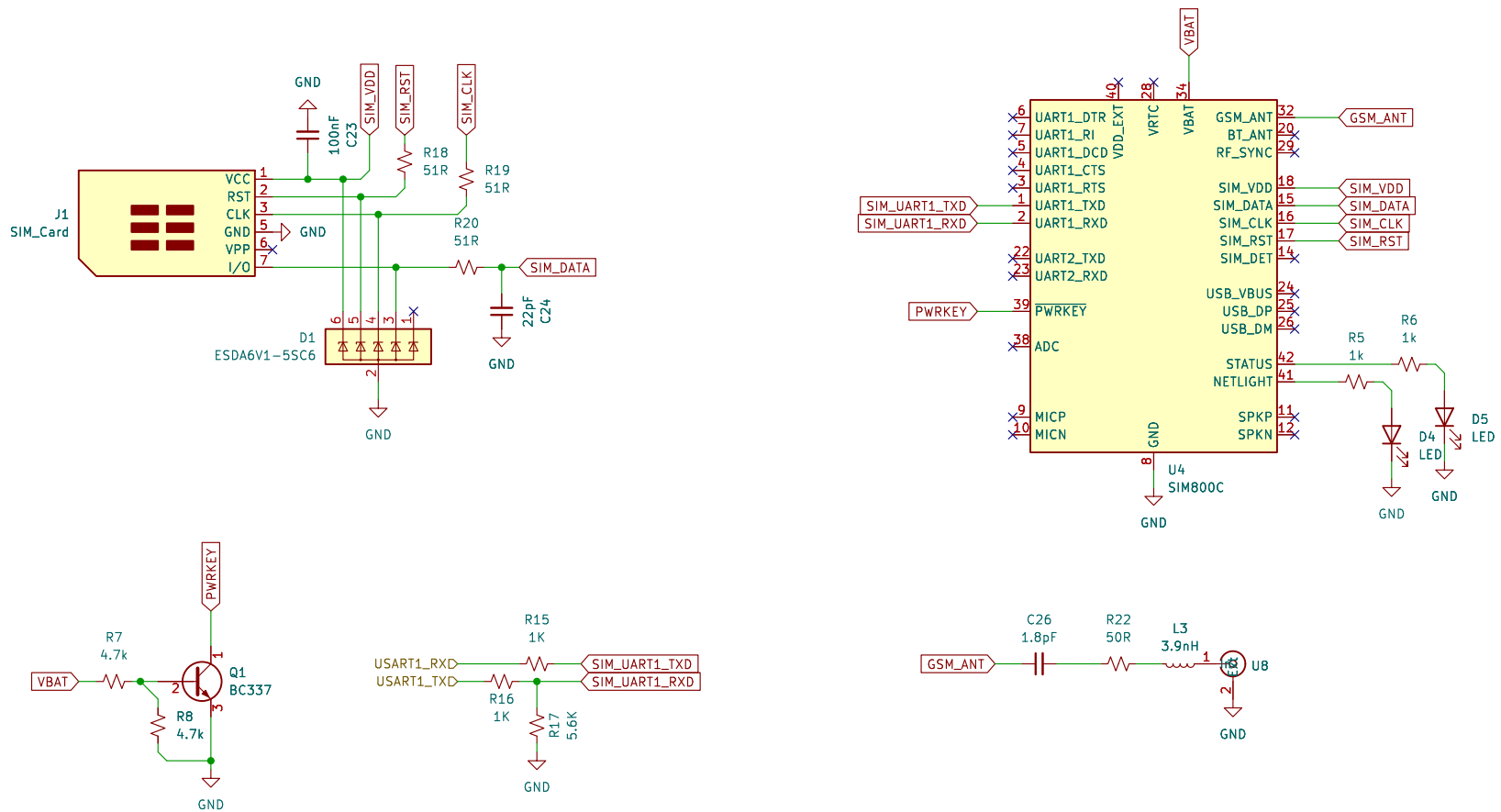
Size: A4

Date:

KiCad E.D.A. kicad 7.0.5

**Rev:**

Id: 3/5



Sheet: /SIM800C/  
File: SIM800C.kicad\_sch

**Title:**

Size: A4  
KiCad E.D.A. kicad 7.0.5

Date:

**Rev:**  
Id: 4/5

