

Sheet: /LoRa/
File: LoRa.kicad_sch

Title:

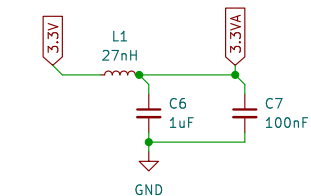
Size: A4

Date:

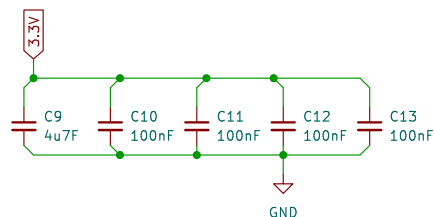
KiCad E.D.A. kicad 7.0.5

Rev:

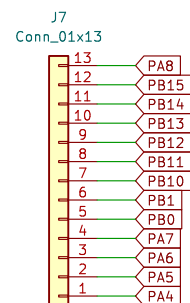
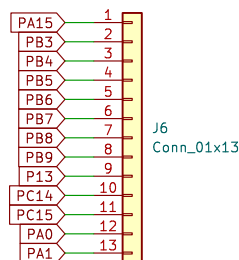
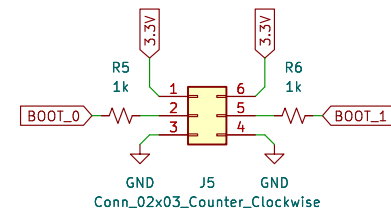
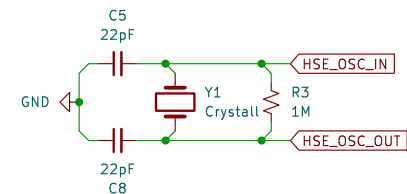
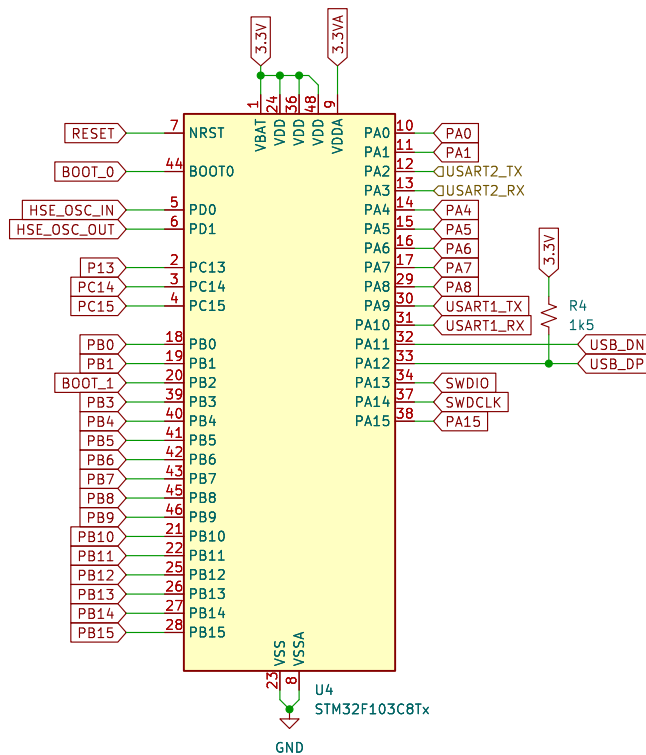
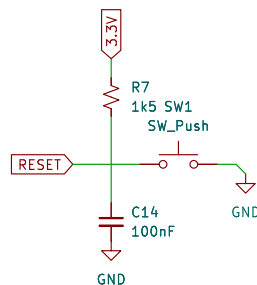
Id: 3/4



'Light' low-pass filter for Analog VDD



Decoupling Cap(+100nF per VDD pin)



Sheet: /MCU/
File: MCU.kicad_sch

Title:

Size: A4
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Id: 4/4