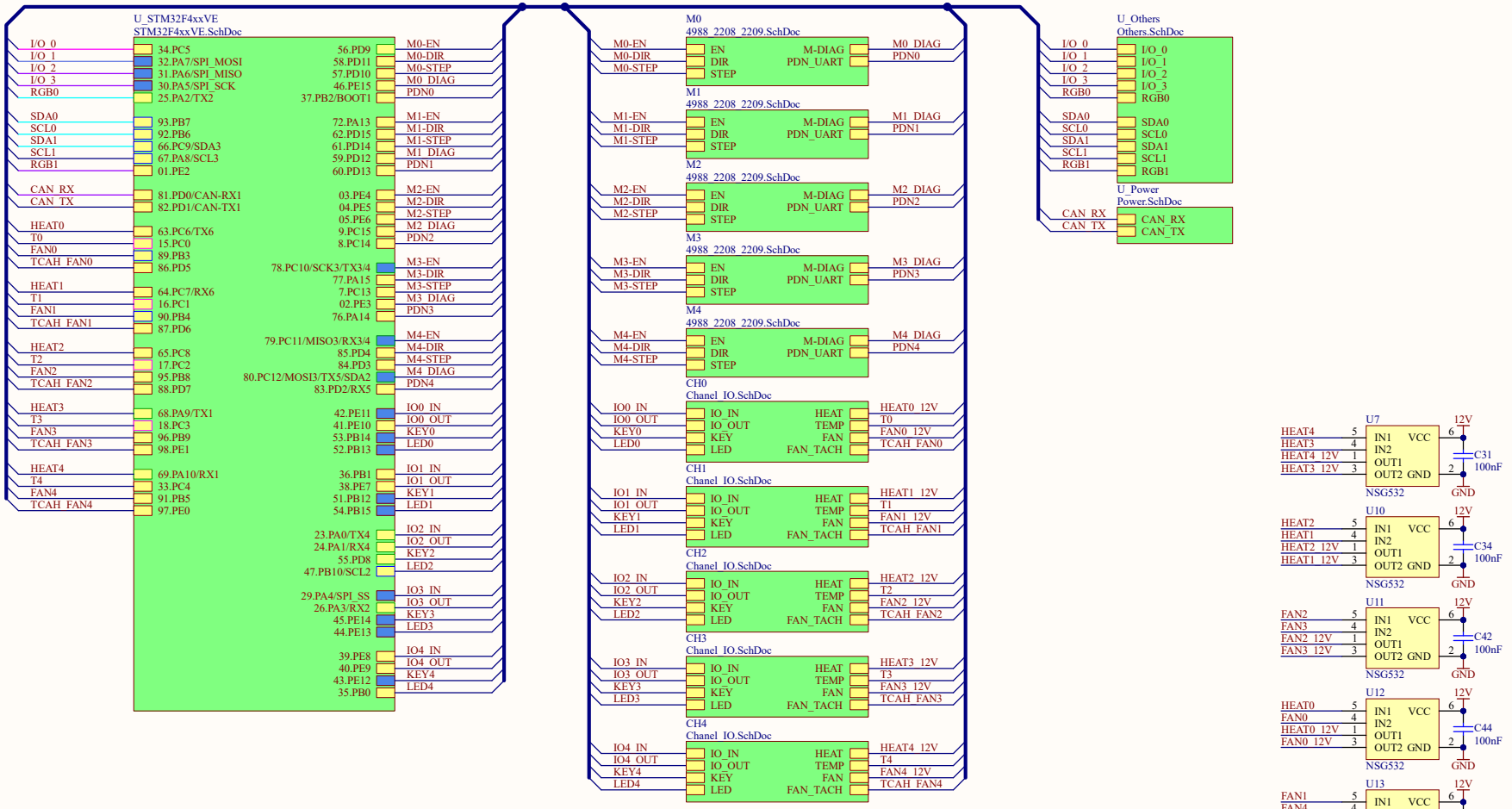

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Date: 2026/1/4	Author: *	Sheet 1 of 6	

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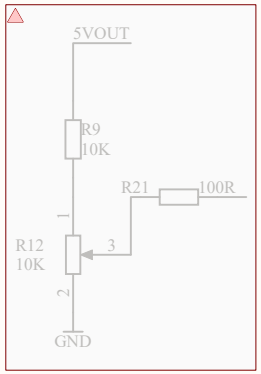

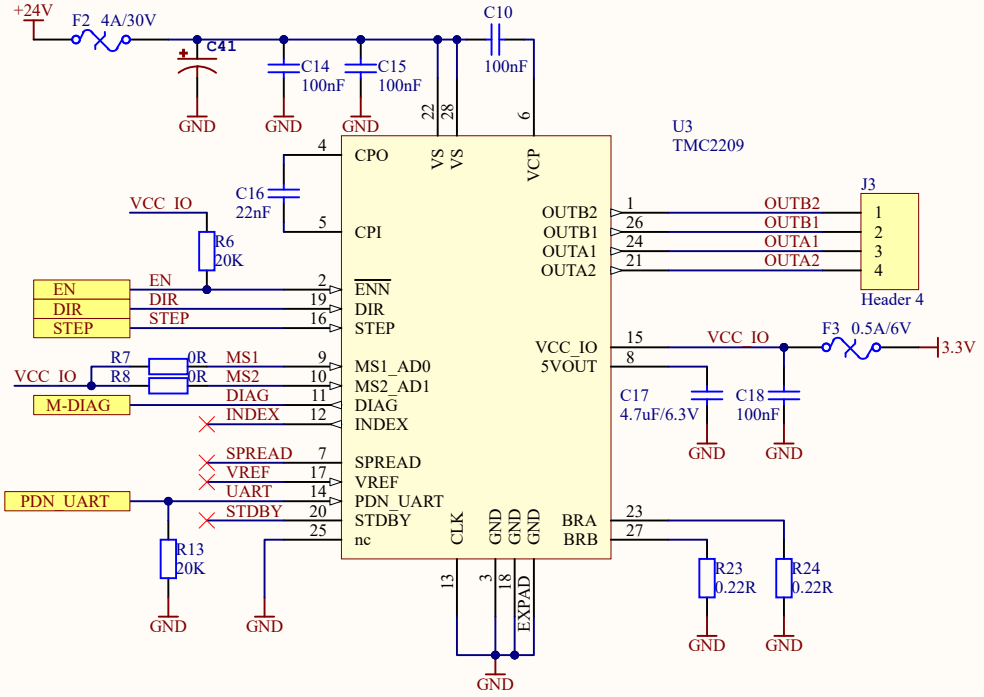

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Date: 2026/1/4	Author: *	Sheet 2 of 6

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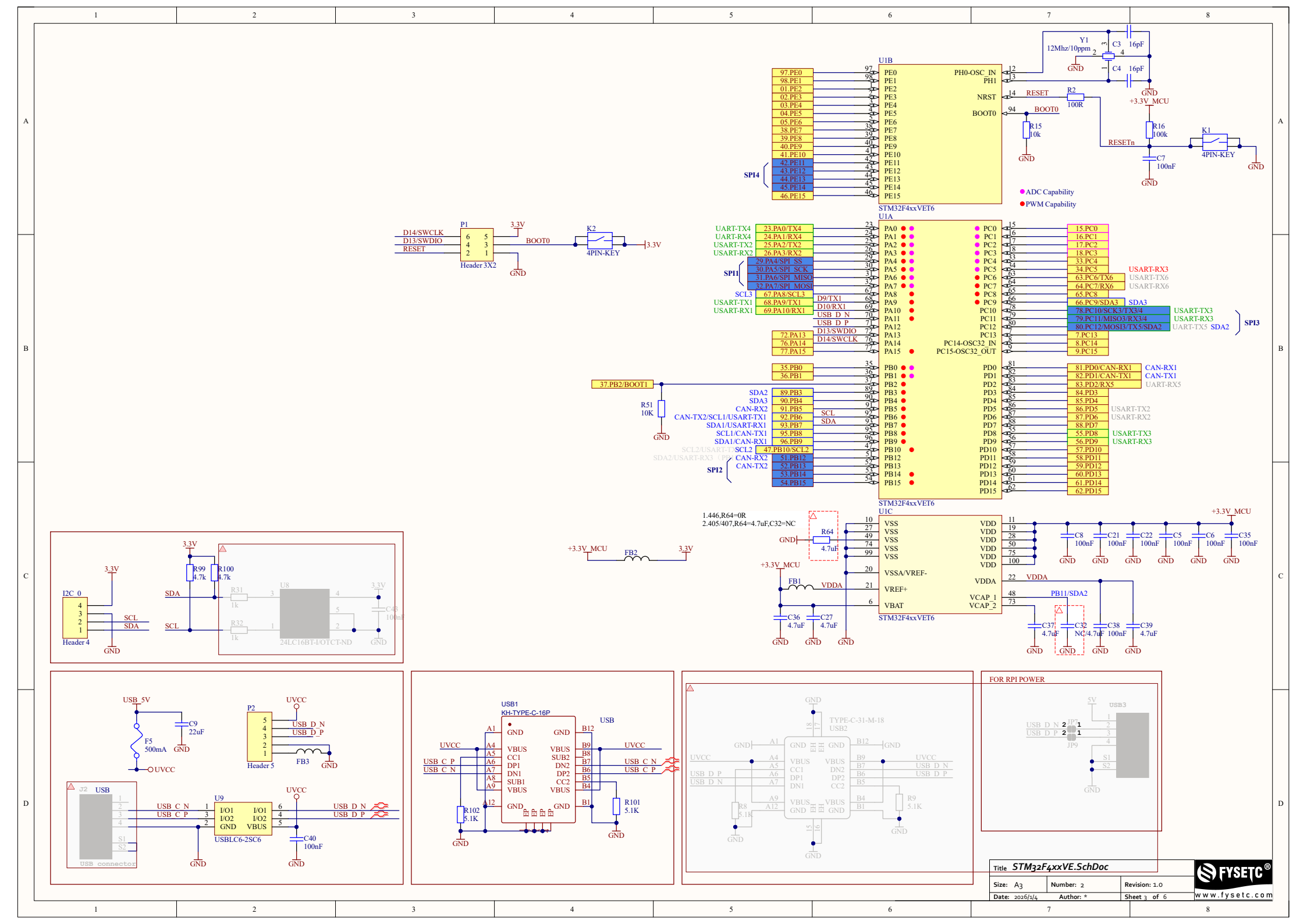
U1B
STM32F4xxVET6
U1A
STM32F4xxVET6
U1C
STM32F4xxVET6
Y1
12Mhz/10ppm
C3 16pF
C4 16pF
R2
100R
R15
10k
R16
100k
C7
100nF
K1
4PIN-KEY
RESET
BOOT0
+3.3V_MCU
GND
ADC Capability
PWM Capability
SPI4
SPI1
SPI2
SPI3
P1
Header 3X2
K2
4PIN-KEY
D14/SWCLK
D13/SWDIO
RESET
BOOT0
3.3V
R51
10K
37.PB2/BOOT1
FB1
FB2
R64
4.7uF
1.446,R64=0R
2.405/407,R64=4.7uF,C32=NC
VDDA
VBAT
VREF+
VSSA/VREF-
C8
C21
C22
C5
C6
C35
C36
C27
C37
C32
C38
C39
100nF
4.7uF
NC/4.7uF
I2C_0
Header 4
R99
4.7k
R100
4.7k
R31
1k
R32
1k
U8
24LC16BT-I/OTCT-ND
C43
100nF
USB_5V
F5
500mA
C9
22uF
P2
Header 5
FB3
J2 USB
USB connector
U9
USBLC6-2SC6
C40
100nF
USB1
KH-TYPE-C-16P
USB
R102
5.1K
R101
5.1K
TYPE-C-31-M-18
USB2
R8
5.1K
R9
5.1K
FOR RPI POWER
USB3
JP7
JP9
Title STM32F4xxVE.SchDoc
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Date: 2026/1/4
Author: *
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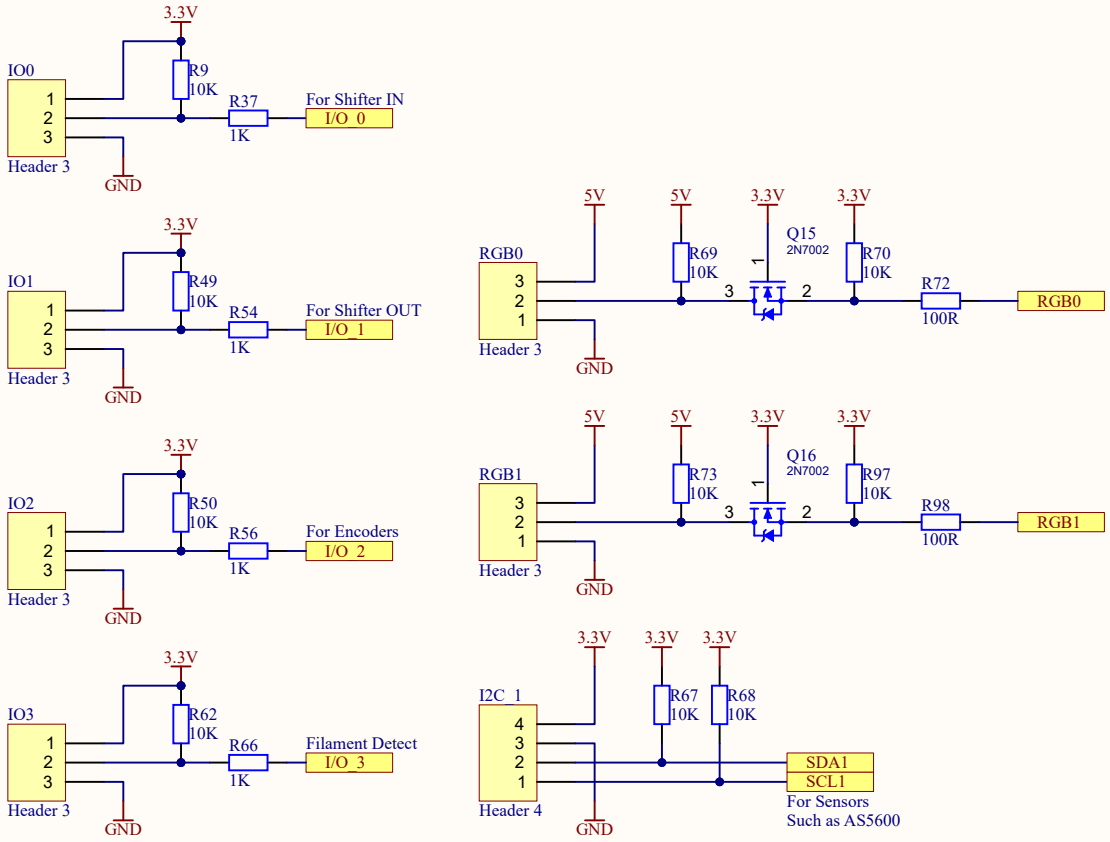
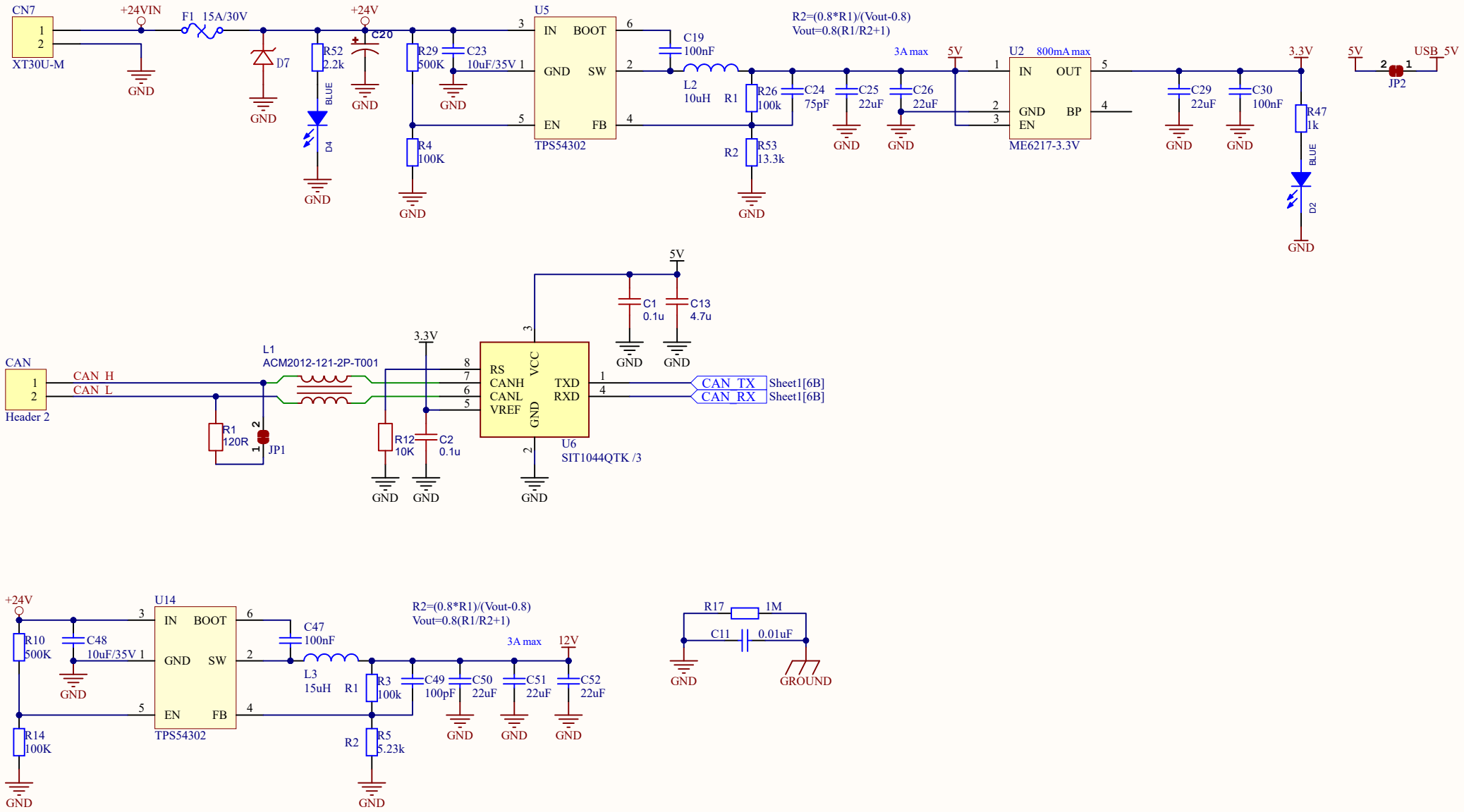
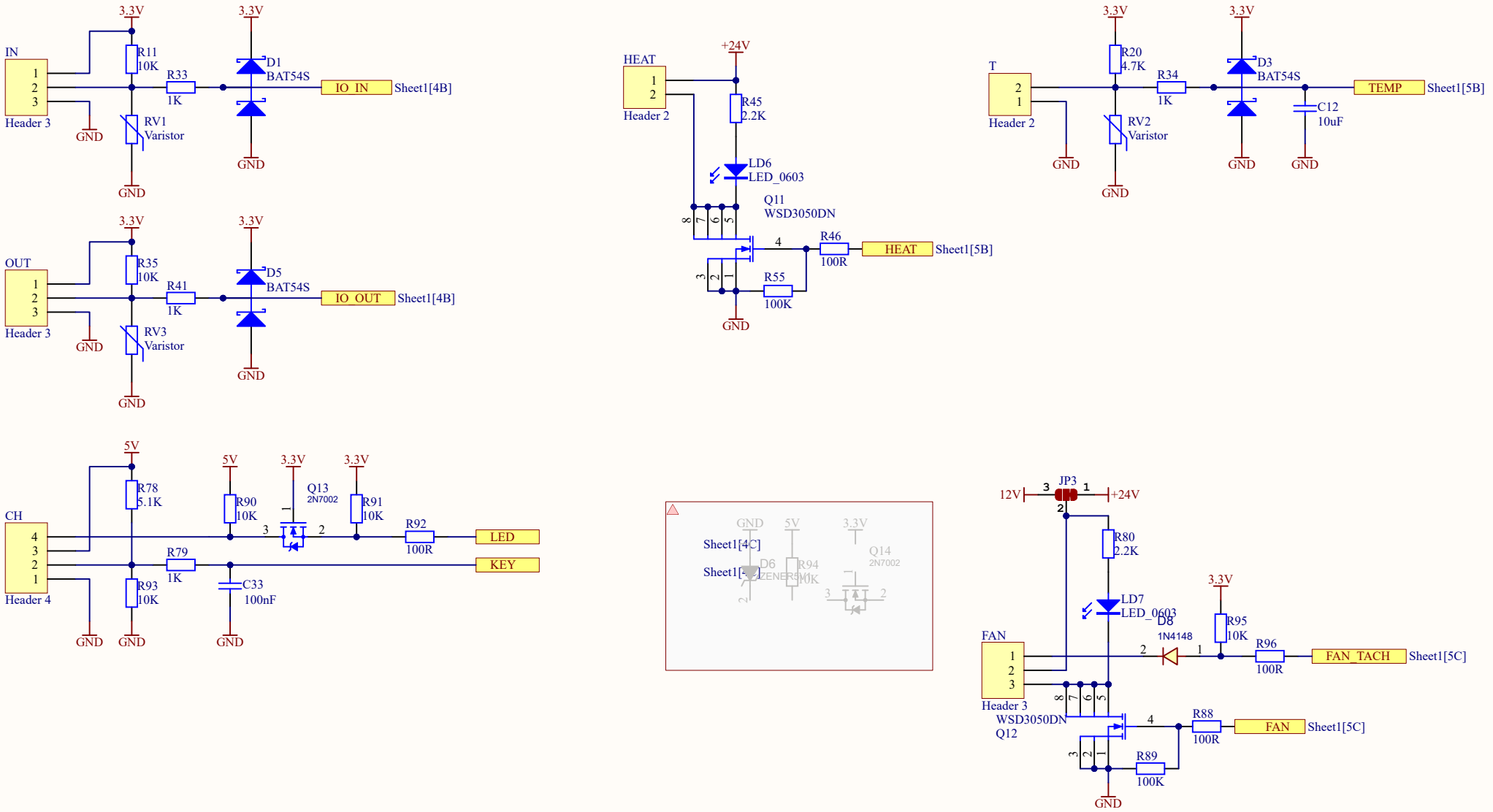

IO0
1
2
3
Header 3
3.3V
R9
10K
R37
1K
For Shifter IN
I/O_0
GND
IO1
1
2
3
Header 3
3.3V
R49
10K
R54
1K
For Shifter OUT
I/O_1
GND
IO2
1
2
3
Header 3
3.3V
R50
10K
R56
1K
For Encoders
I/O_2
GND
IO3
1
2
3
Header 3
3.3V
R62
10K
R66
1K
Filament Detect
I/O_3
GND
RGB0
3
2
1
Header 3
5V
5V
3.3V
3.3V
Q15
2N7002
R69
10K
R70
10K
R72
100R
RGB0
GND
RGB1
3
2
1
Header 3
5V
5V
3.3V
3.3V
Q16
2N7002
R73
10K
R97
10K
R98
100R
RGB1
GND
I2C_1
4
3
2
1
Header 4
3.3V
3.3V
3.3V
R67
10K
R68
10K
SDA1
SCL1
For Sensors
Such as AS5600
GND


CN7
XT30U-M
+24VIN
F1 15A/30V
D7
R52 2.2k
BLUE
D4
+24V
C20
R29 500K
C23 10uF/35V
R4 100K
U5
IN BOOT
GND SW
EN FB
TPS54302
C19 100nF
L2 10uH
R1
R26 100k
C24 75pF
R2
R53 13.3k
R2=(0.8*R1)/(Vout-0.8)
Vout=0.8(R1/R2+1)
3A max
5V
C25 22uF
C26 22uF
U2 800mA max
IN OUT
GND BP
EN
ME6217-3.3V
3.3V
C29 22uF
C30 100nF
R47 1k
BLUE
D2
5V
USB_5V
JP2
GND
CAN
Header 2
CAN_H
CAN_L
L1
ACM2012-121-2P-T001
R1 120R
JP1
3.3V
R12 10K
C2 0.1u
C1 0.1u
C13 4.7u
RS
CANH
CANL
VREF
VCC
GND
TXD
RXD
U6
SIT1044QTK /3
CAN_TX
CAN_RX
Sheet1[6B]
+24V
R10 500K
C48 10uF/35V
U14
IN BOOT
GND SW
EN FB
TPS54302
C47 100nF
L3 15uH
R3 100k
C49 100pF
C50 22uF
C51 22uF
C52 22uF
12V
R14 100K
R5 5.23k
R17 1M
C11 0.01uF
GROUND



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