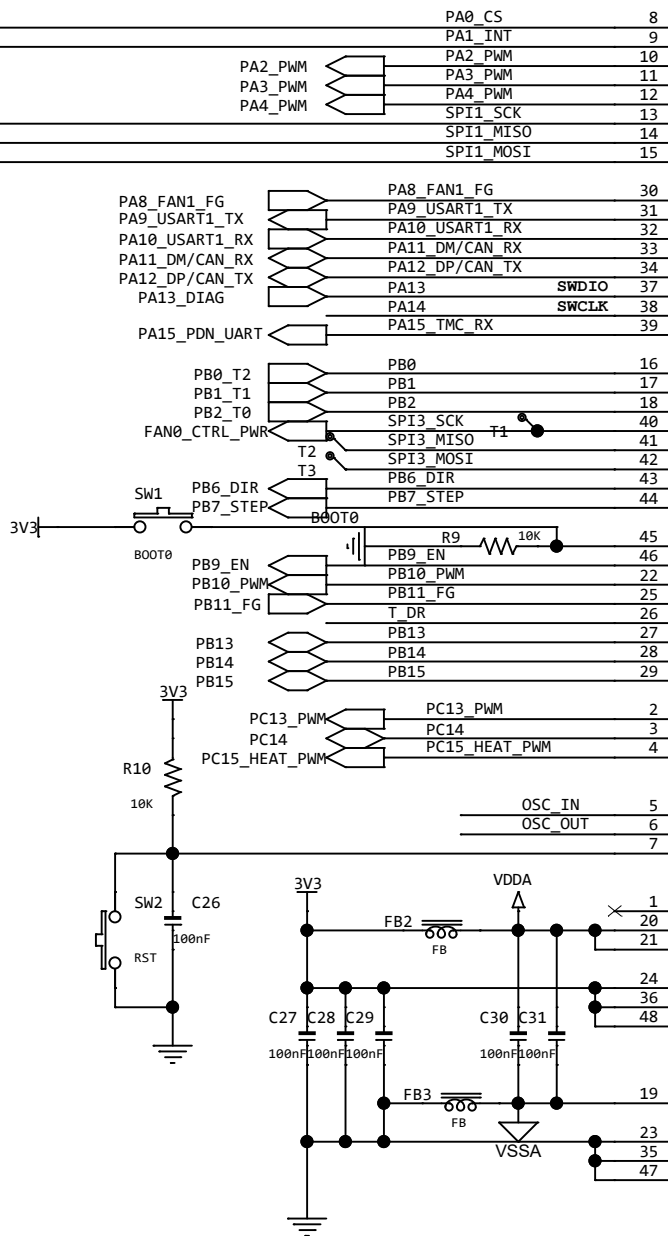
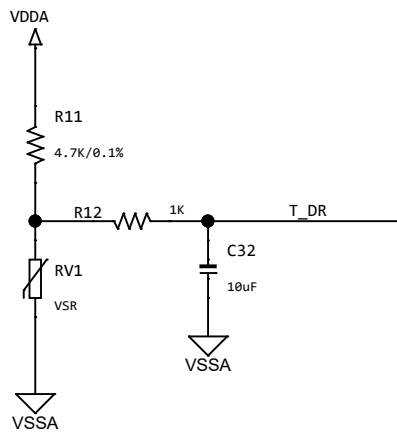
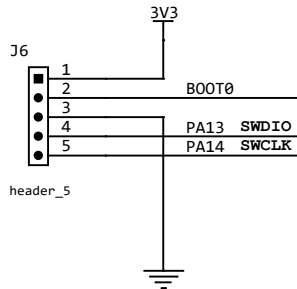
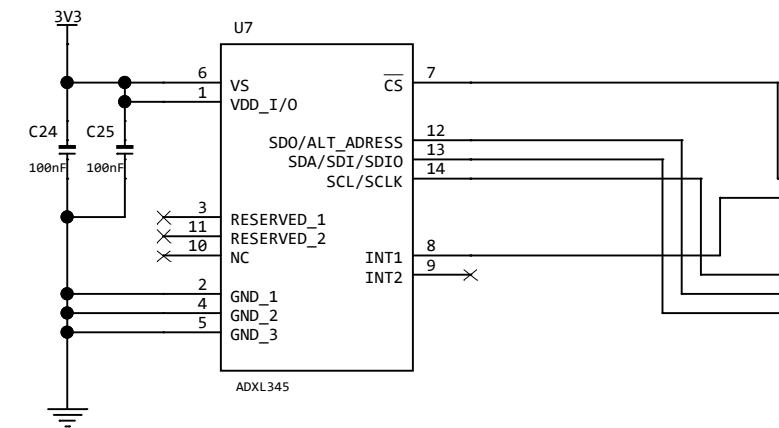


D

C

B

A



U8	PA0/TIM2_CH1/USART2_CTS/COMP1_OUT/TIM8_BKIN/TIM8_ETR/TIM2_ETR/EVENTOUT/ADC12_IN1/COMP1_INM/COMP3_INP/RTC_TAMP2/WKUP1/[TT_a]
	PA1/RTC_REFIN/TIM2_CH2/USART2_RTS_DE/TIM15_CH1N/EVENTOUT/ADC12_IN2/COMP1_INP/OPAMP1_VINP/OPAMP3_VINP/[TT_a]
	PA2/TIM2_CH3/USART2_TX/COMP2_OUT/TIM15_CH1/LPUART1_TX/UCPD1_FRSTX/EVENTOUT/ADC1_IN3/COMP2_INM/OPAMP1_VOUT/WKUP4/LSCO/[TT_a]
	PA3/TIM2_CH4/SAI1_CK1/USART2_RX/TIM15_CH2/LPUART1_RX/SAI1_MCLK_A/EVENTOUT/ADC1_IN4/COMP2_INP/OPAMP1_VINM/OPAMP1_VINP/[TT_a]
	PA4/TIM3_CH2/SPI1_NSS/SPI3_NSS/I2S3_WS/USART2_CK/SAI1_FS_B/EVENTOUT/ADC2_IN17/DAC1_OUT1/COMP1_INM/[TT_a]
	PA5/TIM2_CH1/TIM2_ETR/SPI1_SCK/UCPD1_FRSTX/EVENTOUT/ADC2_IN13/DAC1_OUT2/COMP2_INM/OPAMP2_VINM/[TT_a]
	PA6/TIM16_CH1/TIM3_CH1/TIM8_BKIN/SPI1_MISO/TIM1_BKIN/COMP1_OUT/LPUART1_CTS/EVENTOUT/ADC2_IN3/OPAMP2_VOUT/[TT_a]
	PA7/TIM17_CH1/TIM3_CH2/TIM8_CH1N/SPI1_MOSI/TIM1_CH1N/COMP2_OUT/UCPD1_FRSTX/EVENTOUT/ADC2_IN4/COMP2_INP/OPAMP1_VINP/OPAMP2_VINP/[TT_a]
	PA8/MCO/I2C3_SCL/I2C2_SDA/I2S2_MCK/TIM1_CH1/USART1_CK/TIM4_ETR/SAI1_CK2/SAI1_SCK_A/EVENTOUT/[FT_f]
	PA9/I2C3_SMBA/I2C2_SCL/I2S3_MCK/TIM1_CH2/USART1_TX/TIM15_BKIN/TIM2_CH3/SAI1_FS_A/EVENTOUT/UCPD1_DBCC1/[FT_fd]
	PA10/TIM17_BKIN/USB_CR5_SYNC/I2C2_SMBA/SPI2_MISO/TIM1_CH3/USART1_RX/TIM2_CH4/TIM8_BKIN/SAI1_D1/SAI1_SD_A/EVENTOUT/UCPD1_DBCC2/[FT_da]
	PA11/SPI2_MOSI/I2S2_SD/TIM1_CH1N/USART1_CTS/COMP1_OUT/FDCAN1_RX/TIM4_CH1/TIM1_CH4/TIM1_BKIN2/EVENTOUT/USB_DM/[FT_u]
	PA12/TIM16_CH1/I2SCKIN/TIM1_CH2N/USART1_RTS_DE/COMP2_OUT/FDCAN1_TX/TIM4_CH2/TIM1_ETR/EVENTOUT/USB_DP/[FT_u]
	PA13/SWDIO-JTMS/TIM16_CH1N/I2C1_SCL/IR_OUT/USART3_CTS/TIM4_CH3/SAI1_SD_B/EVENTOUT/[FT_f]
	PA14/SWCLK-JTCK/LPTIM1_OUT/I2C1_SDA/TIM8_CH2/TIM1_BKIN/USART2_TX/SAI1_FS_B/EVENTOUT/[FT_f]
	PA15/JTDI/TIM2_CH1/TIM8_CH1/I2C1_SCL/SPI1_NSS/SPI3_NSS/I2S3_WS/USART2_RX/UART4_RTS_DE/TIM1_BKIN/TIM2_ETR/EVENTOUT/[FT_f]
	PB0/TIM3_CH3/TIM8_CH2N/TIM1_CH2N/UCPD1_FRSTX/EVENTOUT/ADC1_IN15/COMP4_INP/OPAMP2_VINP/OPAMP3_VINP/[TT_a]
	PB1/TIM3_CH4/TIM8_CH3N/TIM1_CH3N/COMP4_OUT/LPUART1_RTS_DE/EVENTOUT/ADC1_IN12/COMP1_INP/OPAMP3_VOUT/[TT_a]
	PB2/RTC_OUT2/LPTIM1_OUT/I2C3_SMBA/EVENTOUT/ADC2_IN12/COMP4_INM/OPAMP3_VINM/[TT_a]
	PB3/JTDO-TRACESW/TIM2_CH2/TIM4_ETR/USB_CR5_SYNC/TIM8_CH1N/SPI1_SCK/SPI3_SCK/I2S3_CK/USART2_TX/TIM3_ETR/SAI1_SCK_B/EVENTOUT/[FT]
	PB4/JTRST/TIM16_CH1/TIM3_CH1/TIM8_CH2N/SPI1_MISO/SPI3_MISO/USART2_RX/TIM17_BKIN/SAI1_MCLK_B/EVENTOUT/UCPD1_CC2/[FT_c]
	PB5/TIM16_BKIN/TIM3_CH2/TIM8_CH3N/I2C1_SMBA/SPI1_MOSI/SPI3_MOSI/I2S3_SD/USART2_CK/I2C3_SDA/TIM17_CH1/LPTIM1_IN1/SAI1_SD_B/EVENTOUT/[FT_f]
	PB6/TIM16_CH1N/TIM4_CH1/TIM8_CH1/TIM8_ETR/USART1_TX/COMP4_OUT/TIM8_BKIN2/LPTIM1_ETR/SAI1_FS_B/EVENTOUT/UCPD1_CC1/[FT_c]
	PB7/TIM17_CH1N/TIM4_CH2/I2C1_SDA/TIM8_BKIN/USART1_RX/COMP3_OUT/TIM3_CH4/LPTIM1_IN2/UART4_CTS/EVENTOUT/[FT_f]
	PB8/BOOT0/TIM16_CH1/TIM4_CH3/SAI1_CK1/I2C1_SCL/USART3_RX/COMP1_OUT/FDCAN1_RX/TIM8_CH2/TIM1_BKIN/SAI1_MCLK_A/EVENTOUT/[FT_f]
	PB9/TIM17_CH1/TIM4_CH4/SAI1_D2/I2C1_SDA/IR_OUT/USART3_TX/COMP2_OUT/FDCAN1_TX/TIM8_CH3/TIM1_CH3N/SAI1_FS_A/EVENTOUT/[FT_f]
	PB10/TIM2_CH3/USART3_TX/LPUART1_RX/TIM1_BKIN/SAI1_SCK_A/EVENTOUT/OPAMP3_VINM/[TT_a]
	PB11/TIM2_CH4/USART3_RX/LPUART1_TX/EVENTOUT/ADC12_IN14/[FT_a]
	PB12/I2C2_SMBA/SPI2_NSS/I2S2_WS/TIM1_BKIN/USART3_CK/LPUART1_RTS_DE/EVENTOUT/ADC1_IN11/[FT_a]
	PB13/I2C2_SCK/I2S2_CK/TIM1_CH1N/USART3_CTS/LPUART1_CTS/EVENTOUT/OPAMP3_VINP/[TT_a]
	PB14/TIM15_CH1/SPI2_MISO/TIM1_CH2N/USART3_RTS_DE/COMP4_OUT/EVENTOUT/ADC1_IN5/OPAMP2_VINP/[TT_a]
	PB15/RTC_REFIN/TIM15_CH2/TIM15_CH1N/COMP3_OUT/TIM1_CH3N/SPI2_MOSI/I2S2_SD/EVENTOUT/ADC2_IN15/[FT_a]
	PC13/TIM1_BKIN/TIM1_CH1N/TIM8_CH4N/EVENTOUT/WKUP2/RTC_TAM1/RTC_TS/RTC_OUT1/[FT]
	PC14/OSC32_IN/EVENTOUT/[FT]
	PC15/OSC32_OUT/EVENTOUT/[FT]
	PF0/OSC_IN/I2C2_SDA/SPI2_NSS/I2S2_WS/TIM1_CH3N/EVENTOUT/ADC1_IN10/OSC_IN/[FT_fa]
	PF1/OSC_OUT/SPI2_SCK/I2S2_CK/EVENTOUT/ADC2_IN10/COMP3_INM/OSC_OUT/[FT_fa]
	PG10/NRST/MCO/EVENTOUT
	VBAT
	VREF+
	VDDA
	VDD_1
	VDD_2
	VDD_3
	VSSA
	VSS_1
	VSS_2
	VSS_3

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