

CATALYST

BASED ON ARM A55 & M4
Design For
VORON V0
Version 2.0

FYSETC.COM
By Eli

RGB
NeoPixel / DotStar

CAN
A

J25
J26
J27

J34
J35
J36

J16
J17
J18

J19
J20
J21

J22
J23
J24

J25
J26
J27

J28
J29
J30

A

B

B

P3

Warning:
Please confirm
the CM68
direction

CM68 DEBUG

Z

+ FAN3 + FAN2 + FAN1
24 12 5 24 12 5 24 12 5
JP1
JP6
JP2

FAN1
FAN2
FAN0

12V

- IN + + BED -

POWER
RESET
MASKROM
RECOVERY

24V
BED



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68_CANL

68_CANH

CS/PDN : PC14

DIR : PC13

STEP : PC15

I2C

SPI

UART

BOOT0

CS/PDN : PB5

5V

RGB

DIAG: PB3

Neopixel / DotStar

EN : PB1

STEP : PB6

DIR : PB4

EN : PD2

STEP : PC12

DIR : PC10

EN : PA10

DIAG: PA9

FAN_PWR

24V

EMOT : A1

EMOT : A2

EMOT : B1

EMOT : B2

CS : PA4

SCK : PA5

FAN0 : PA13

FAN1 : PA14

MISO : PA6

MOSI : PA7

I/O_0 : PA0

T0 : PC0

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

CS/PDN : PC11

GND

5V

PC3

GND

PA3

GND

24V

5V

24V

3V3

3V3

3V3

3V3

3V3

3V3

3V3

3V3

3V3

3V3

3V3

PC3

GND

PA3

PA2

PA1

PA0

GND

GND

GND

GND

GND

GND

GND

GND

GND

GND

PROBE I/O_3

I/O_2

I/O_1

I/O_0

68_RX2

68_TX2

GND

PB1

PB0

GND

GND

T0

T2

T1

CM68 CS/PDN : PC4

DIR : PC5

STEP : PB2

EN : PB10

24V

BED

POWER

RESET

MASKROM

RECOVERY

Warning:
Please confirm
the CM68
direction

24	12	5	FAN1	+
24	12	5	FAN2	+
24	12	5	FAN3	+
24	12	5	FAN4	+

FAN2: PA8
24V

GND

24V

24V

BED : PC8

—POWER IN—

+ BED —