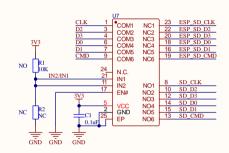


SD_D3 SD_CMD	SD 1 2 3 4 5	
3V3 SD CLK SD D0 SD D1	4 5 6 7 8	
SD_D2 GPIO.32/ADC1_4 GPIO.26/12S_WS IO2 GPIO.00 FN	9 - 10 - 11 - 12 - 13	
EN GPIO.03/RX0 GPIO.01/TX0 SCL/GPIO.22/V_SPI_WP/RTS0 SDA/GPIO.21/V_SPI_HD MISO/GPIO.19/V_SPI_QCTS0	- 14 - 15 - 16 - 17 - 18	
wiiso/drio.19/v_sri_QC130	Header 1	9

С

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GPIO.15/ADC2_3/HSPI_CS0	ESP_SD_CMD
GPIO.14/ADC2_6/HSPI_CLK	ESP_SD_CLK
CS/GPIO.02/ADC2_2/HSPI_WP	ESP_SD_D0
GPIO.04/ADC2_0/HSPI_HD	ESP_SD_D1
GPIO.12/ADC2_5/HSPI_Q	ESP_SD_D2
GPIO.13/ADC2_4/HSPI_ID	ESP_SD_D3
GPIO.26/I2S_WS	IN2/IN1



PI信号线			端口	
MISO			102	
MOSI			1015	
CLK			1014	
CS			IO13	
		Slot1	Slot2	
Pin Name	Corresponding pins in SPI mode	GPIO Num	ber	
CLK	SCLK	6	14	
CMD	MOSI	11	15	
DAT0	MISO	7	2	
DAT1	Interrupt	8	4	
DAT2	N.C. (pullup)	9	12	
DAT3	#CS	10	13	

1-bit SD mon	le Connec	+ CIK	CMD	DATO	DAT1	nins and	the ground	

- · 4-bit SD mode: Connect all pins and the ground.
- · SPI mode: Connect SCLK, MOSI, MISO, Interrupt, #CS pins and the ground.

Signal	Slot 0	Slot 1			
CMD	GPIO11	GPIO15			
CLK	GPIO6	GPIO14			
00	GPIO7	GPIO2			
01	GPIO8	GPIO4			
02	GPIO9	GPIO12			
03	GPIO10	GPIO13			
04	GPIO16				
05	GPIO17				
06	GPIO5				
07	GPIO18				
CD	any input via GPIO matrix				
WP	any input via GPIO matrix				