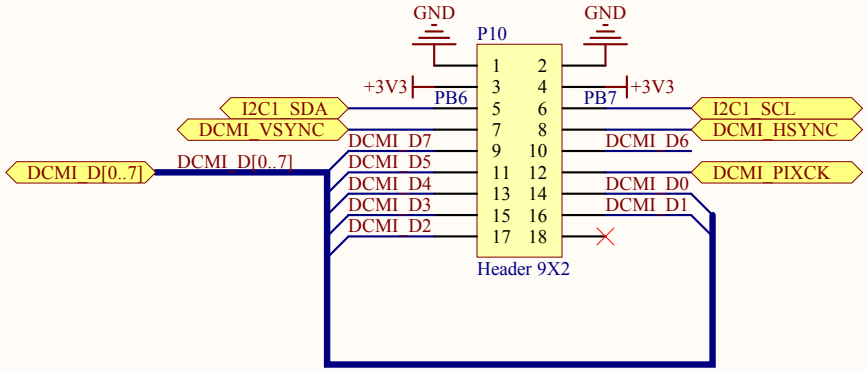

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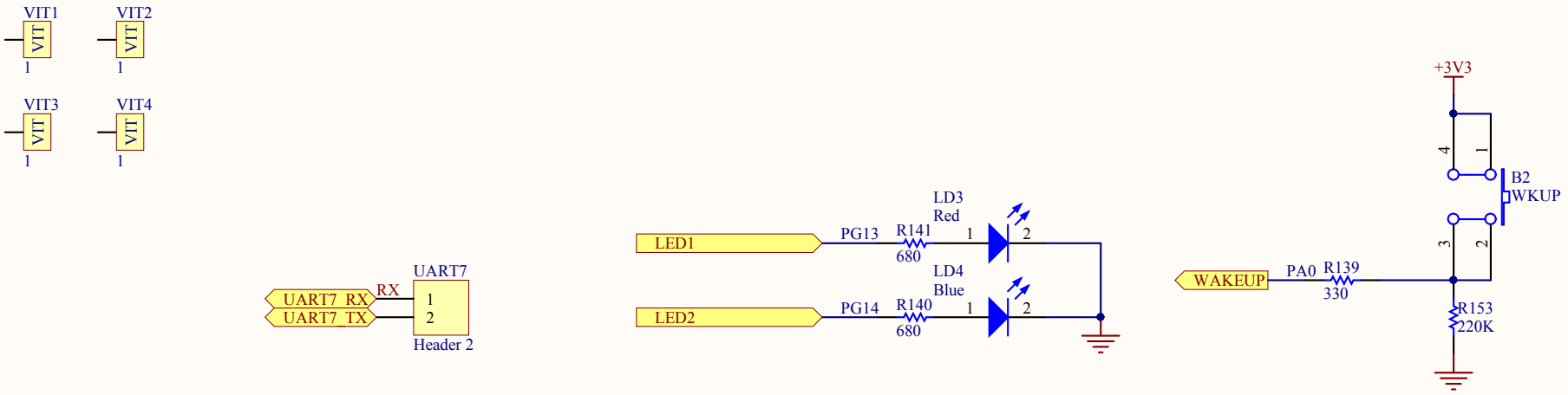
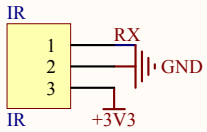
Altium





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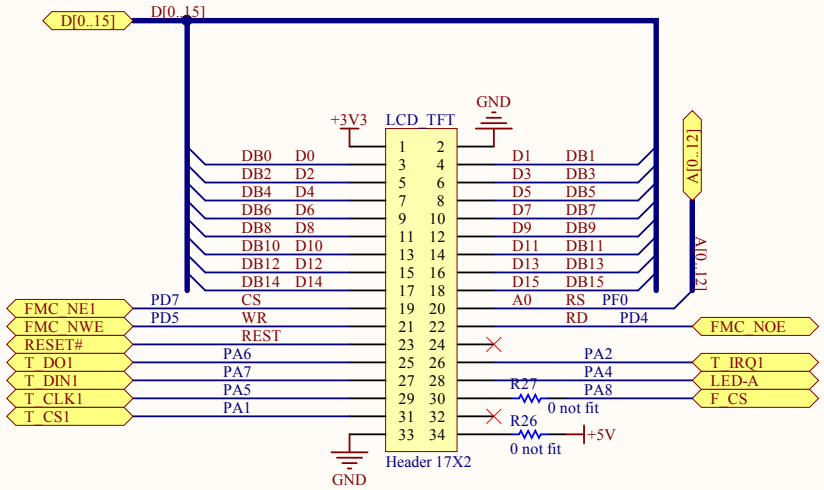

STMicroelectronics

Title: LCD

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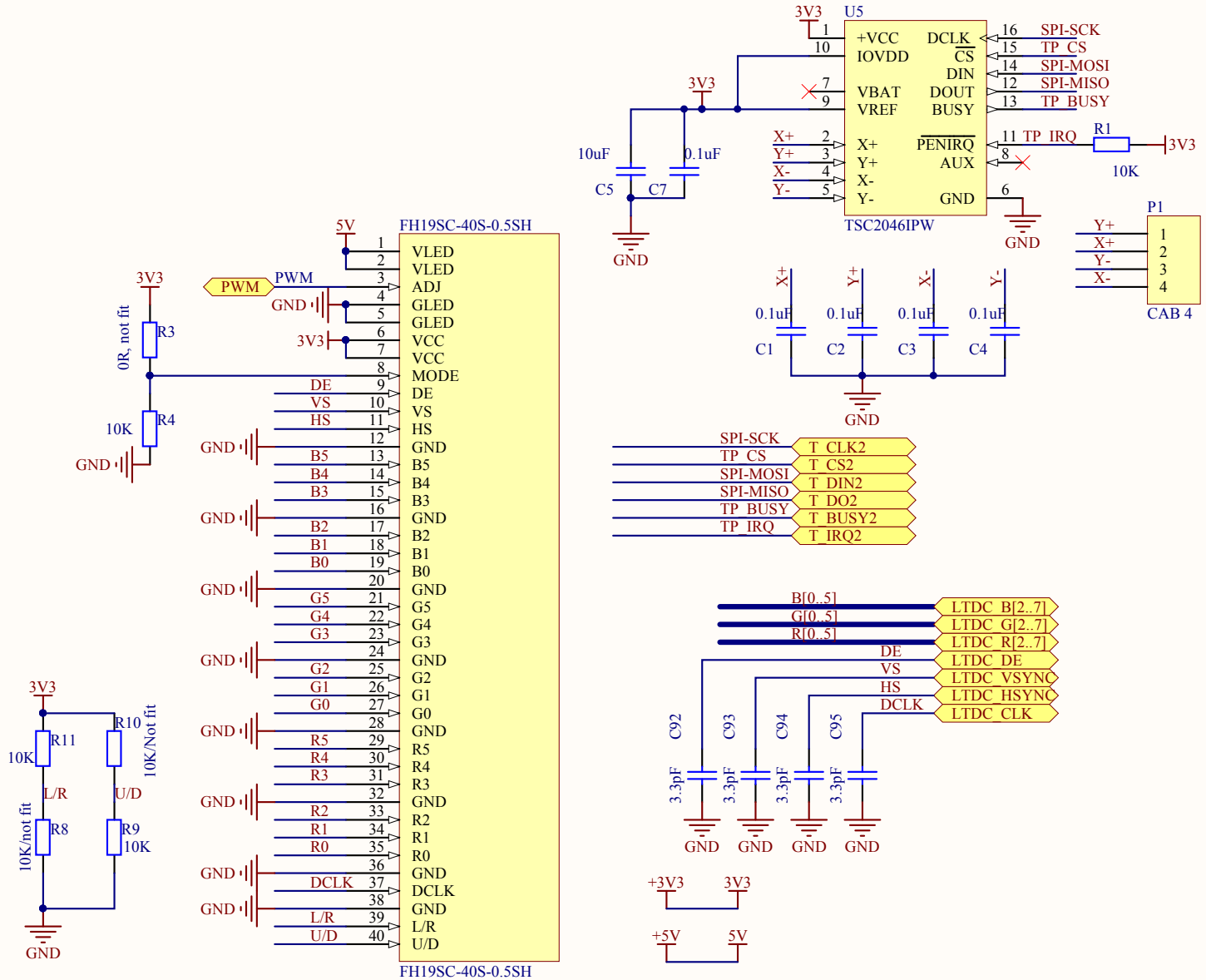

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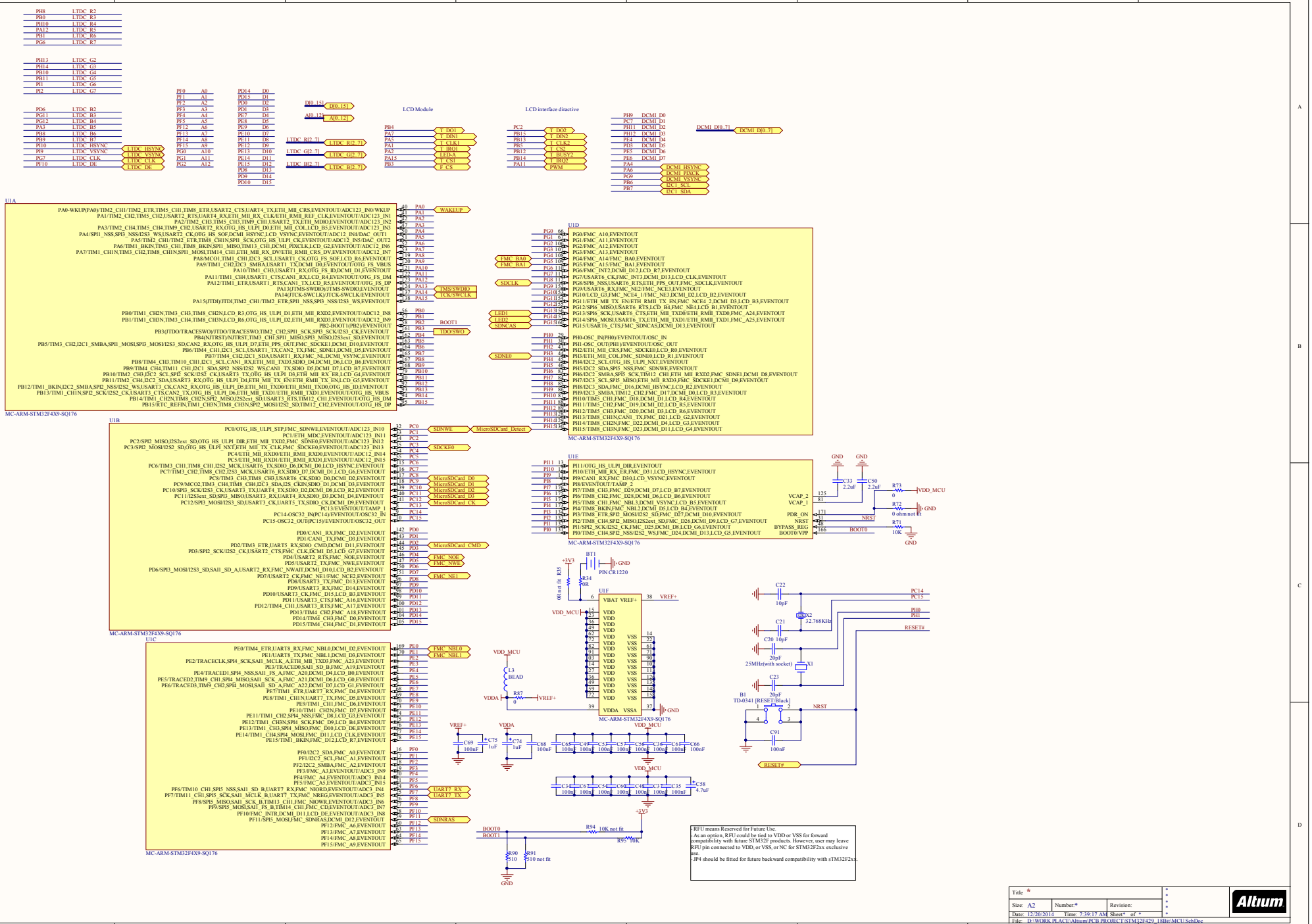
LTDC_R2
LTDC_R3
LTDC_R4
LTDC_R5
LTDC_R6
LTDC_R7
LTDC_G2
LTDC_G3
LTDC_G4
LTDC_G5
LTDC_G6
LTDC_G7
LTDC_B2
LTDC_B3
LTDC_B4
LTDC_B5
LTDC_B6
LTDC_B7
LTDC_HSYNC
LTDC_VSYNC
LTDC_CLK
LTDC_DE
D[0..15]
A[0..12]
LTDC_R[2..7]
LTDC_G[2..7]
LTDC_B[2..7]
LCD Module
T_DO1
T_DIN1
T_CLK1
T_IRQ1
LED-A
T_CS1
F_CS
LCD interface diractive
T_DO2
T_DIN2
T_CLK2
T_CS2
T_BUSY2
T_IRQ2
PWM
DCMI_D[0..7]
DCMI_HSYNC
DCMI_PIXCK
DCMI_VSYNC
I2C1_SCL
I2C1_SDA
U1A
MC-ARM-STM32F4X9-SQI76
WAKEUP
TMS/SWDIO
TCK/SWCLK
BOOT1
TDO/SWO
LED1
LED2
SDNCAS
SDCLK
FMC_BA0
FMC_BA1
SDNE0
U1D
U1B
SDNWE
MicroSDCard_Detect
SDCKE0
MicroSDCard_D0
MicroSDCard_D1
MicroSDCard_D2
MicroSDCard_D3
MicroSDCard_CK
MicroSDCard_CMD
FMC_NOE
FMC_NWE
FMC_NE1
U1E
U1C
FMC_NBL0
FMC_NBL1
UART7_RX
UART7_TX
SDNRAS
U1F
BT1
PIN CR1220
R34
0R
0R not fit
R35
L3
BEAD
R87
0
VDD_MCU
VDDA
VREF+
VBAT
VDD
VSS
VDDA_VSSA
C69
100nF
C75
1uF
C74
1uF
C68
100nF
C65
100nF
C49
100nF
C55
100nF
C56
100nF
C36
100nF
C6
100nF
C66
100nF
C34
100nF
C67
100nF
C54
100nF
C66
100nF
C48
100nF
C33
100nF
C35
100nF
C58
4.7uF
+3V3
R94
10K not fit
R95
10K
BOOT0
BOOT1
R90
S10
R91
S10 not fit
C22
10pF
C21
C20
10pF
20pF
25MHz(with socket)
X1
C23
20pF
C91
100nF
RESET#
PC14
PC15
PH0
PH1
B1
TD-0341 [RESET]Black]
NRST
C33
2.2uF
C50
2.2uF
R73
0
R72
0 ohm not fit
R71
10K
VDD_MCU
GND
VCAP_2
VCAP_1
PDR_ON
NRST
BYPASS_REG
BOOT0/VPP
BOOT0
RFU means Reserved for Future Use.
- As an option, RFU could be tied to VDD or VSS for forward compatibility with future STM32F products. However, user may leave RFU pin connected to VDD, or VSS, or NC for STM32F2xx exclusive use.
- JP4 should be fitted for future backward compatibility with sTM32F2xx.
Title *
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Date: 12/20/2014
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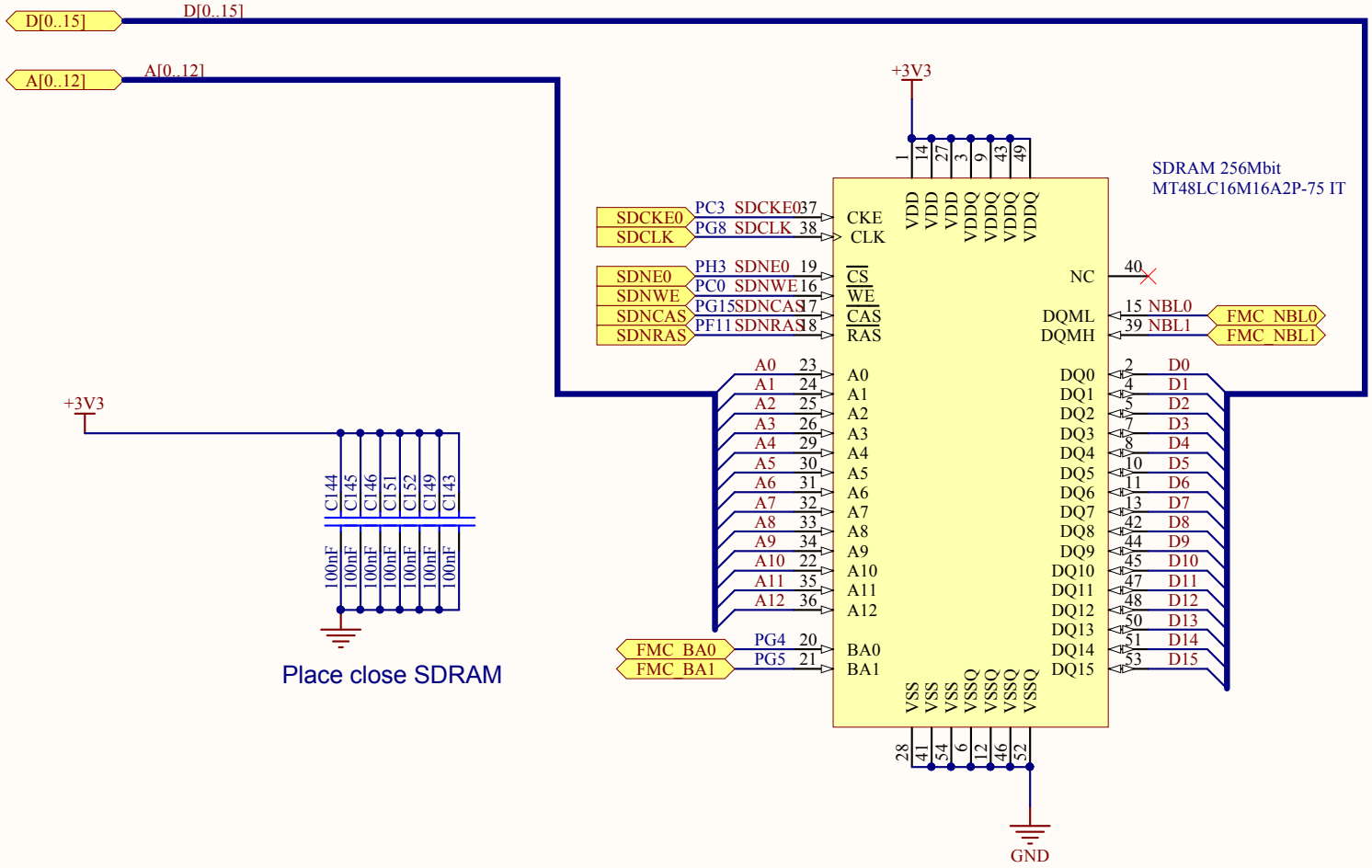

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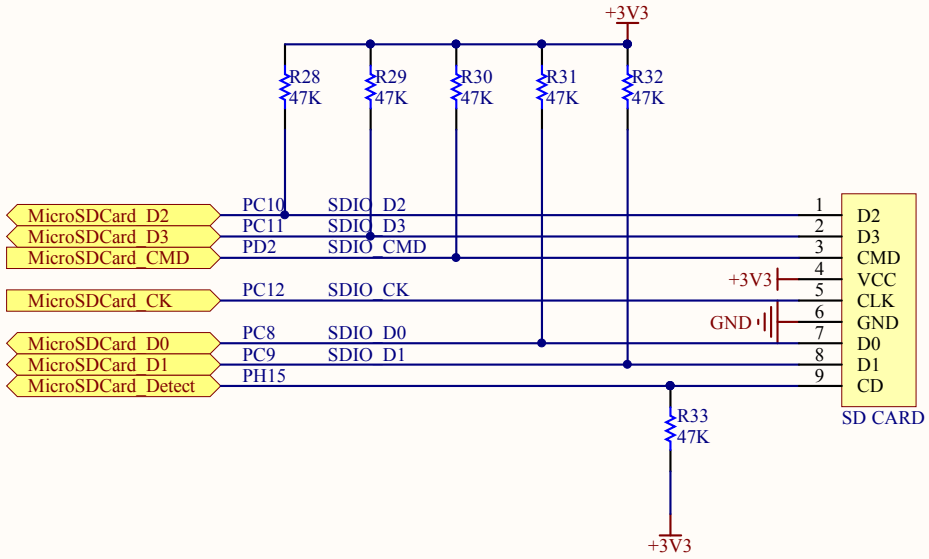

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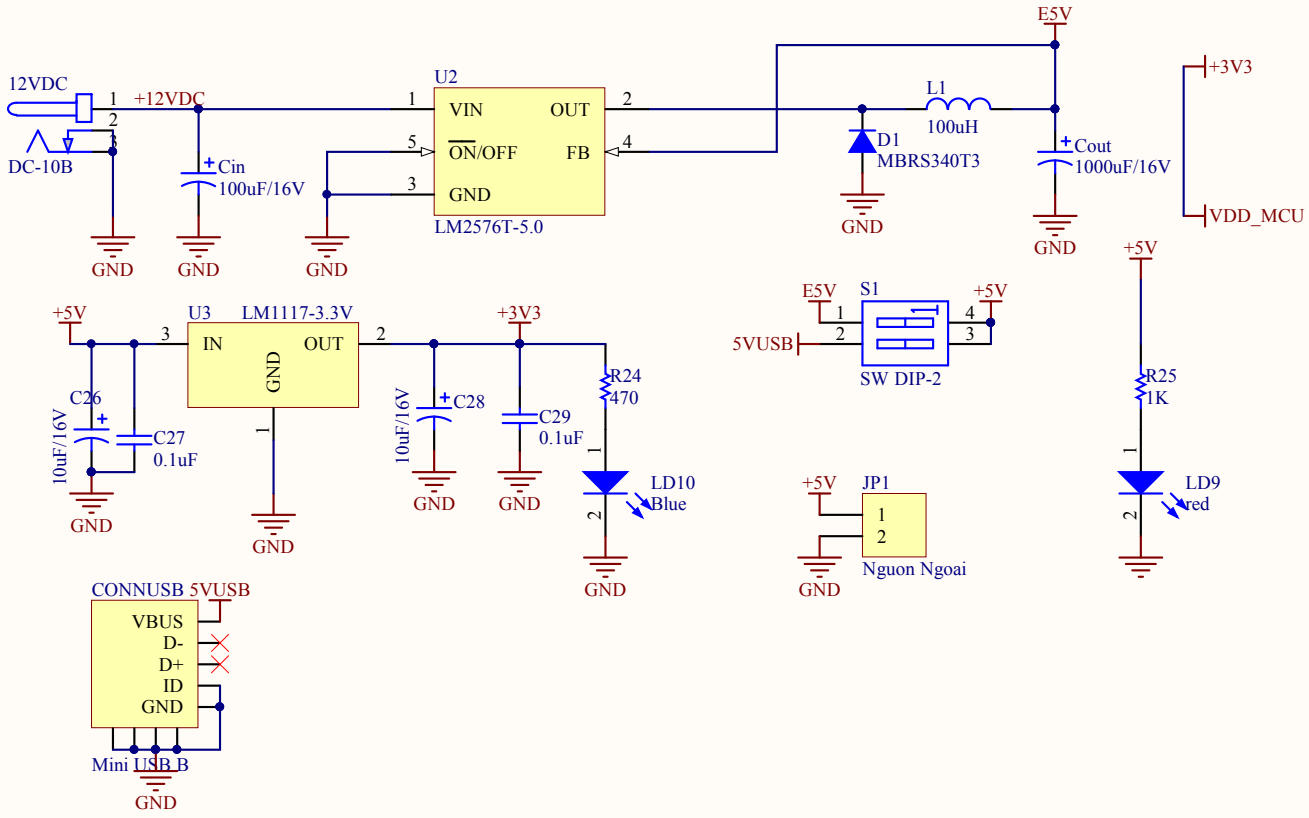

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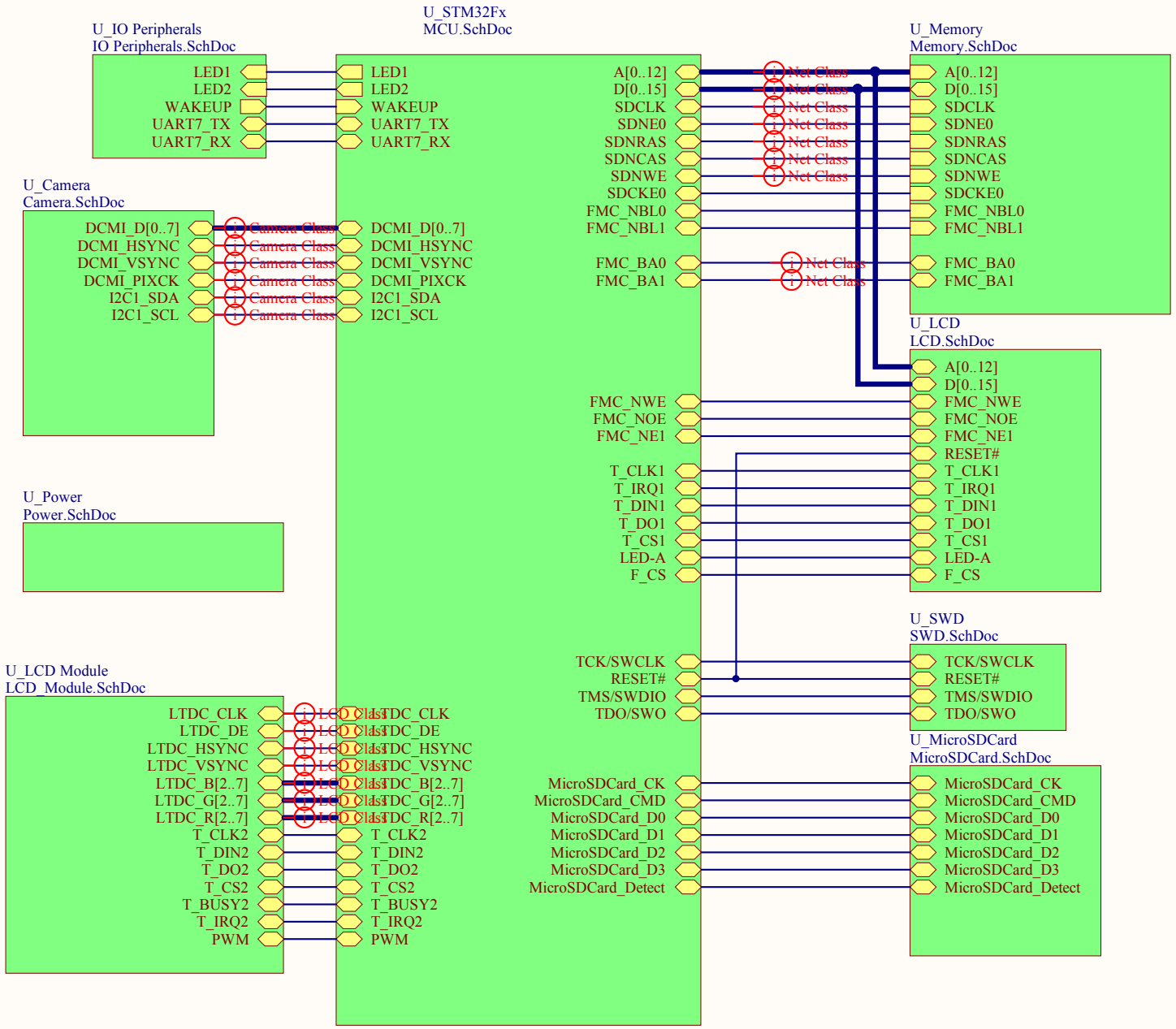

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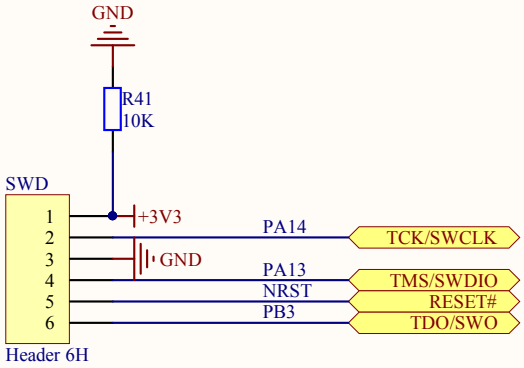
Altium



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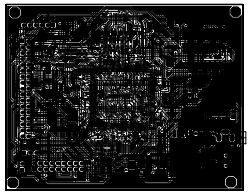

Board S