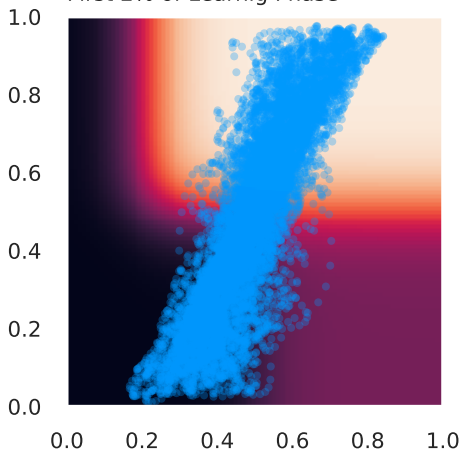
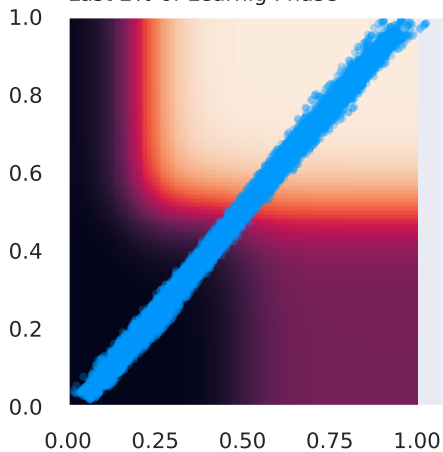


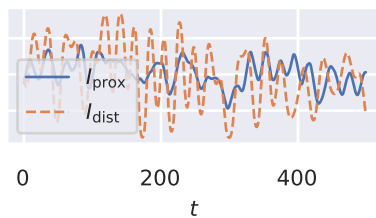
First 2% of Learning Phase



Last 2% of Learning Phase



Input Currents



Input Currents

