

Ideas for Low Celluclast Performance

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Basic Architecture

- Accesses different memory than CPU
- Starting thousands of threads at low costs

Memory Management

```
1  typedef int dtype;
2  class Storage {
3      public:
4          explicit Storage(const std::vector<int>&);
5
6      private:
7          std::vector<int> __data;
8          dtype* __cpu_pointer;
9          dtype* __gpu_pointer;
10         void initialize_gpu_memory();
11     };
```

- Memory pool, takes ownership
- Initializes the gpu memory as copy
- two different pointer, to cpu/gpu locations

```
1  typedef int dtype;
2  class Storage {
3      public:
4          explicit Storage(const std::vector<int>&);
5          const dtype* cpu_pointer_const();
6          const dtype* gpu_pointer_const();
7          dtype* cpu_pointer();
8          dtype* gpu_pointer();
9
10     private:
11         std::vector<int> __data;
12         dtype* __cpu_pointer;
13         dtype* __gpu_pointer;
14         void initialize_gpu_memory();
15         std::string recent_head;
16         void sync_to_cpu();
17         void sync_to_gpu();
18     };
```

Merge

Write about CPU merge

How to spwan to many threads?

Paper that does that, show as example, the cutting approach
Naiv approach : 2 Threads Cut both a and b into half,

$$A = 0000B \qquad = 1111C = ????????$$

$$A = \underbrace{00}_{\text{Thread 1}} \mid \underbrace{00}_{\text{Thread 2}} B = \underbrace{11}_{\text{Thread 1}} \mid \underbrace{11}_{\text{Thread 2}} C = \underbrace{????}_{\text{Thread 1}} \mid \underbrace{????}_{\text{Thread 2}}$$