

chip id:0x71

0 100 200 300 400 500

0

200

400

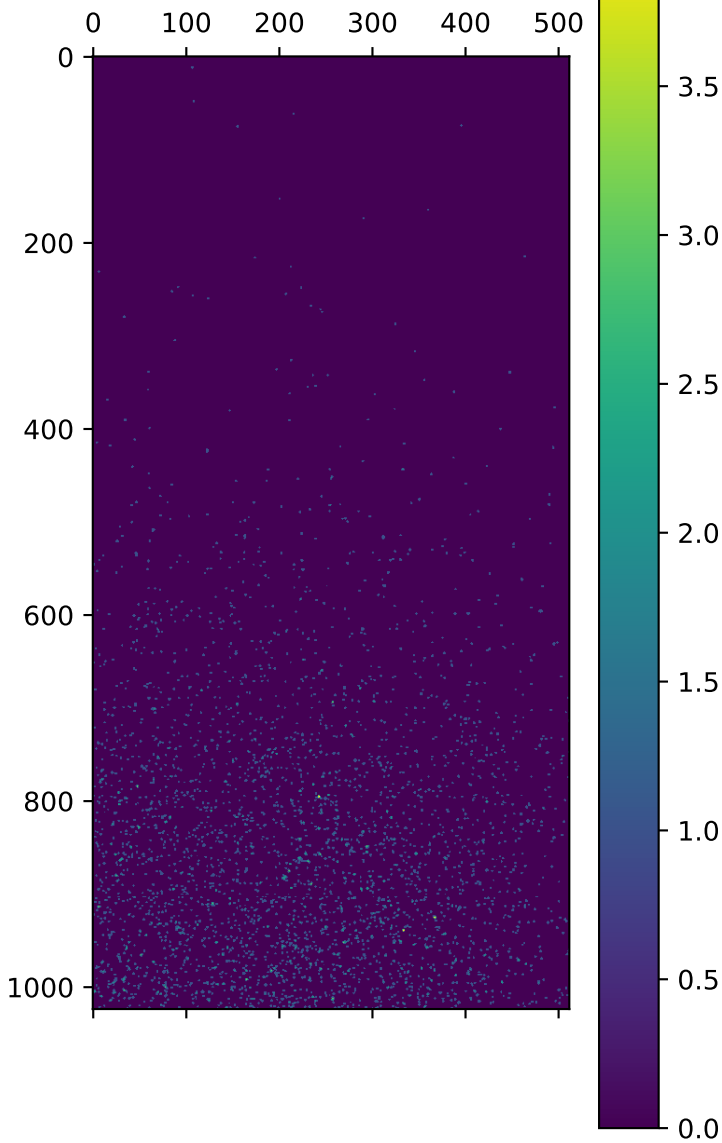
600

800

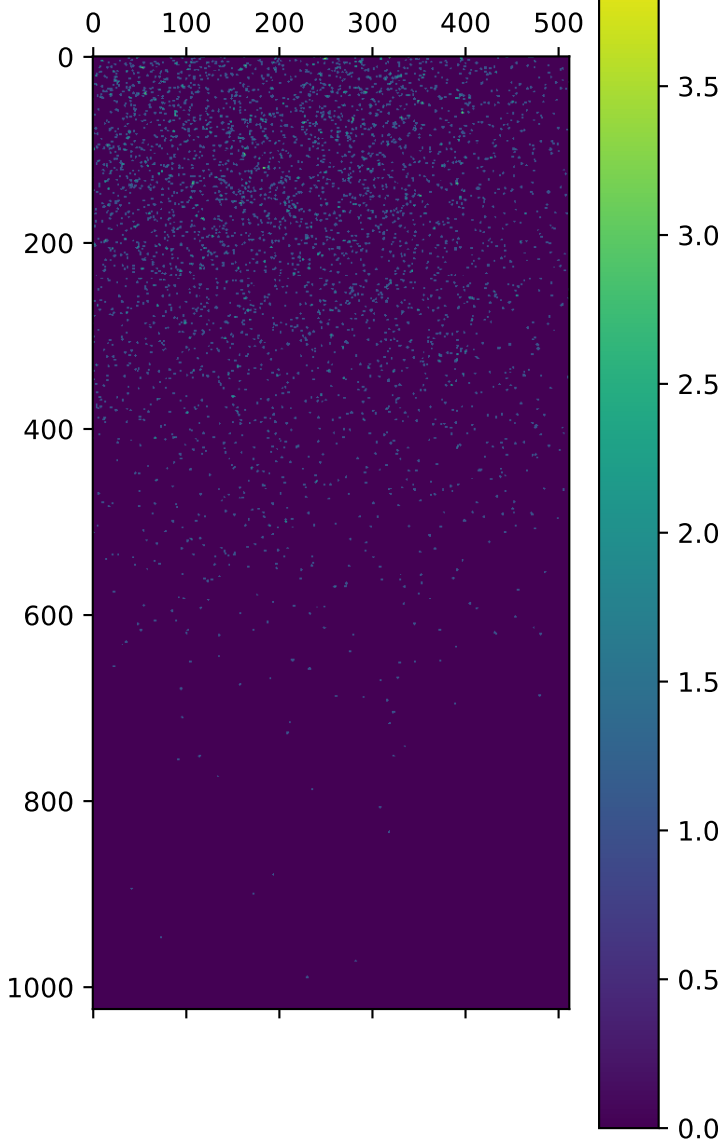
1000



chip id:0x72



chip id:0x73



chip id:0x78

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x79

0 100 200 300 400 500

0

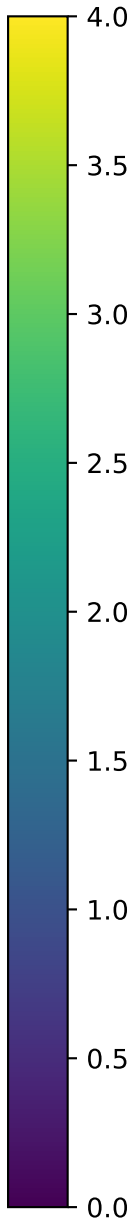
200

400

600

800

1000



chip id:0x7a

0 100 200 300 400 500

0

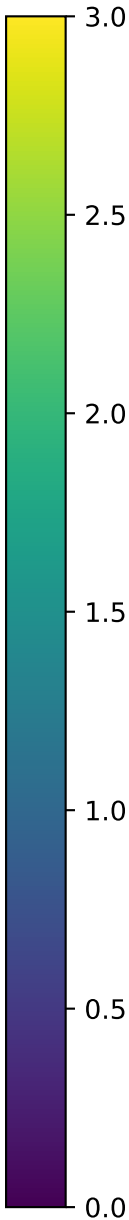
200

400

600

800

1000



chip id:0x7b

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x171

0 100 200 300 400 500

0

200

400

600

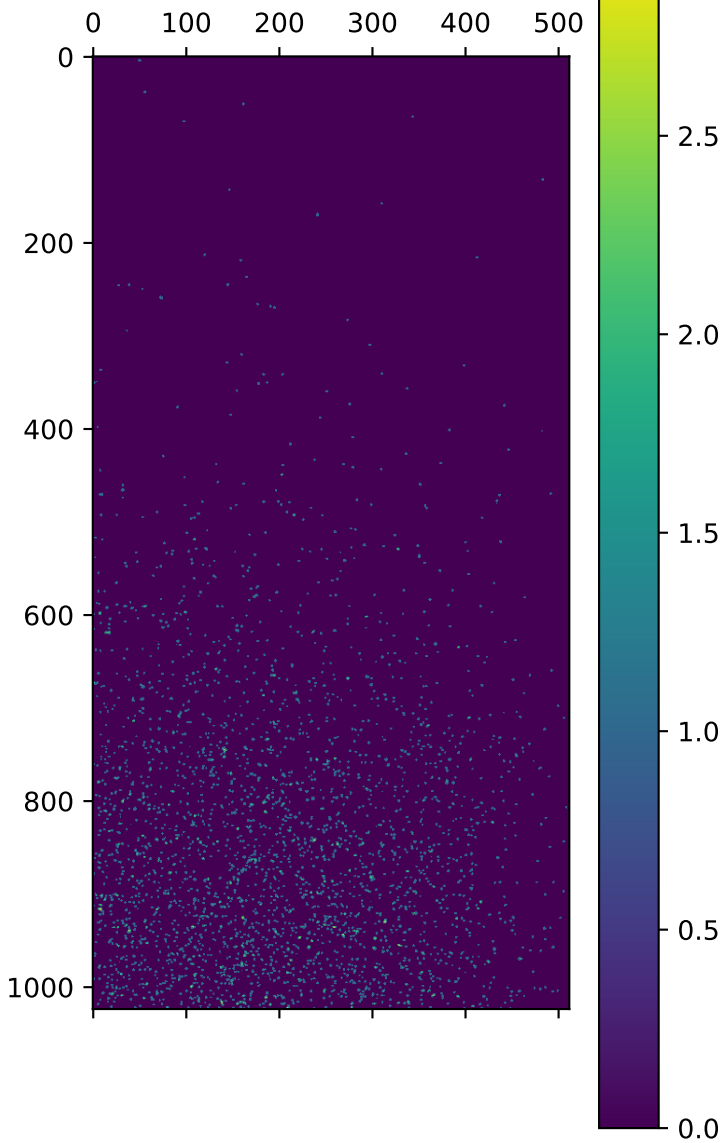
800

1000





chip id:0x172



chip id:0x173

0 100 200 300 400 500

0

200

400

600

800

1000

3.0

2.5

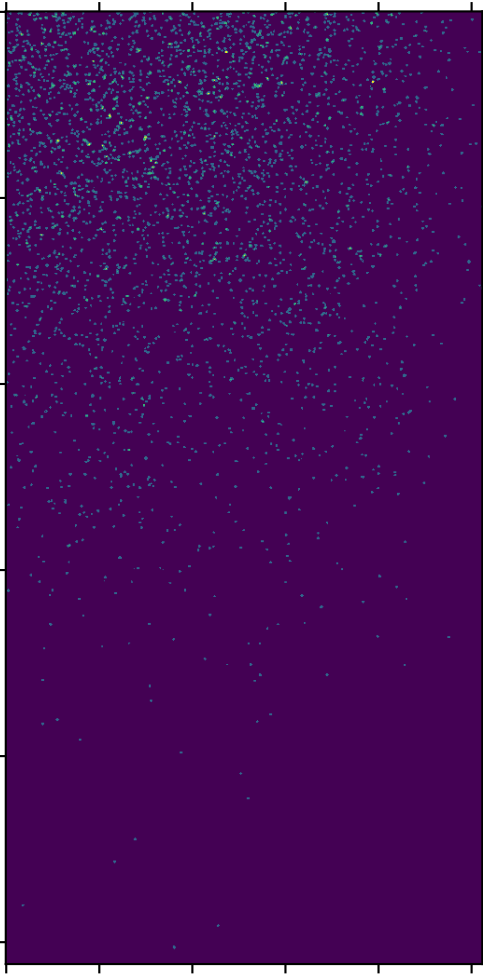
2.0

1.5

1.0

0.5

0.0



chip id:0x178

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x179

0 100 200 300 400 500

0

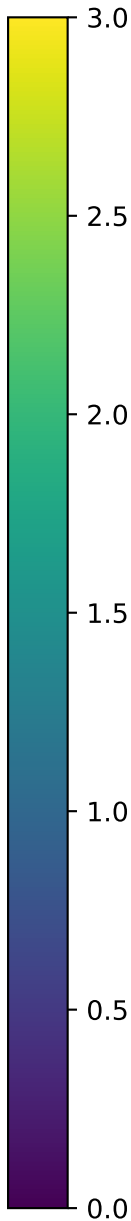
200

400

600

800

1000



chip id:0x17a

0 100 200 300 400 500

0

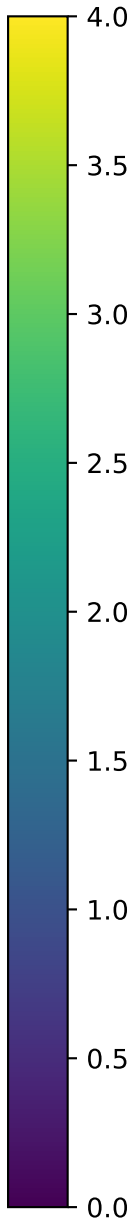
200

400

600

800

1000



chip id:0x17b

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x271

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x272

0 100 200 300 400 500

0

200

400

600

800

1000

3.0

2.5

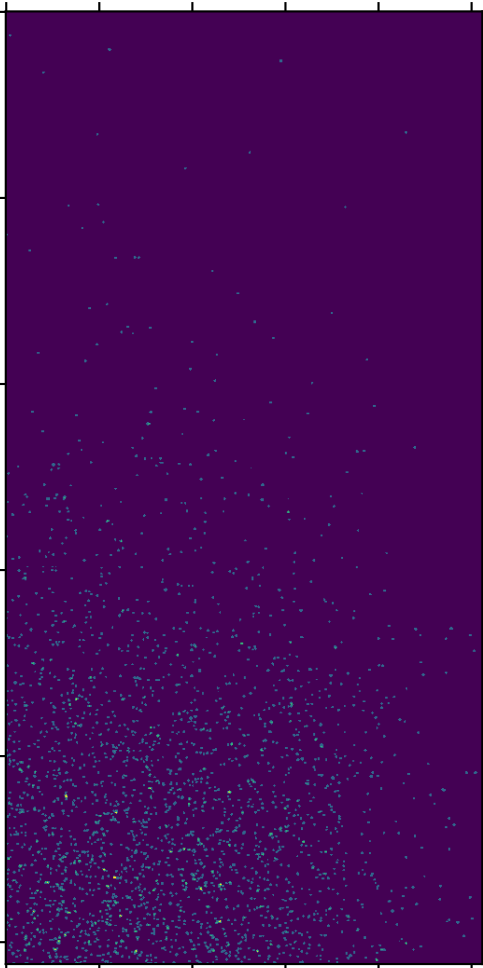
2.0

1.5

1.0

0.5

0.0





chip id:0x273

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x278

0 100 200 300 400 500

0

200

400

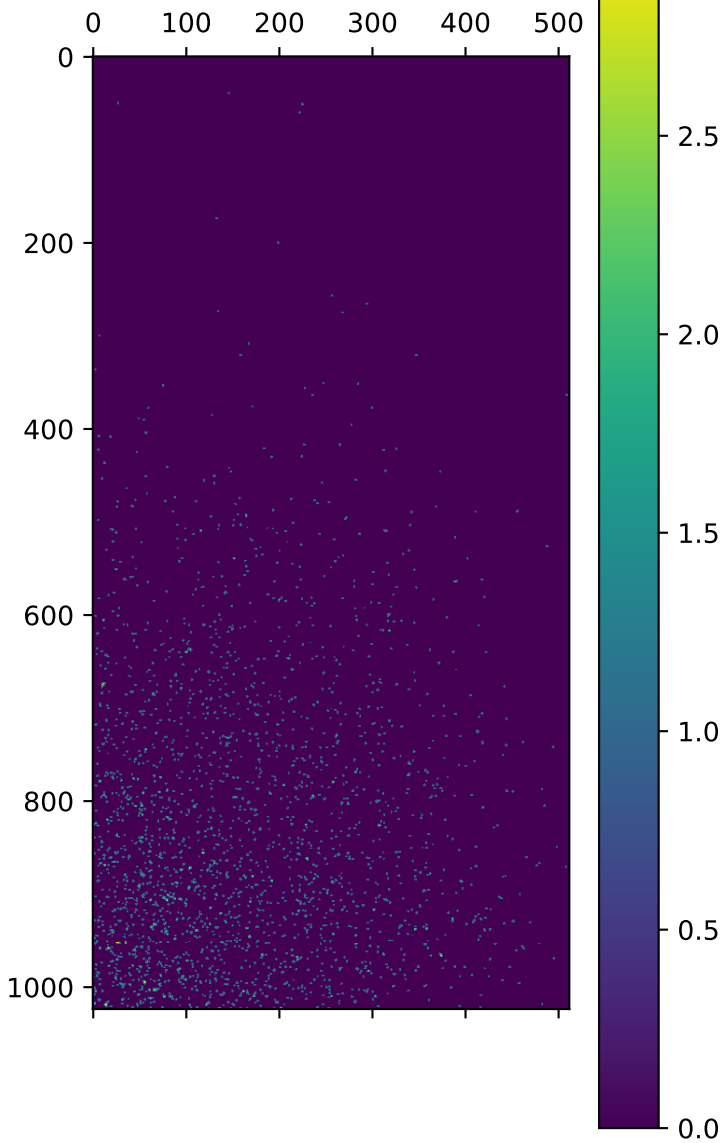
600

800

1000



chip id:0x279



chip id:0x27a

0 100 200 300 400 500

0

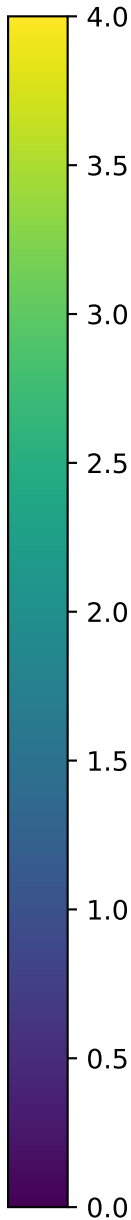
200

400

600

800

1000



chip id:0x27b

0 100 200 300 400 500

0

200

400

600

800

1000

