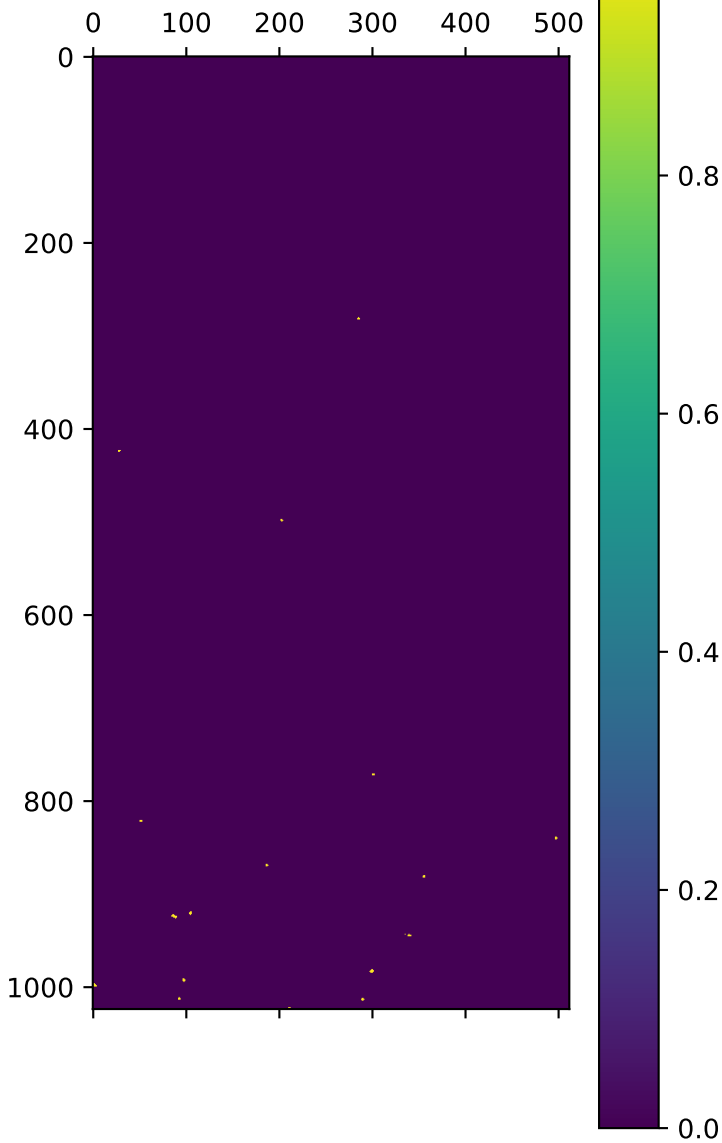
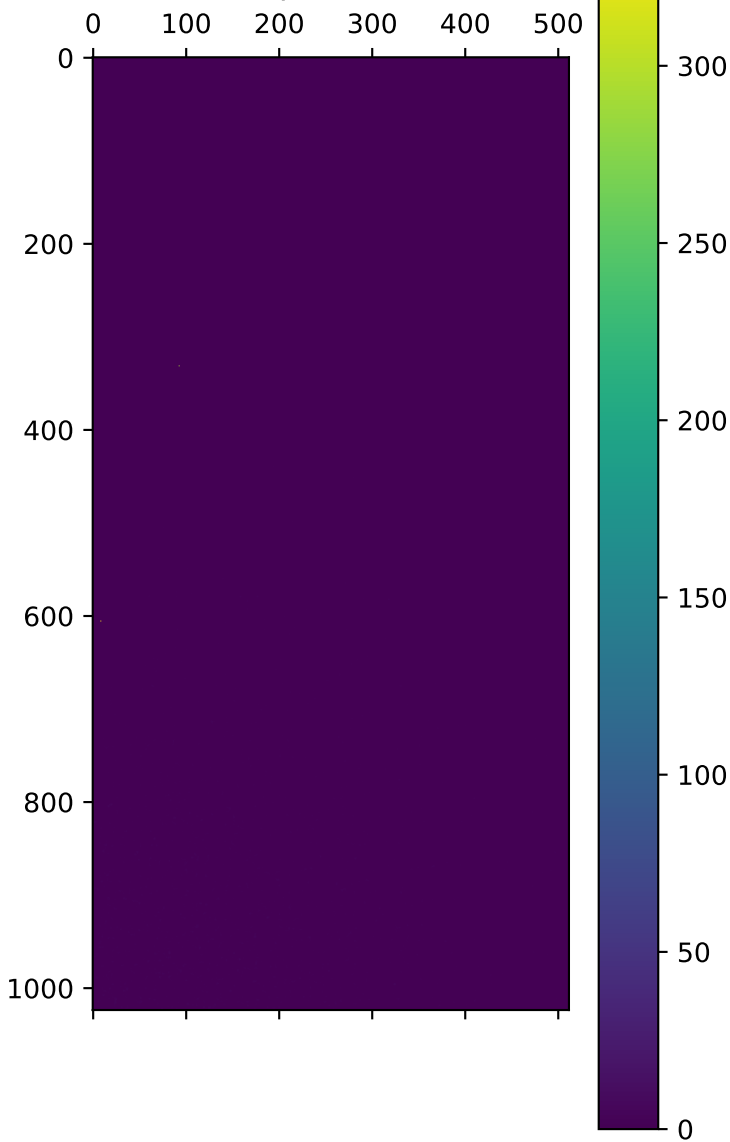


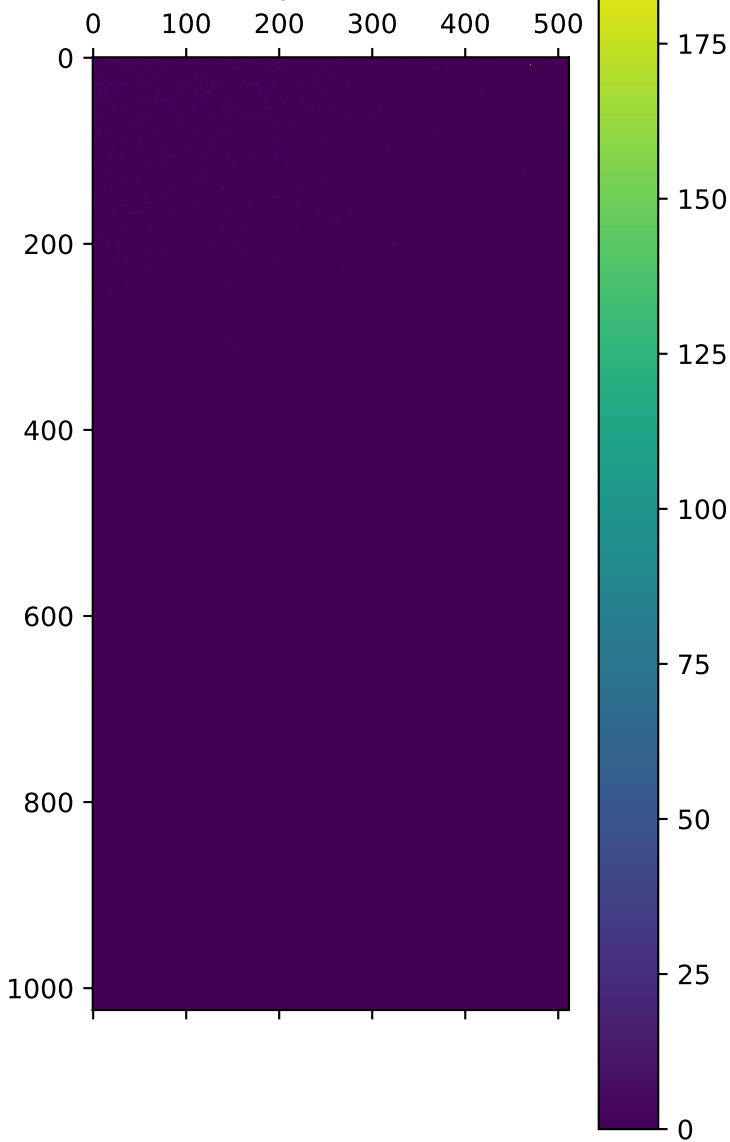
chip id:0x71



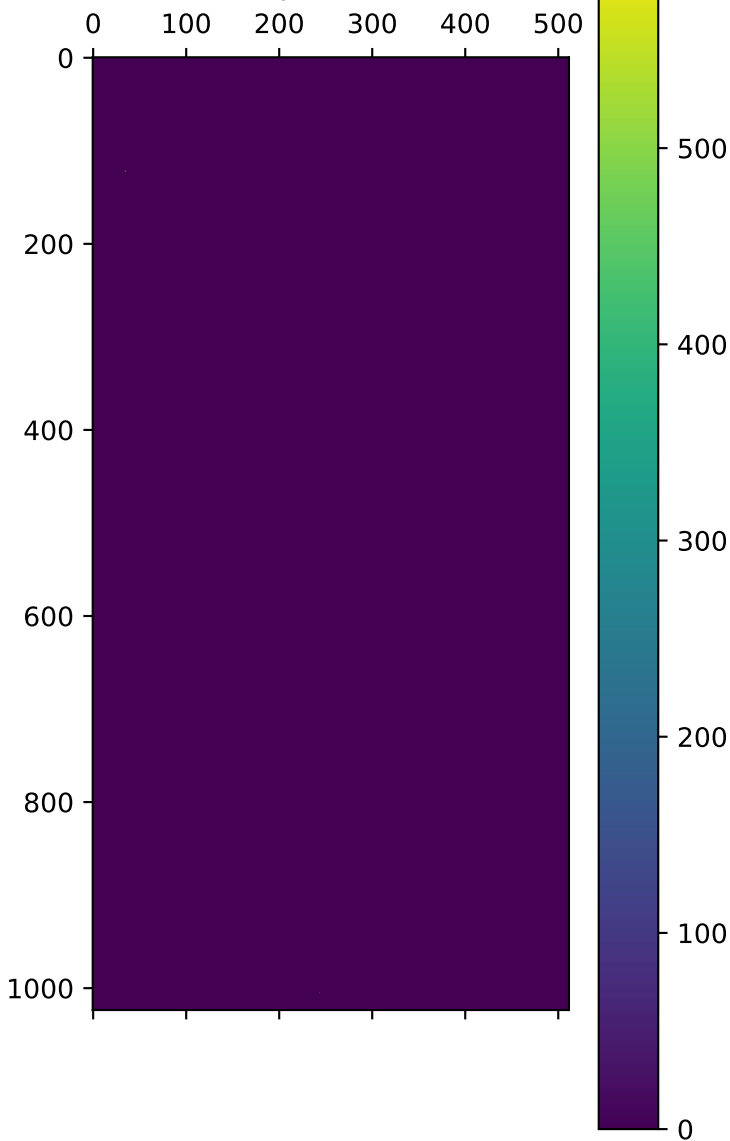
chip id:0x72



chip id:0x73



chip id:0x78



chip id:0x79

0 100 200 300 400 500

0

200

400

600

800

1000

5

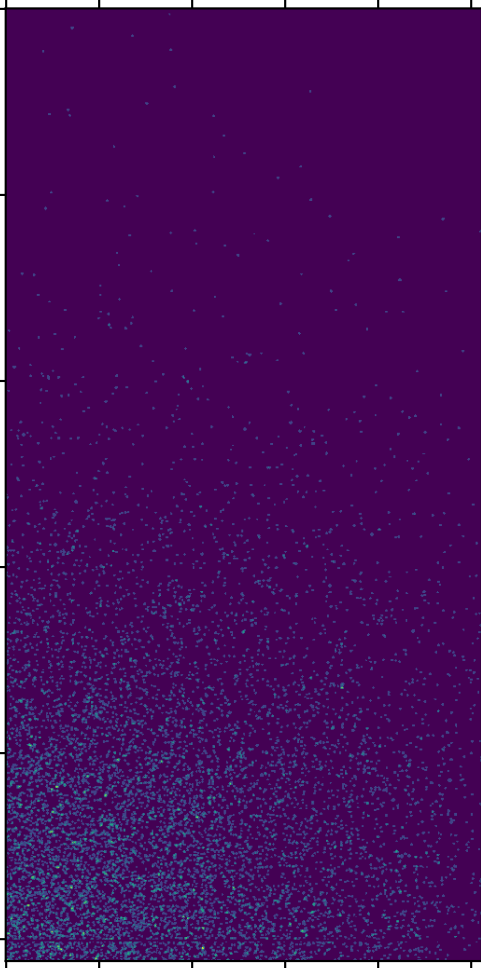
4

3

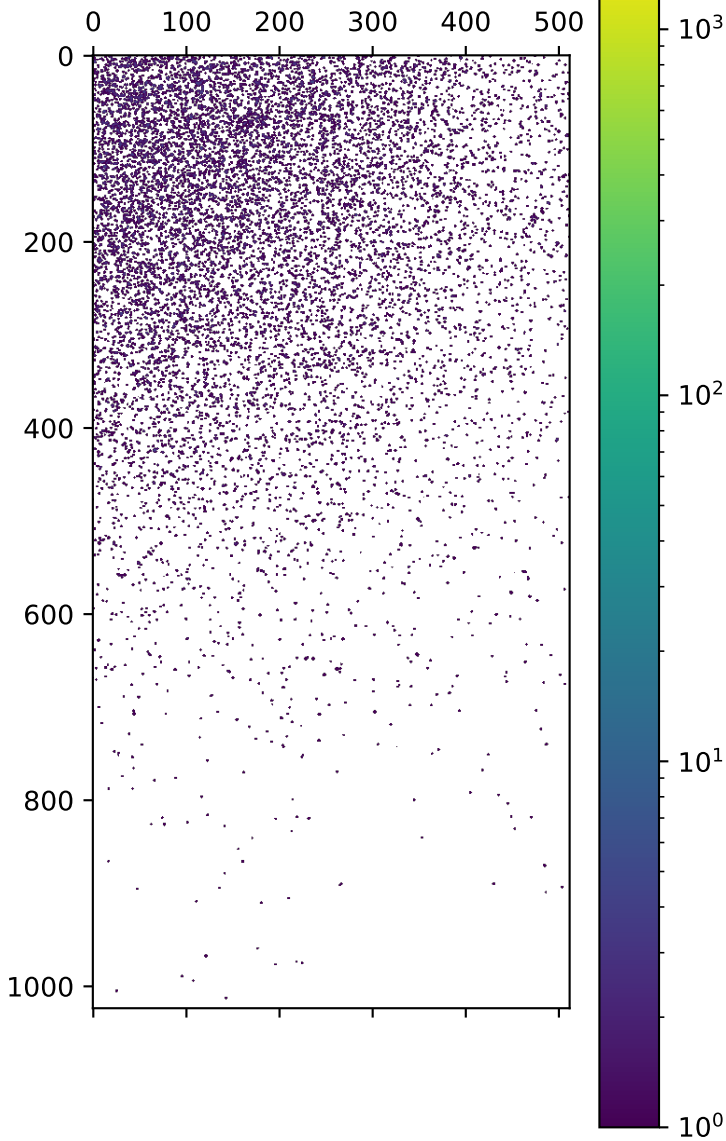
2

1

0



chip id:0x7a



chip id:0x7b

0 100 200 300 400 500

0

200

400

600

800

1000

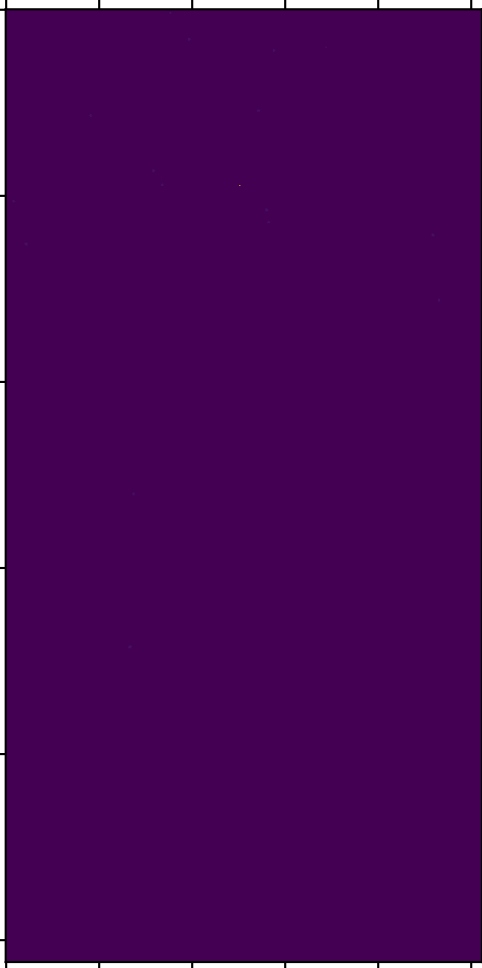
20

15

10

5

0



chip id:0x7c

0 100 200 300 400 500

0

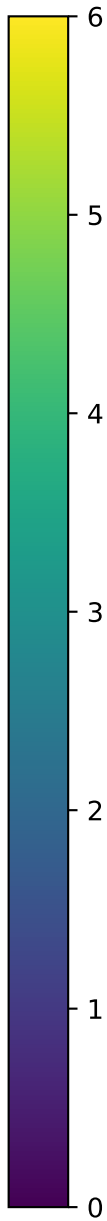
200

400

600

800

1000



chip id:0x170

0 100 200 300 400 500

0

200

400

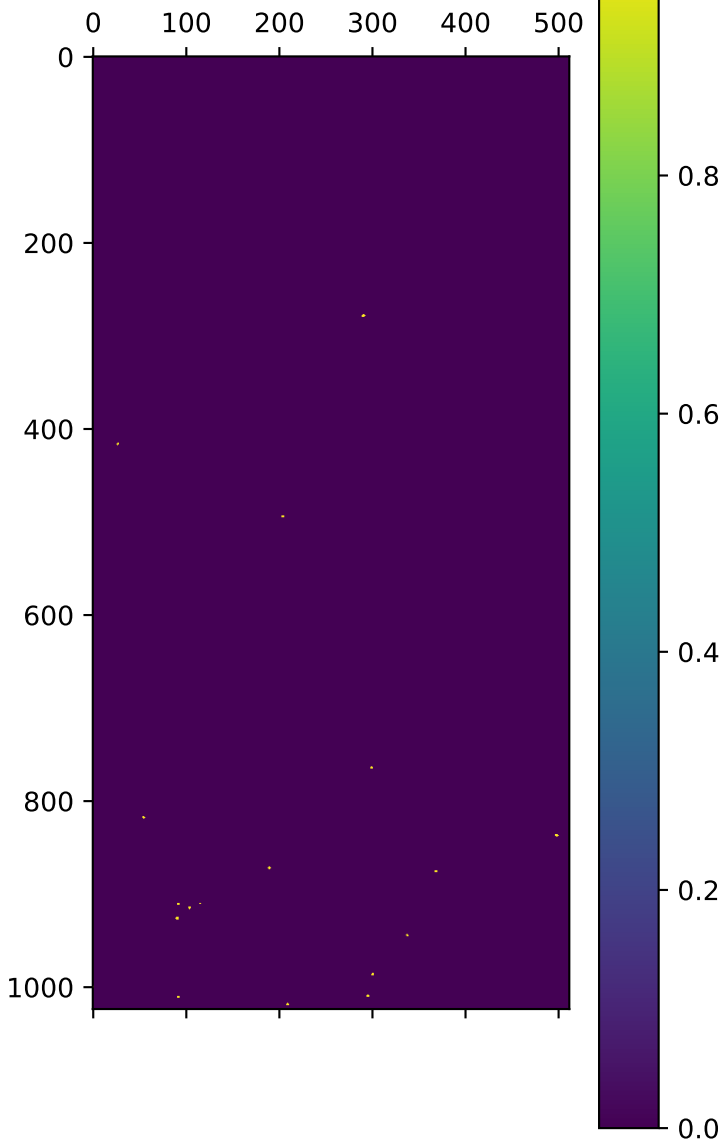
600

800

1000



chip id:0x171



chip id:0x172

0 100 200 300 400 500

0

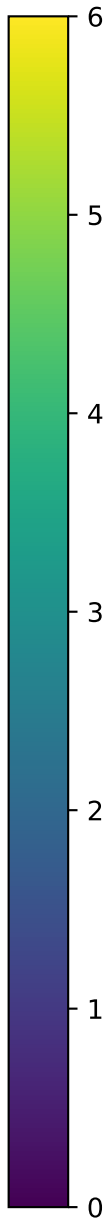
200

400

600

800

1000



chip id:0x173

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x178

0 100 200 300 400 500

0

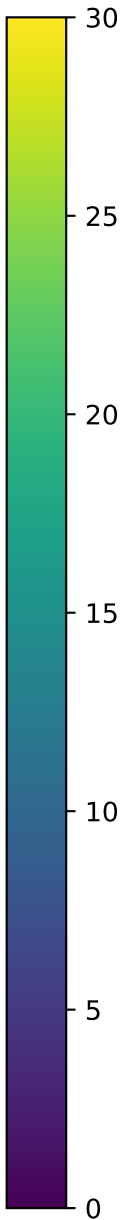
200

400

600

800

1000



chip id:0x179

0 100 200 300 400 500

0

200

400

600

800

1000

6

5

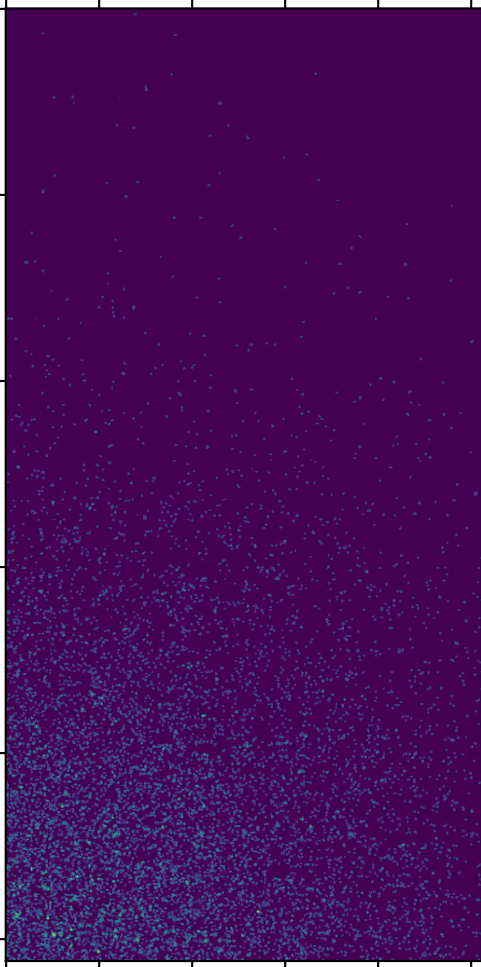
4

3

2

1

0



chip id:0x17a

0 100 200 300 400 500

0

200

400

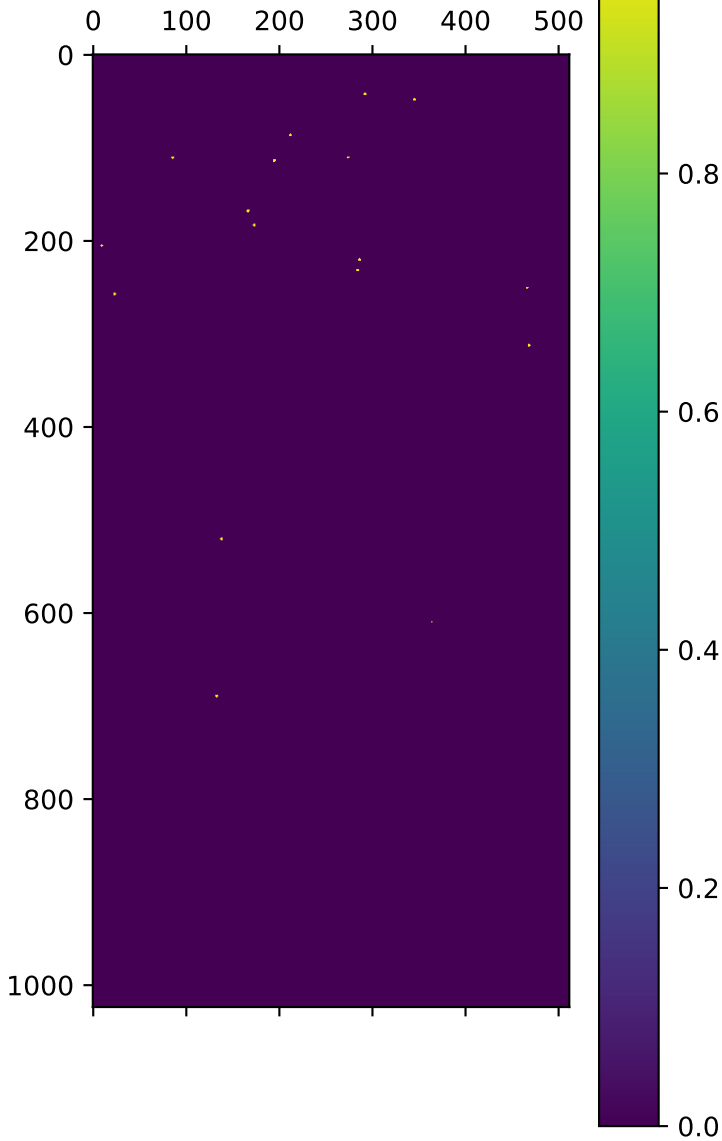
600

800

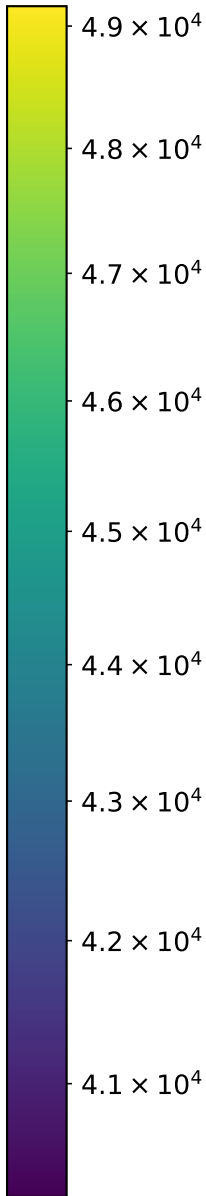
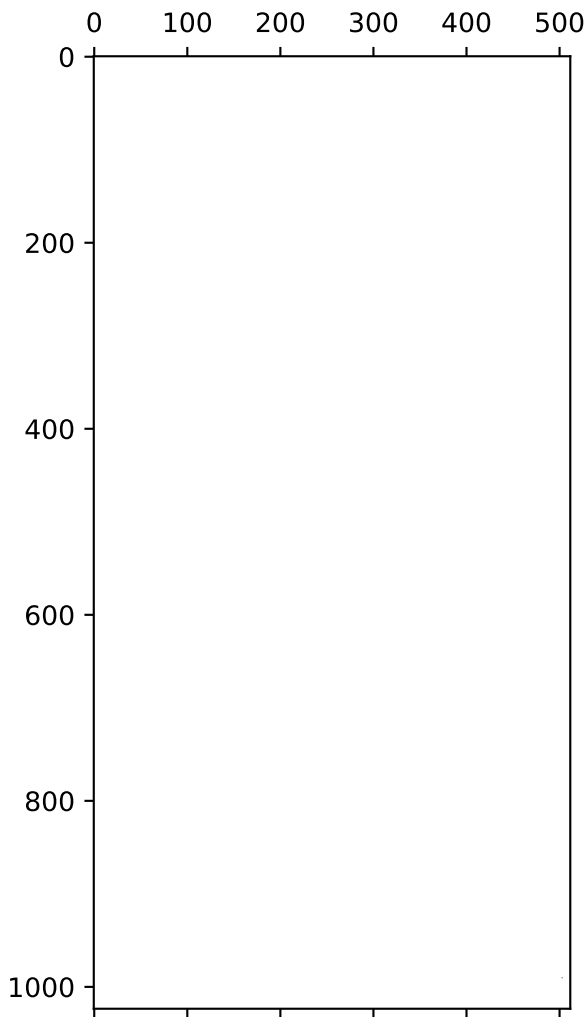
1000



chip id:0x17b



chip id:0x17c



chip id:0x270

0 100 200 300 400 500

0

200

400

600

800

1000

4.8×10^3

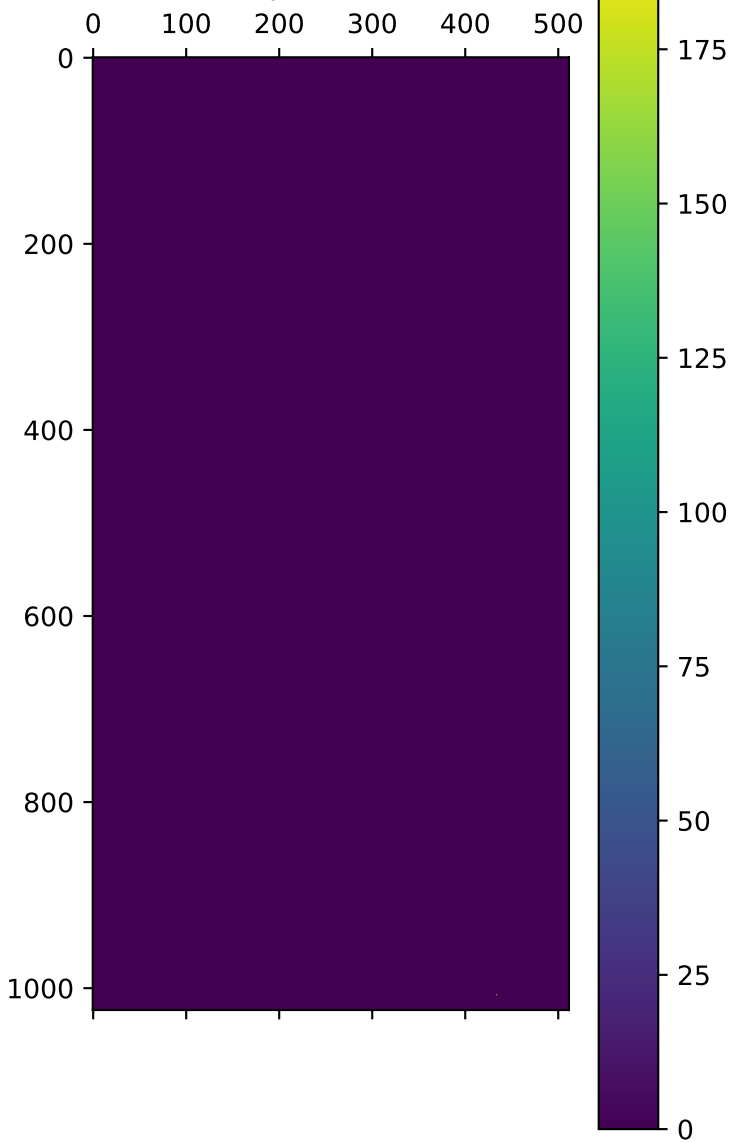
4.6×10^3

4.4×10^3

4.2×10^3



chip id:0x271



chip id:0x272

0 100 200 300 400 500

0

200

400

600

800

1000

7

6

5

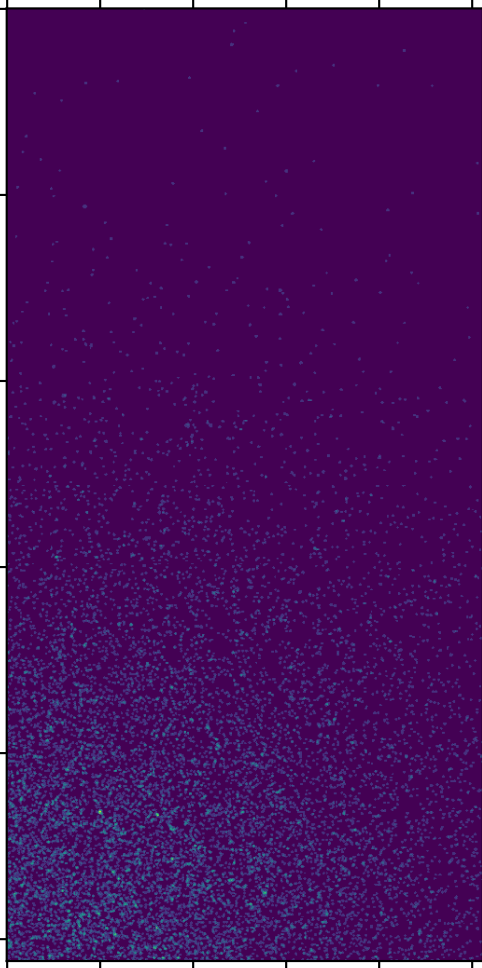
4

3

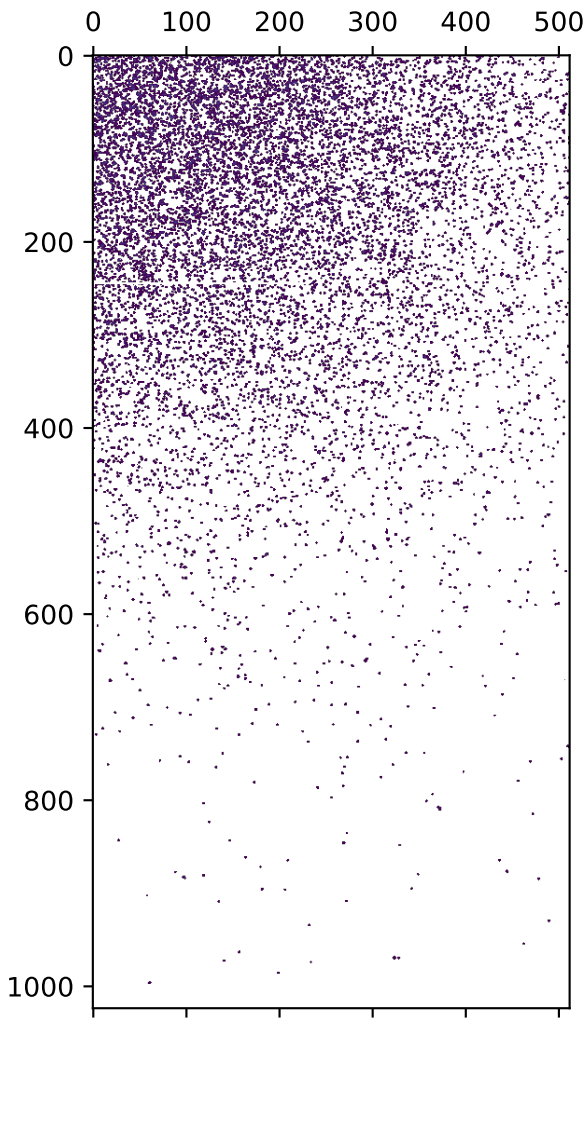
2

1

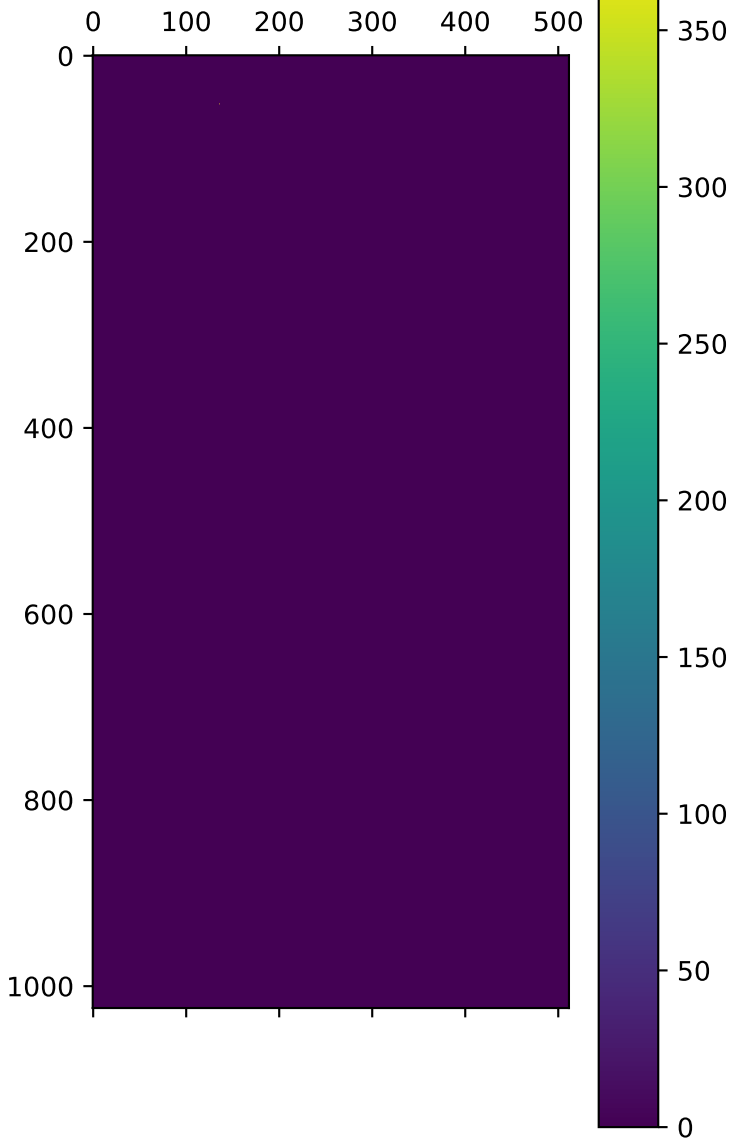
0



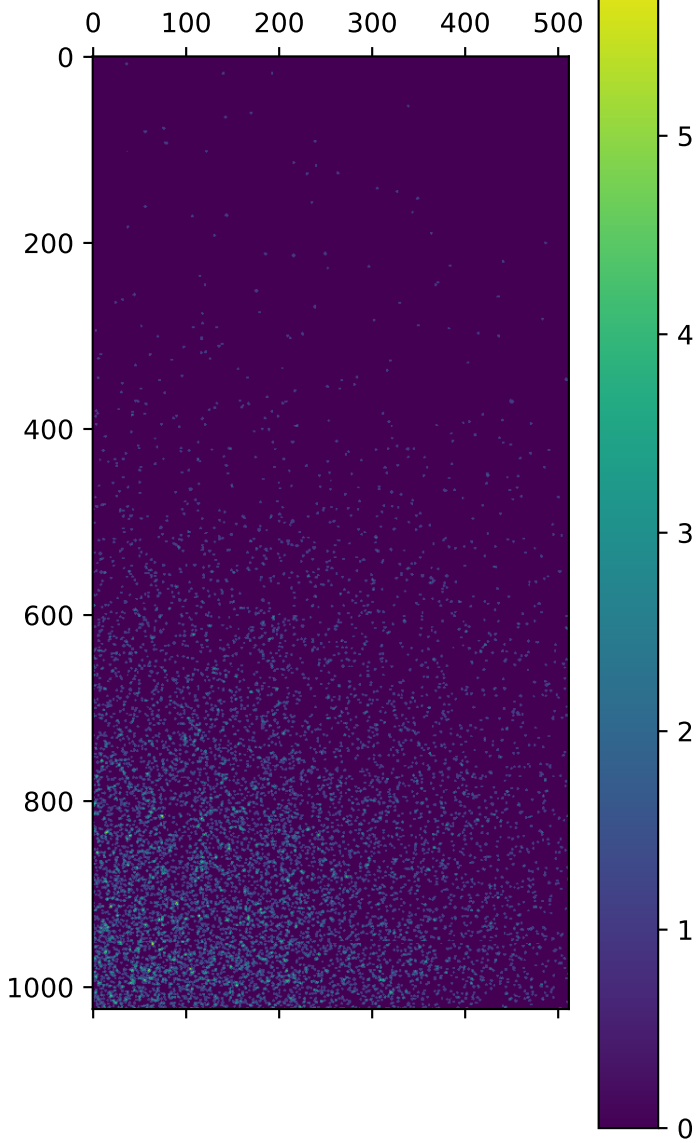
chip id:0x273



chip id:0x278



chip id:0x279



chip id:0x27a

0 100 200 300 400 500

0

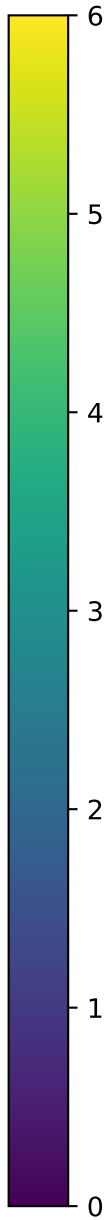
200

400

600

800

1000



chip id:0x27b

0 100 200 300 400 500

0

200

400

600

800

1000

30

25

20

15

10

5

0

