

chip id:0x71

0 100 200 300 400 500

0

200

400

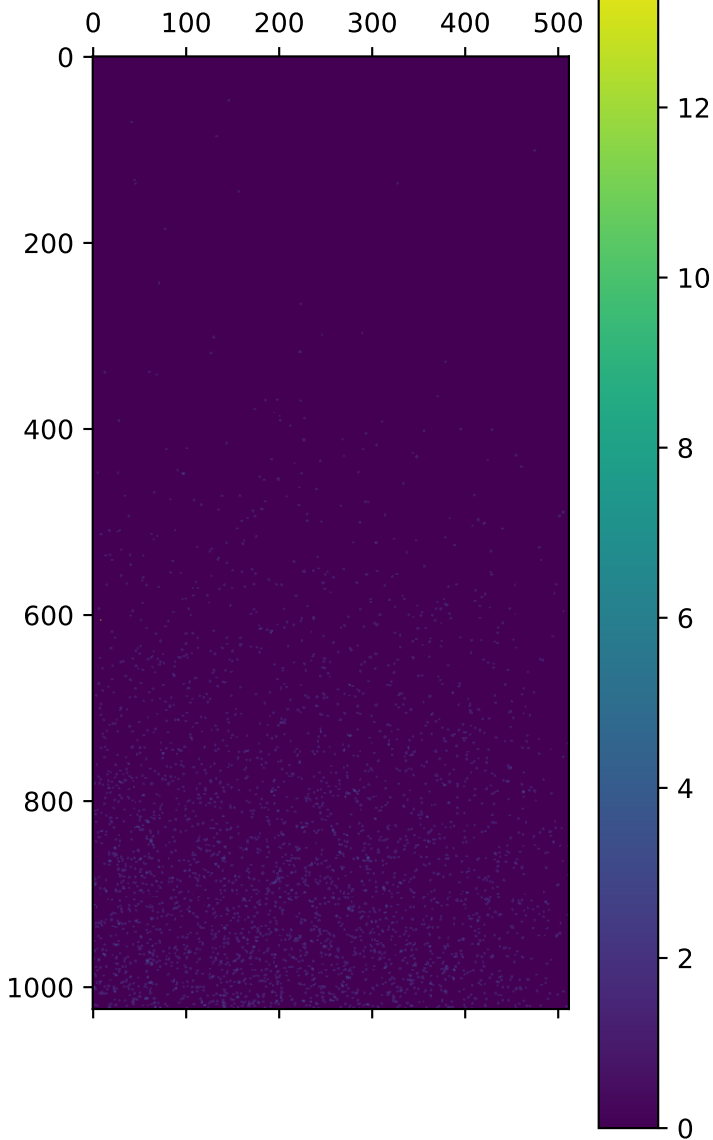
600

800

1000



chip id:0x72



chip id:0x73

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x79

0 100 200 300 400 500

0

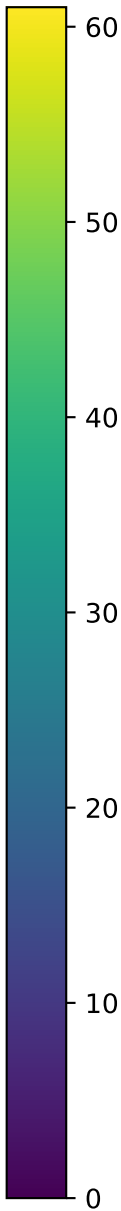
200

400

600

800

1000



chip id:0x7a

0 100 200 300 400 500

0

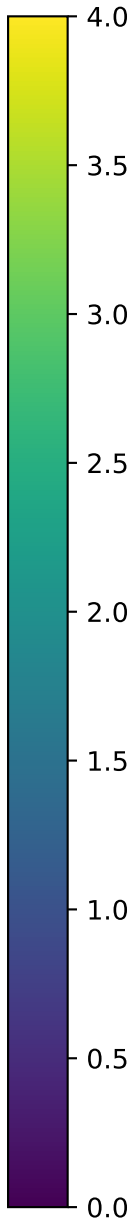
200

400

600

800

1000



chip id:0x7b

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x171

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x172

0 100 200 300 400 500

0

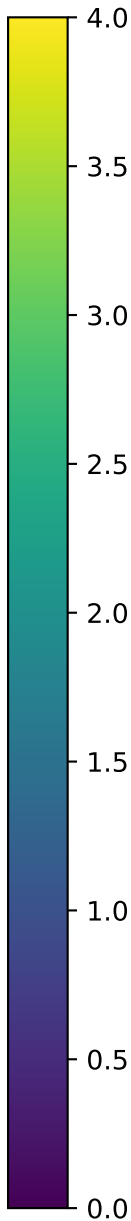
200

400

600

800

1000





chip id:0x173

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x178

0 100 200 300 400 500

0

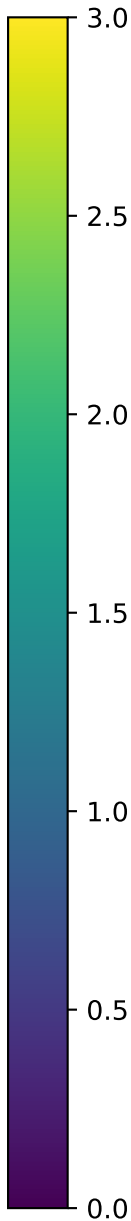
200

400

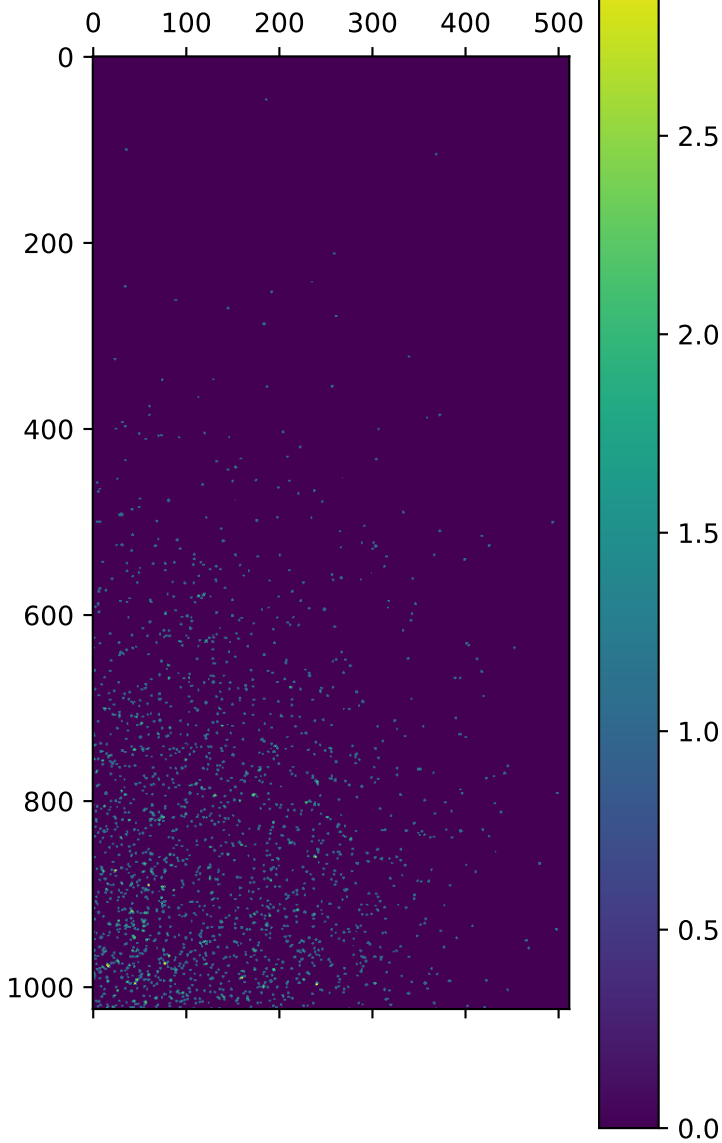
600

800

1000



chip id:0x179



chip id:0x17a

0 100 200 300 400 500

0

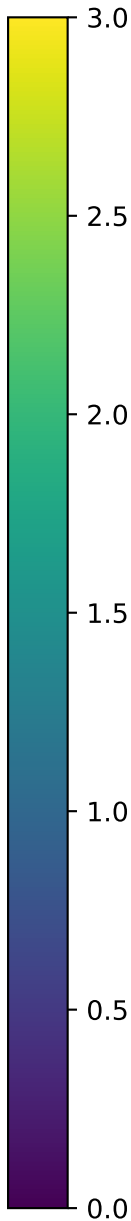
200

400

600

800

1000



chip id:0x17b

0 100 200 300 400 500

0

200

400

600

800

1000



chip id:0x271

0 100 200 300 400 500

0

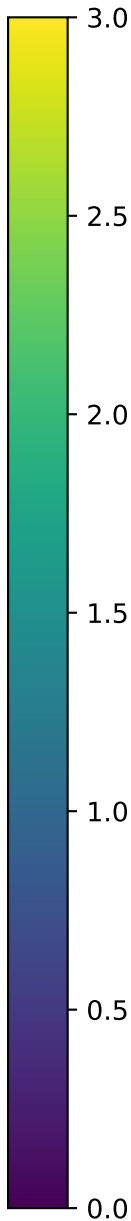
200

400

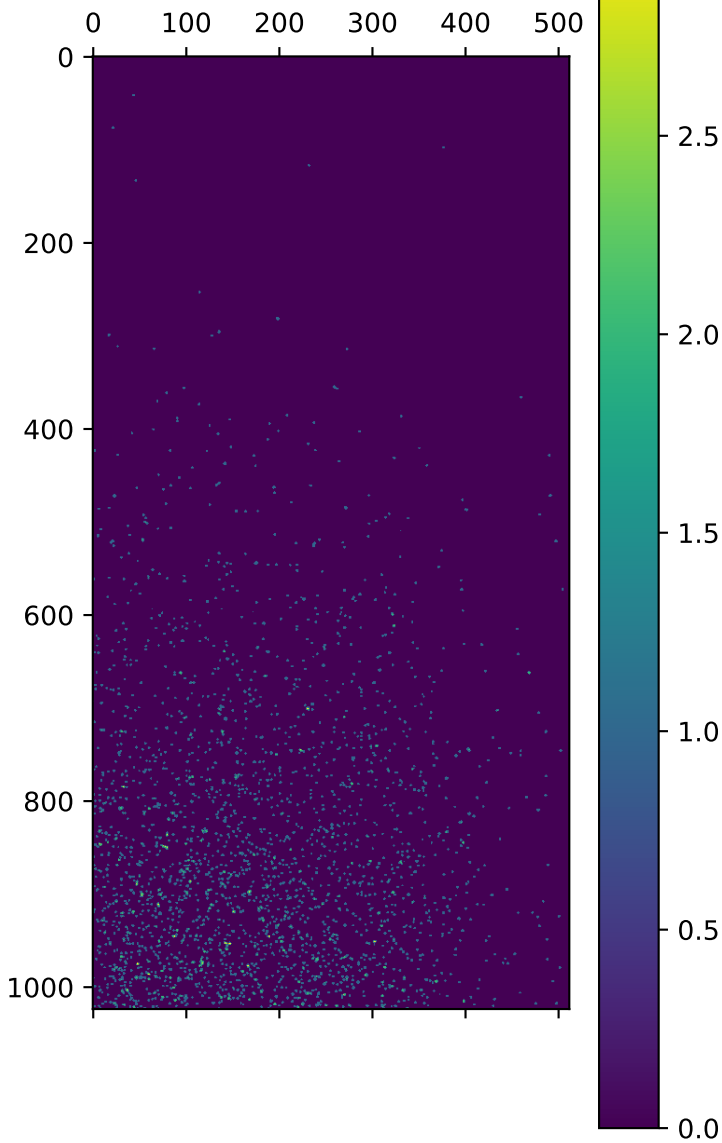
600

800

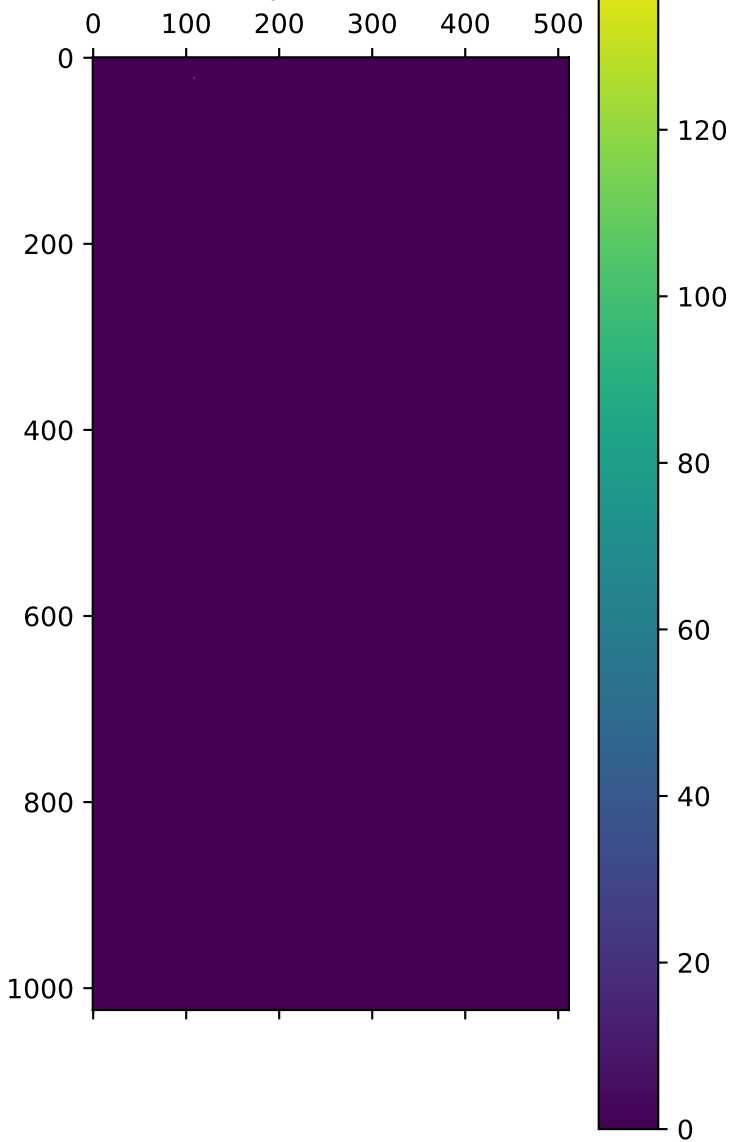
1000



chip id:0x272

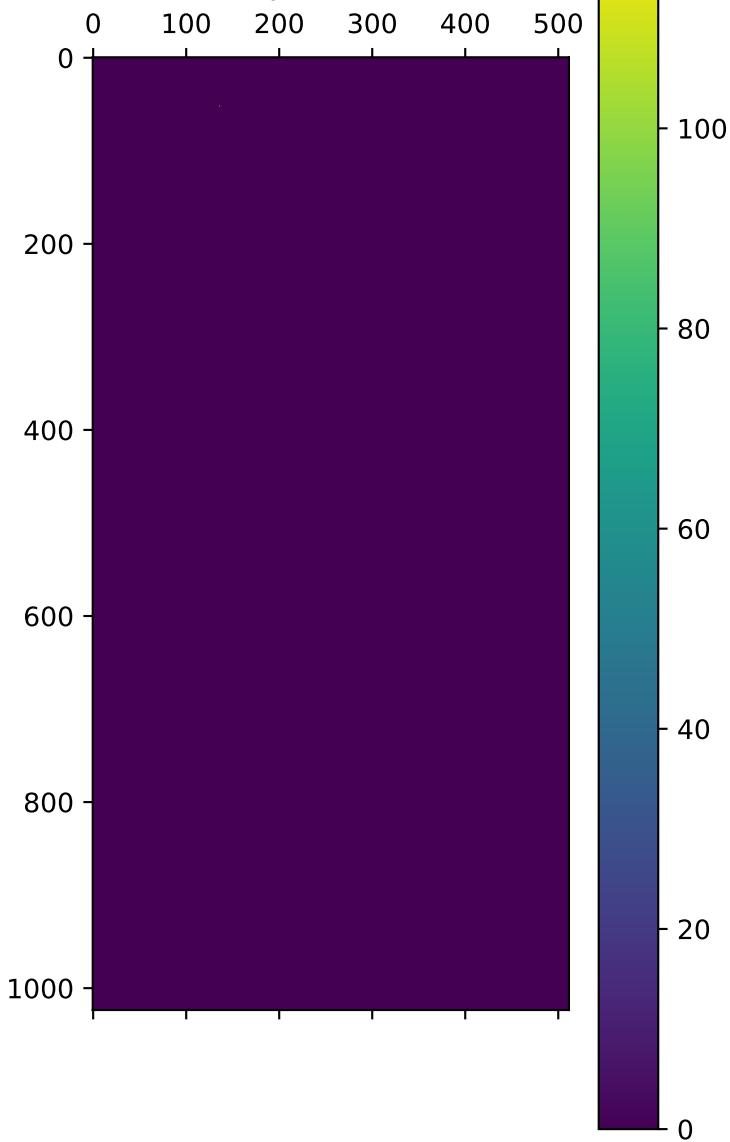


chip id:0x273





chip id:0x278



chip id:0x279

0 100 200 300 400 500

0

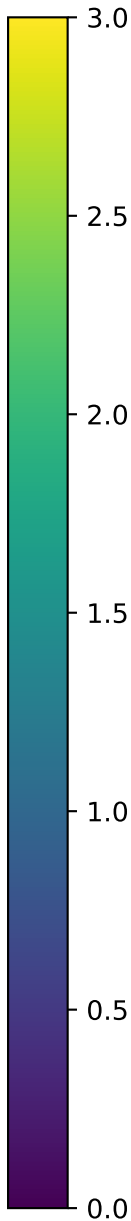
200

400

600

800

1000



chip id:0x27a

0 100 200 300 400 500

0

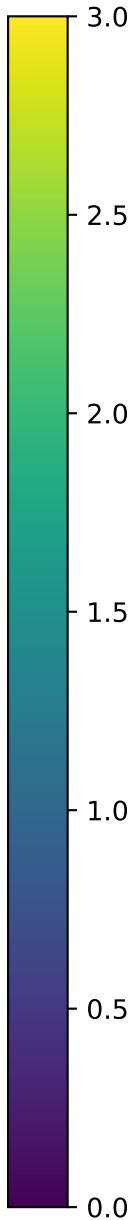
200

400

600

800

1000



chip id:0x27b

0 100 200 300 400 500

0

200

400

600

800

1000

