

HP 150

	33	65
BA(0) →	← PwrWr	← PwrRd
BA(1) →	← PwrOK	← Gnd
BA(2) →	← <u>StETCS</u>	← Gnd
BA(3) →	← BRst	← +5V
BA(4) →	← IR4	← Gnd
BA(5) →	← ExWait	← +5V
BA(6) →	← +5V	← Gnd
BA(7) →	← B10/M	← Gnd
BA(8) →	← -12V	← Gnd
BA(9) →	← IRO	← Gnd
BA(10) →	← -12V	← Gnd
BA(11) →	← BG0	← Gnd
BA(12) →	← Gnd	← Gnd
BA(13) →	← Gnd	← Gnd
BA(14) →	← BC1h	← Gnd
BA(15) →	← Gnd	← Gnd
BA(16) →	← Gnd	← Gnd
BA(17) →	← B10/Ma	← Gnd
BA(18) →	← +12V	← Gnd
BA(19) →	← BDT/R	← Gnd
BD(0) →	← +12V	← Gnd
BD(1) →	← LH1DA	← Gnd
BD(2) →	← BWr	← Gnd
BD(3) →	o(N/C)	← +5V
BD(4) →	← BRd	← +5V
BD(5) →	← +5V	← +5V
BD(6) →	← (sp)	← +5V
BD(7) →	← Gnd	← TSData
<u>BSS0</u> →	← TSC1h	← Gnd
+Vbut →	o(N/C)	← IR1
+Vbut →	← Memsize	← TSSync
	32	96

	36
BA(0) →	← +5V
BA(1) →	← Gnd
BA(2) →	← <u>BRst</u>
BA(3) →	← Gnd
BA(4) →	← <u>ExWait</u>
BA(5) →	← +5V
BA(6) →	← B10/M
BA(7) →	← Gnd
BA(8) →	← IRO
BA(9) →	← Gnd
BA(10) →	← BG0
BA(11) →	← Gnd
BA(12) →	← BC1h
BA(13) →	← Gnd
BA(14) →	← +5V
BA(15) →	← Gnd
BA(16) →	← B10/Ma
BA(17) →	← Gnd
BA(18) →	← BDT/R
BA(19) →	o(N/C)
BD(0) →	← +5V
BD(1) →	o(N/C)
BD(2) →	← <u>BRd</u>
BD(3) →	← Gnd
BD(4) →	← (sp)
BD(5) →	← Gnd
BD(6) →	← +5V
BD(7) →	o-Gnd
+5V →	← VSync
+5V →	← Gnd
+12V →	o-HSync
+12V →	← Gnd
+12V →	← FullBr
+Vbut →	← FullBr
+Vbut →	← Gnd

35 70

LHS
↑
Top
CPU PCB →
Backplane

LHS
↑
Top
Video PCB
→ Backplane

MP150

36

BA(0) →	0-Gnd
BA(1) →	0-PwrOK
BA(2) →	0-Gnd
BA(3) →	0-BRst
BA(4) →	0+5V
BA(5) →	0-Gnd
BA(6) →	0-Gnd
BA(7) →	0-Gnd
BA(8) →	0-Gnd
BA(9) →	0-ExtWait
BA(10) →	0-Gnd
BA(11) →	0-B10/M
BA(12) →	0+5V
BA(13) →	0-12V
BA(14) →	0-IRI
BA(15) →	0-Gnd
BA(16) →	0-12V
BA(17) →	0-LHLD/A
BA(18) →	0-BG0
BA(19) →	0+5V
Gnd →	0-Gnd
Gnd →	0-BC1k
BD(0) →	0-Gnd
BD(1) →	0-MemSize (Slot1 Only)
BD(2) →	0+12V
BD(3) →	0-Gnd
BD(4) →	0-B10/Ma
BD(5) →	0+12V
BD(6) →	0-BWr
BD(7) →	0+5V
Gnd →	0-BDT/R
<u>BRd</u> →	0-Gnd
Gnd →	0-BSSD
+5V →	0-Gnd
+Vbat →	0-SLutCS
35	20

LHS
Top ↑
Expansion PCB

1	
+5V	0-HSync
Gnd	0+12V
<u>HalfBrk</u>	0-Gnd
Gnd	0+12V
<u>FullBrk</u>	0-Gnd
Gnd	0+12V
<u>MonRst</u>	0-Gnd
-12V	0-Gnd
+12V	0+12V
<u>TSSync</u>	0+5V

Monitor
PCB

1	
+12V	0+12V
<u>TsData</u>	0+12V
-12V	0-Gnd
<u>TSSync</u>	0-TSclk
Gnd	0-Gnd

Touchscreen
PCB

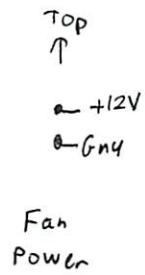
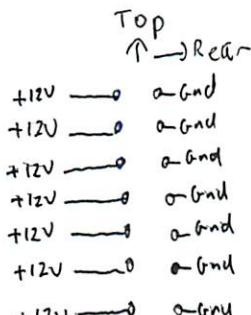
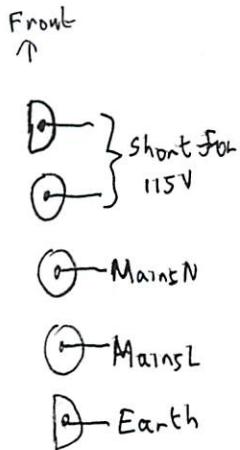
1	
PrtData(0) →	0-PrtData(1)
PrtData(2) →	0-PrtData(3)
PrtData(4) →	0-PrtData(5)
PrtData(6) →	0-PrtData(7)
<u>BRst</u> →	0-PrtStb
<u>PrtAch</u> →	0-PrtPnt
(N/C) 0	0-PrtOnLine
Gnd →	0-Gnd
+5V →	0+5V
(N/C) 0	0(N/C)

Internal
Printer

1	
PwrOK	X
0-Gnd	0-Gnd
0-12V	0+12V
0-Gnd	0-Gnd
0-	0
0+12V	0+12V
0-Gnd	0-Gnd
0+5V	0+5V
0+5V	0+5V
0-Gnd	0-Gnd

PSU Output

MP150

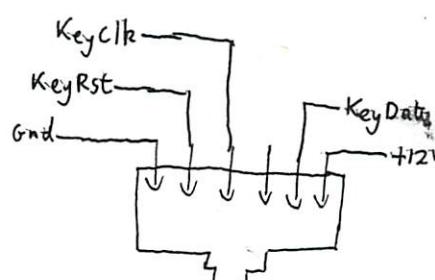
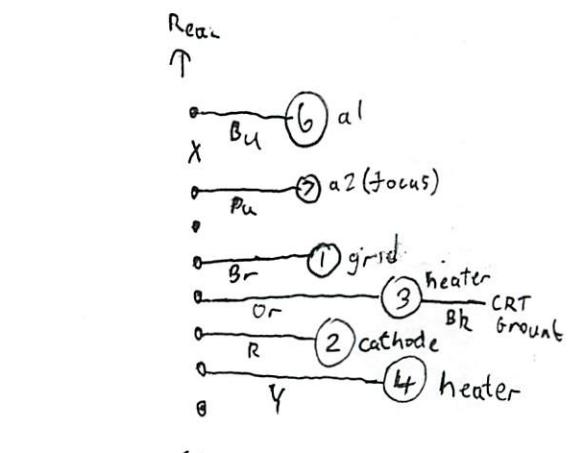


Printer Power

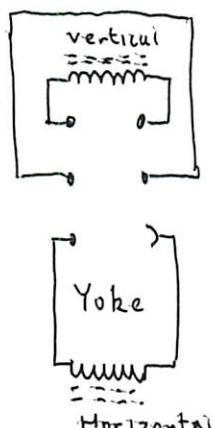
PSU	
	Mains
LHS	Rear ↑
1 31	MA(2)
B10/Ma	MRst
Gnd	MA(1)
MA(3)	+5V
BA(10)	BA(8)
MRd	B10/M
MA(6)	MA(5)
LH1dA	Config OE
MWr	Gnd
Gnd	+5V
Gnd	Gnd
+VBack	MA(7)
DEn	+5V
+5V	Memsize
Gd	+5V
MemDEN	MA(4)
Config Wr	Config CS
MA(13)	MA(12)
Mclk	MA(18)
MA(14)	MA(17)
MA(16)	MemWait
CPUAD(7)	MA(15)
MOT/R	CPUAD(6)
CPUAD(4)	CPUAD(5)
CPUAD(2)	CPUAD(3)
CPUAD(0)	CPUAD(1)
BPwrOK	MHold
BA(9)	StotCS
MA(19)	BA(11)
+5V	StotCS
30	60

Memory PCB
→ CPU PCB

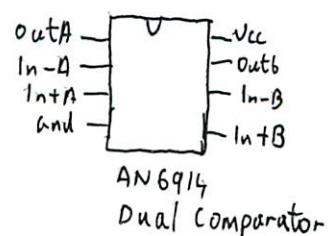
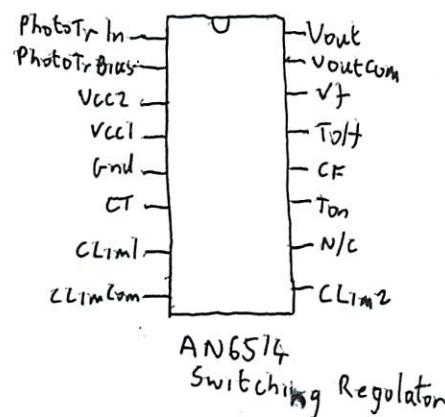
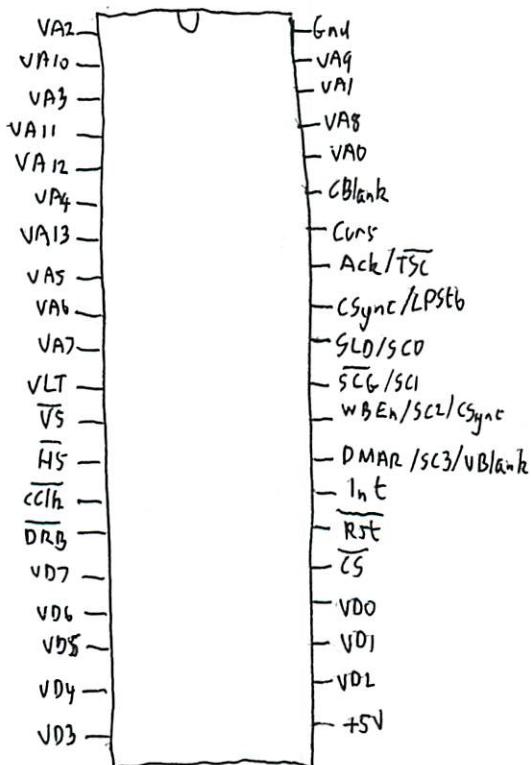
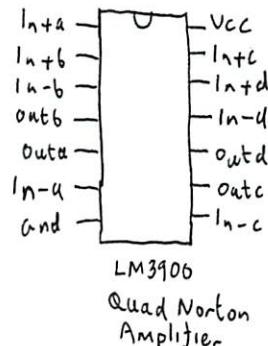
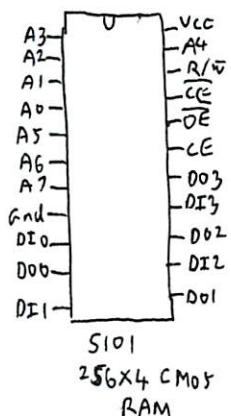
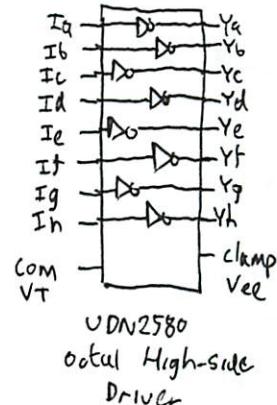
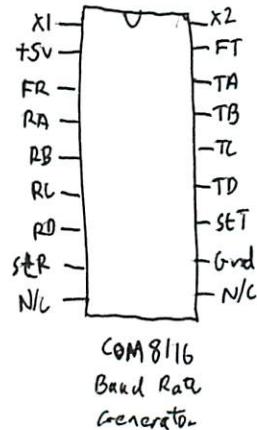
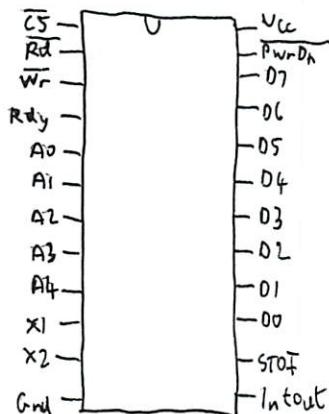
		Rear ↑
1	19	LHS ↑
+12V	→ -12V	
+5V	→ +5V	
RxCA	→ TxCA	
DCDA	→ CTSA	
PortAFit	→ RTSA	
RxD	→ TxDA	
DTRA	→ PD(7)	
Gnd	→ PD(6)	
PD(4)	→ PD(5)	
PD(2)	→ PD(3)	
PD(0)	→ PD(1)	
Gnd	→ BaudA	
SerAcS	→ PU	
MRd	→ CPUWr	
Gnd	→ Rst	
X	X	
X	X	
X	X	
18	36	
RS232 PCB → CPU PCB		

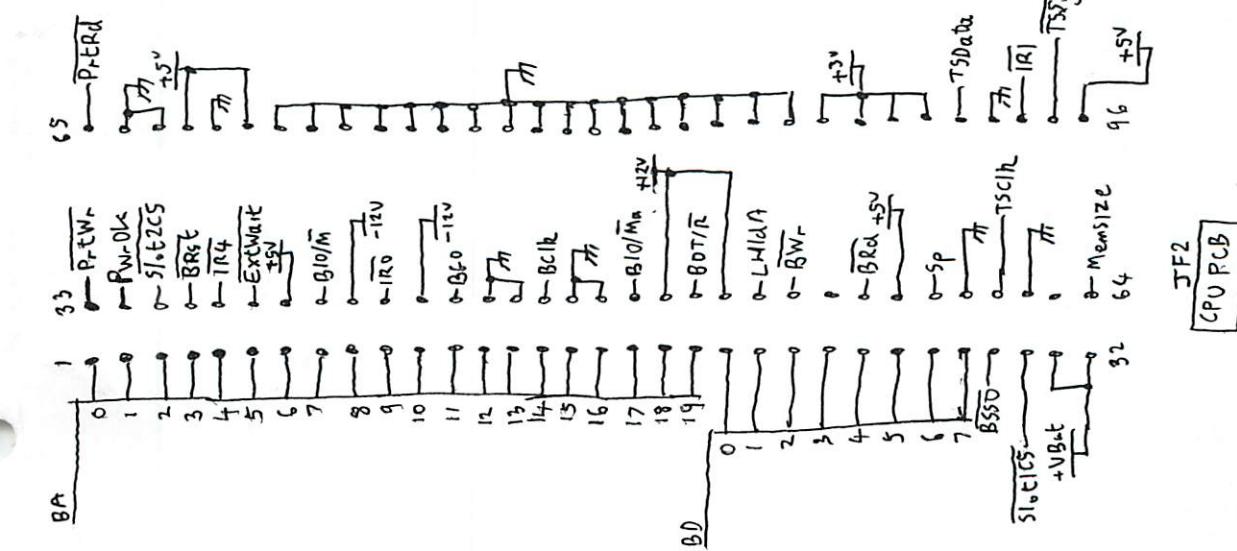
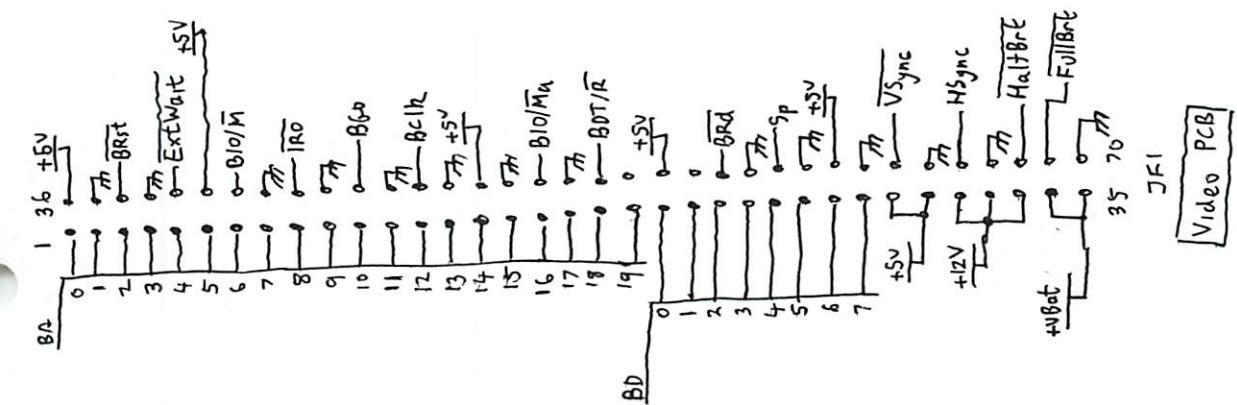
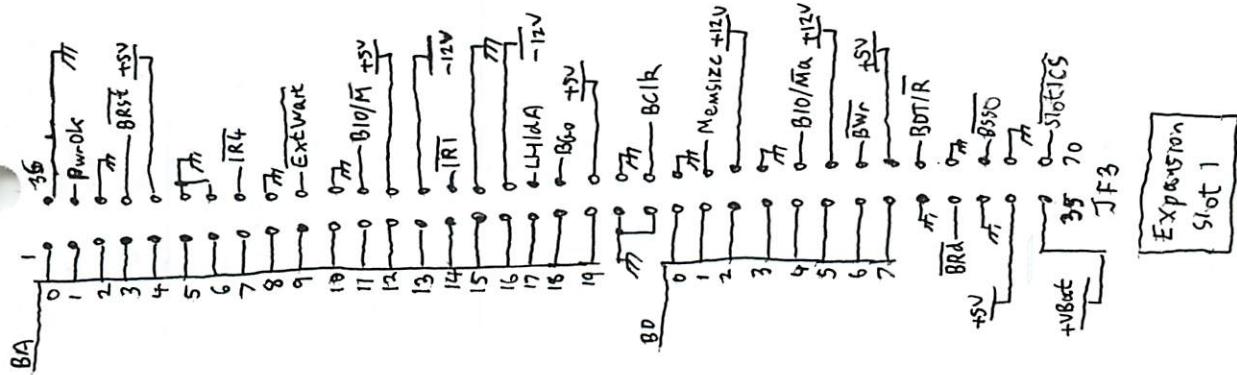
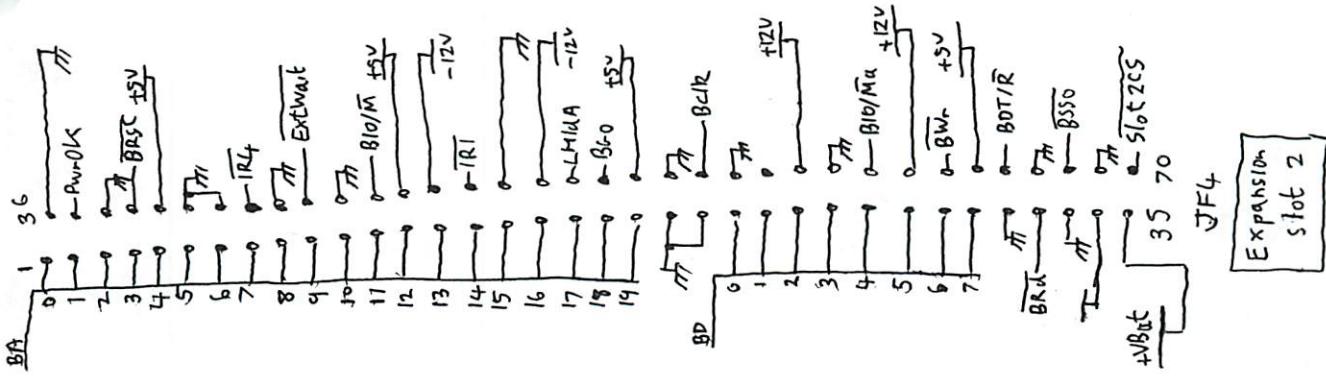


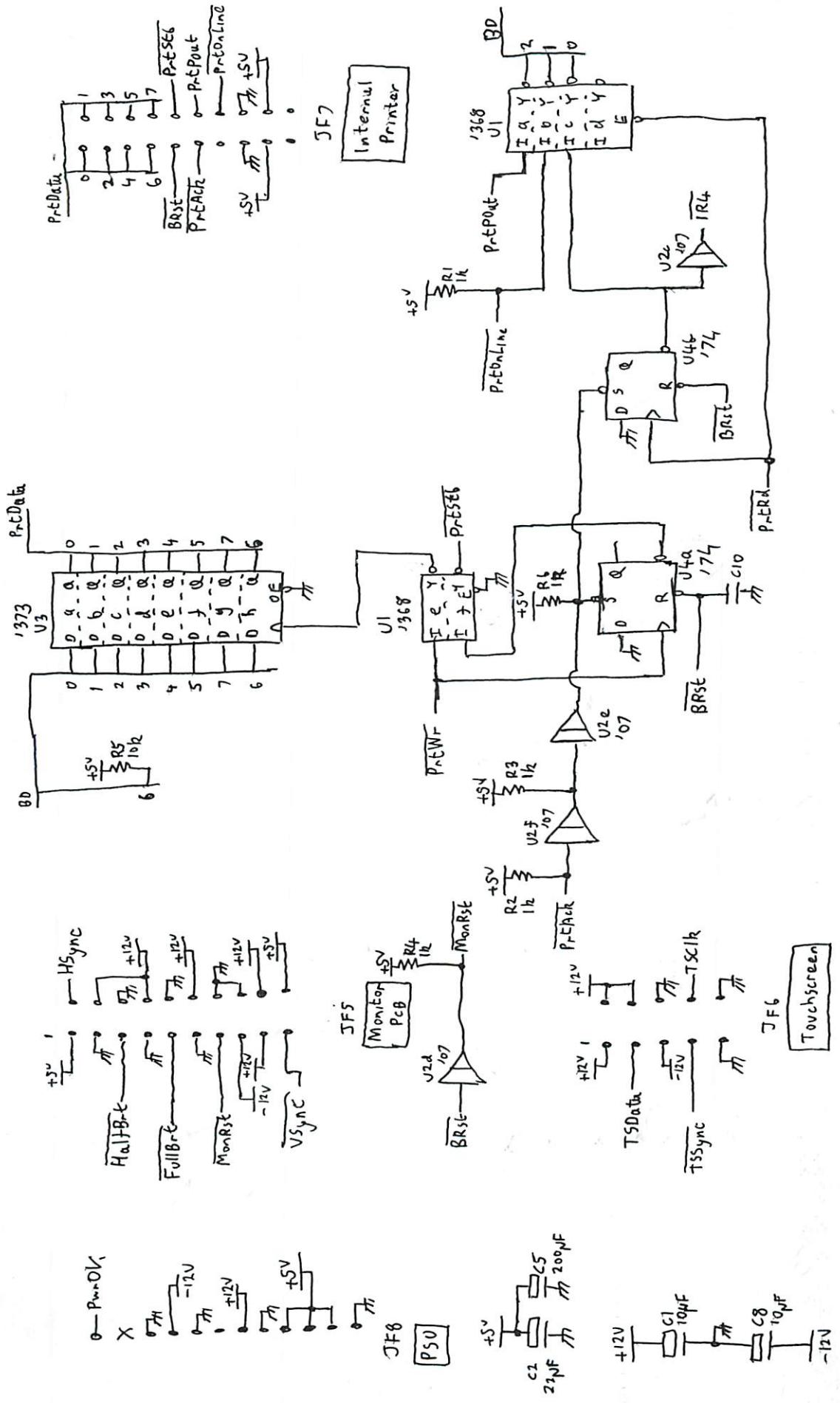
Keyboard

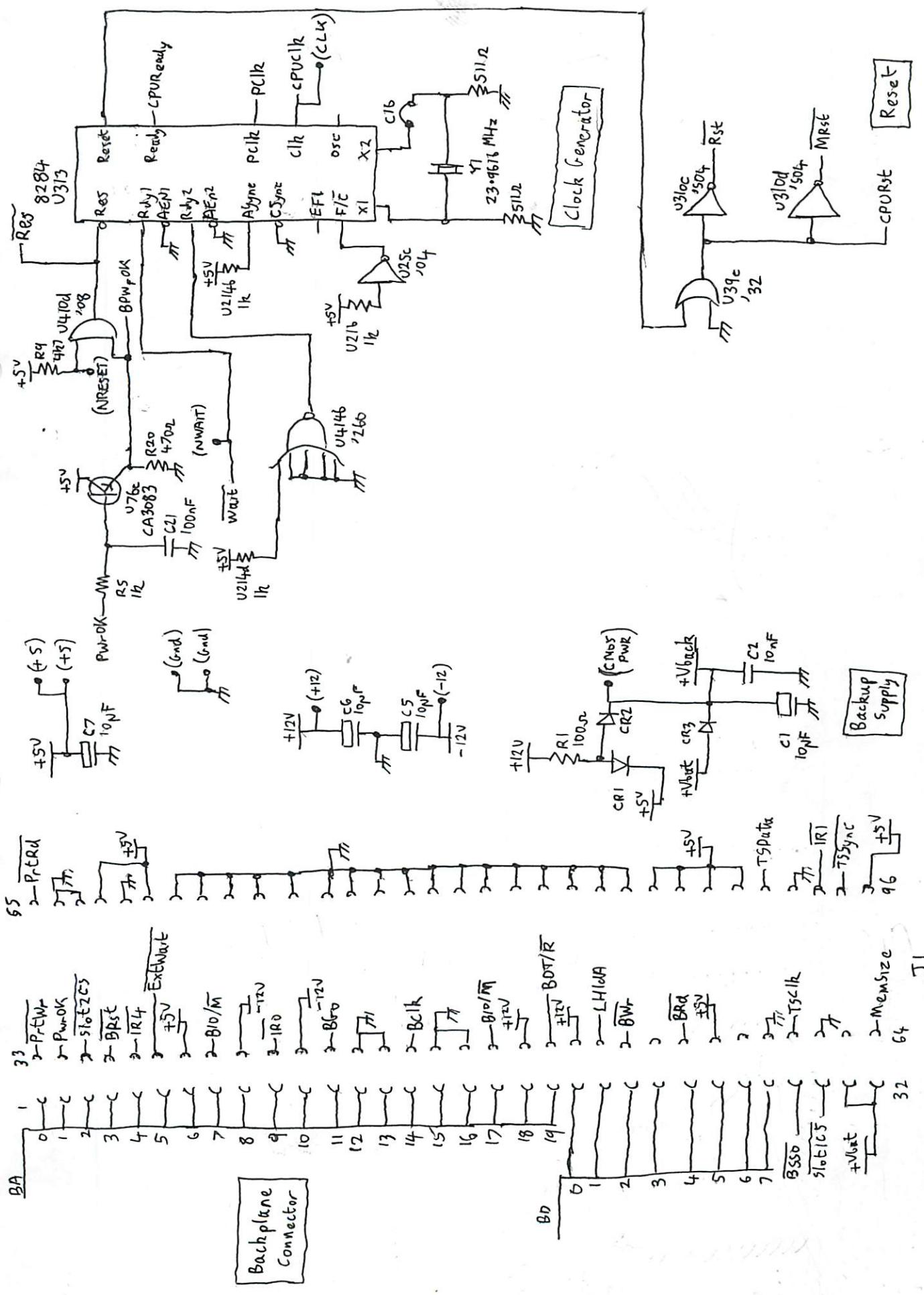


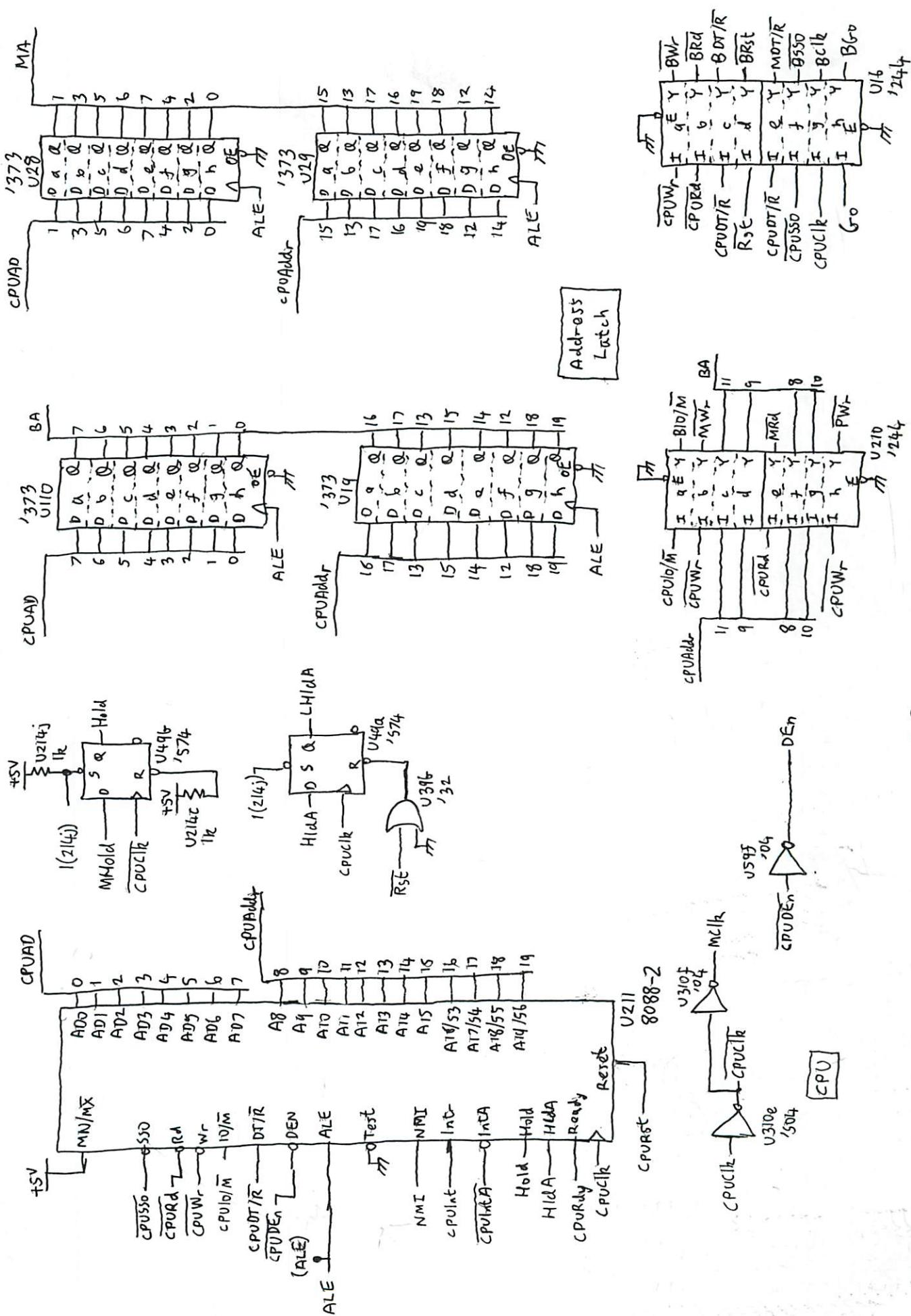
MP150

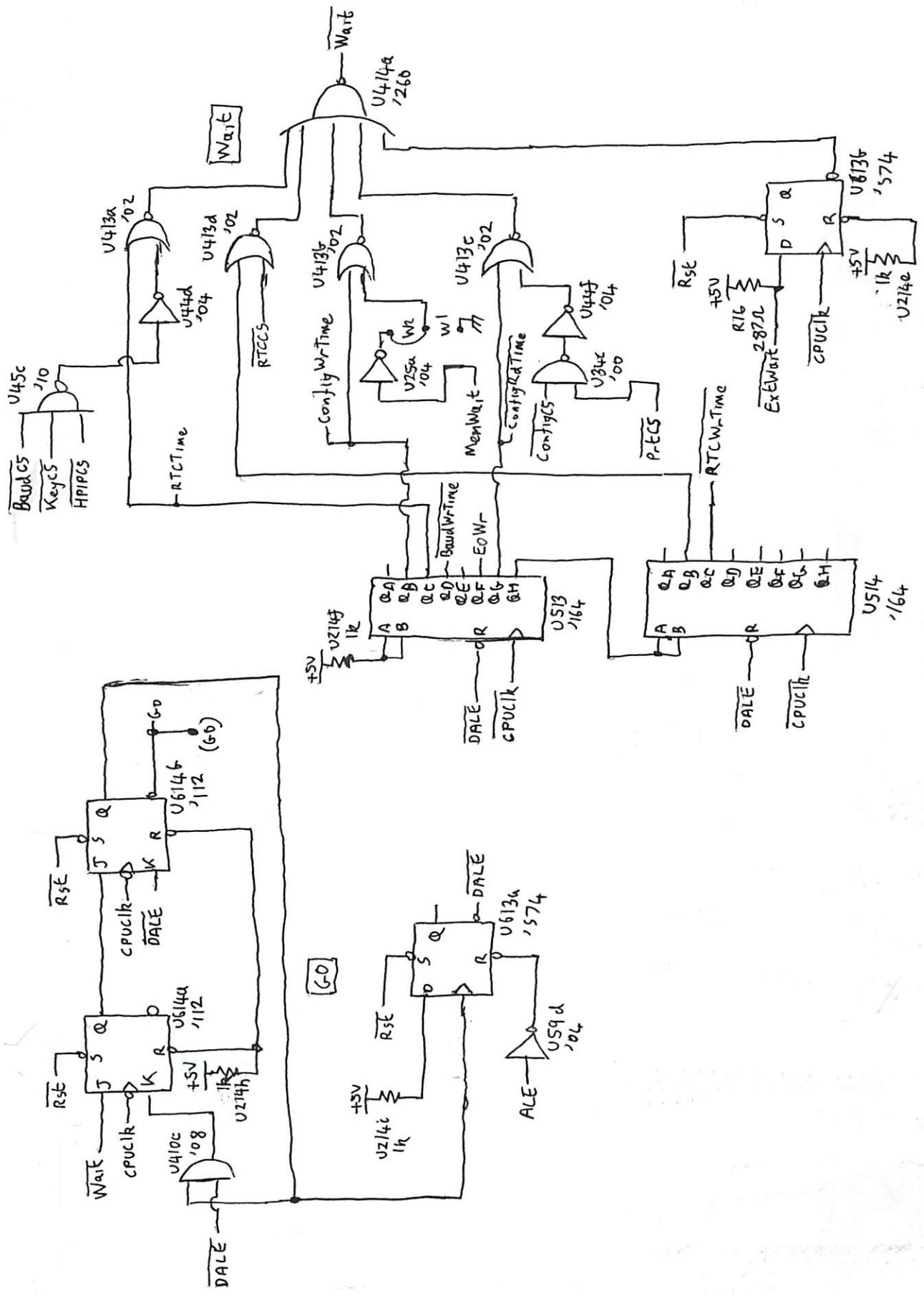






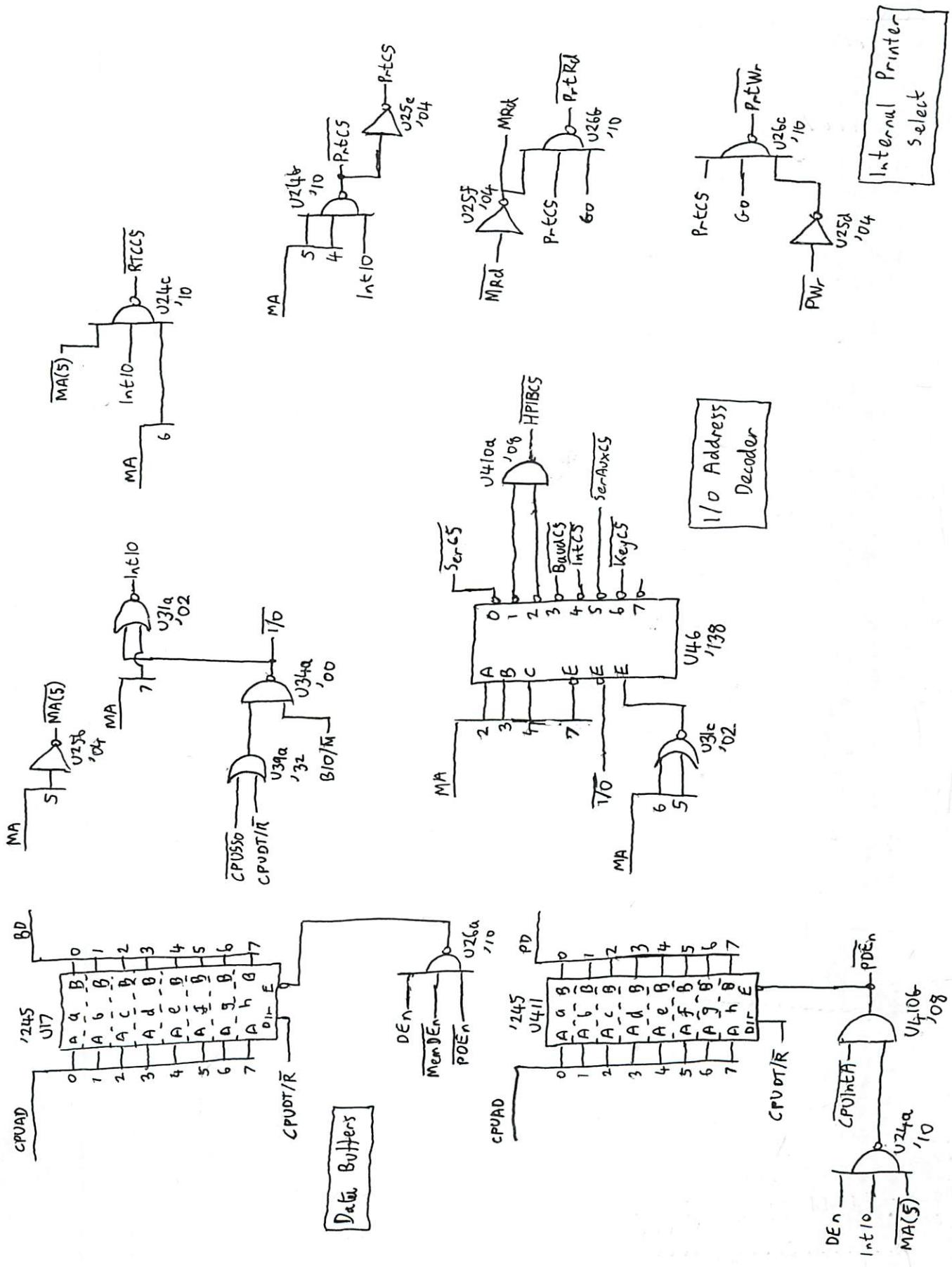


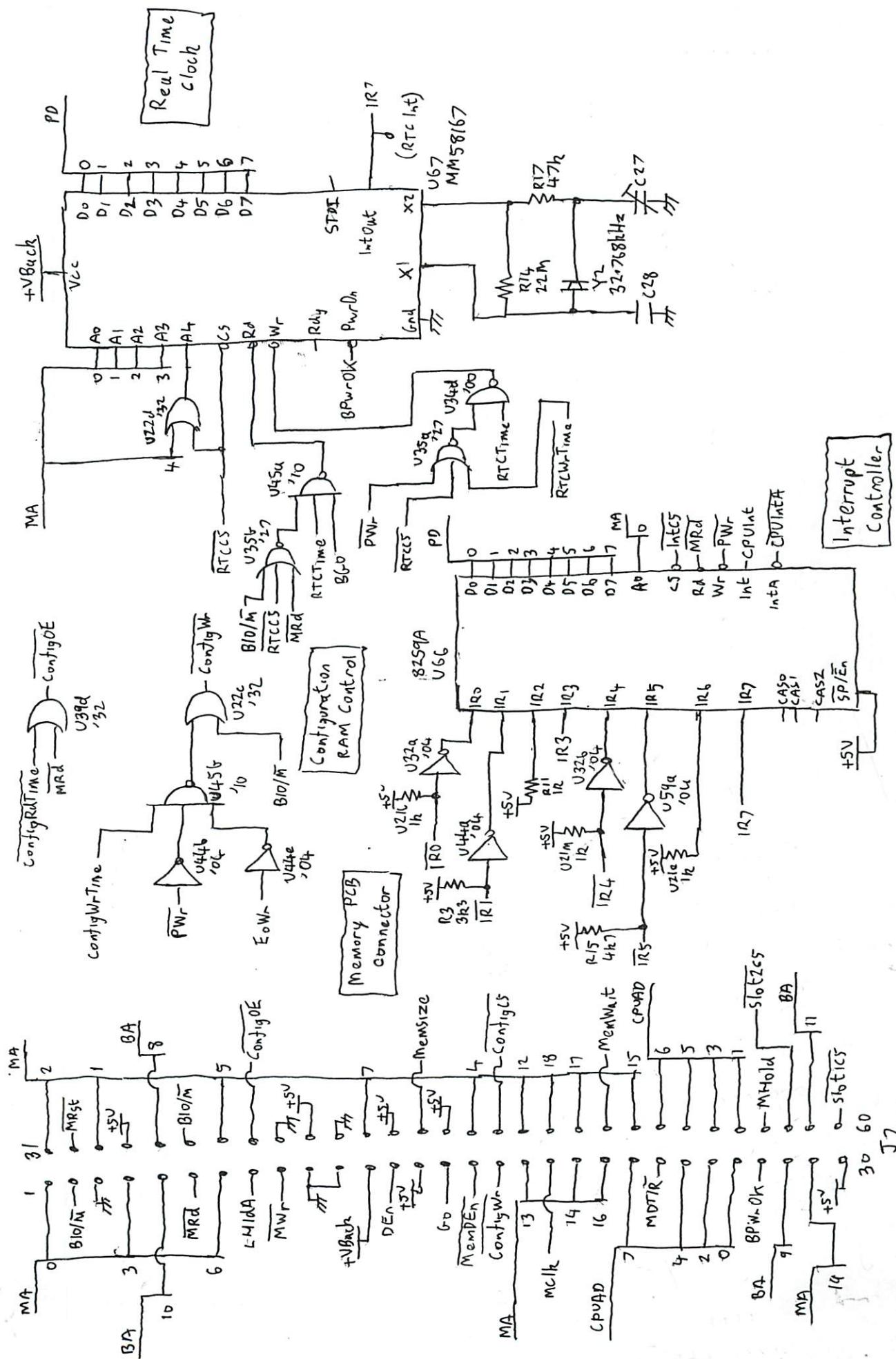




45611 - 60002

MP150 CPU PCB Sheet ③

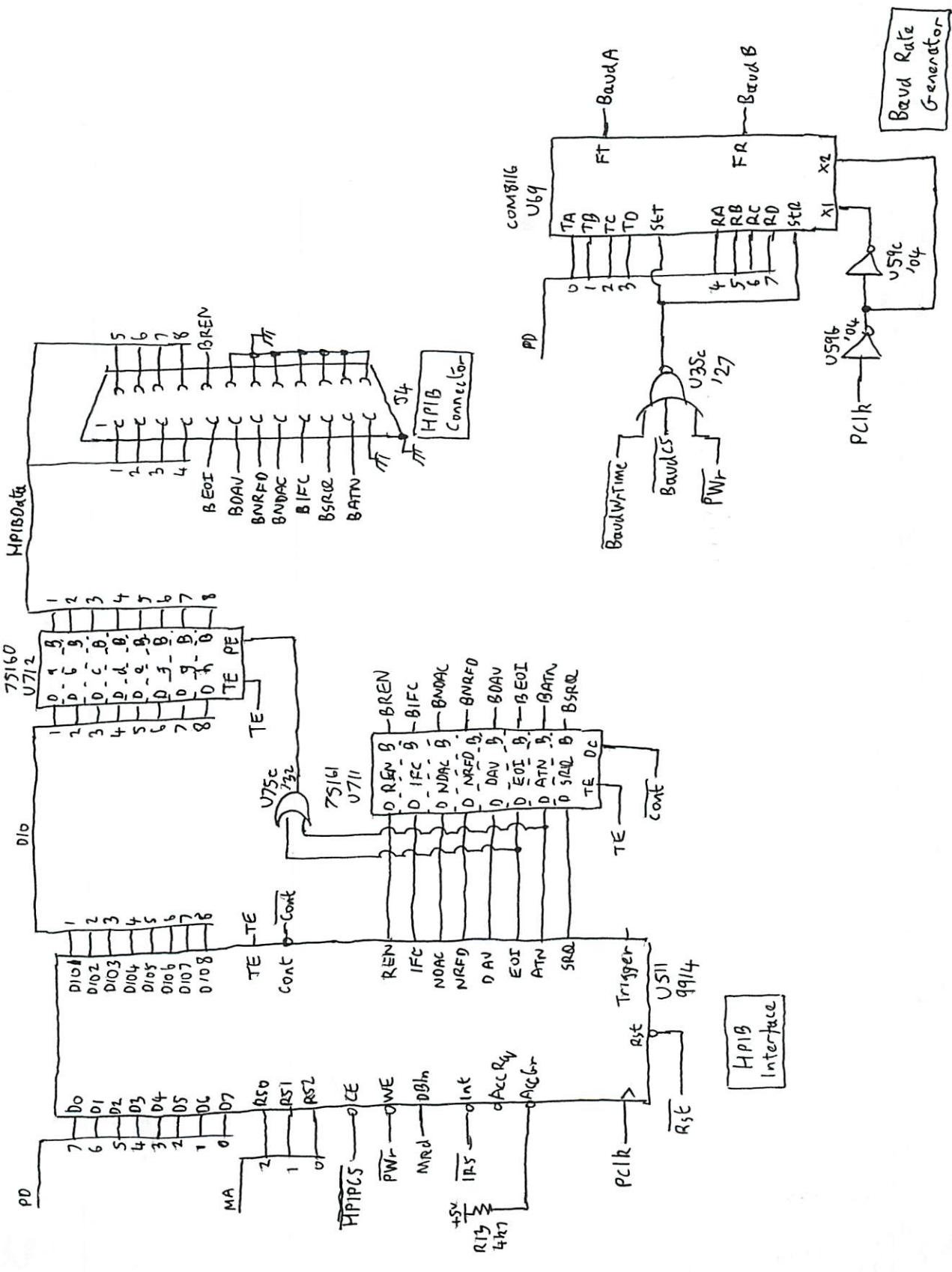


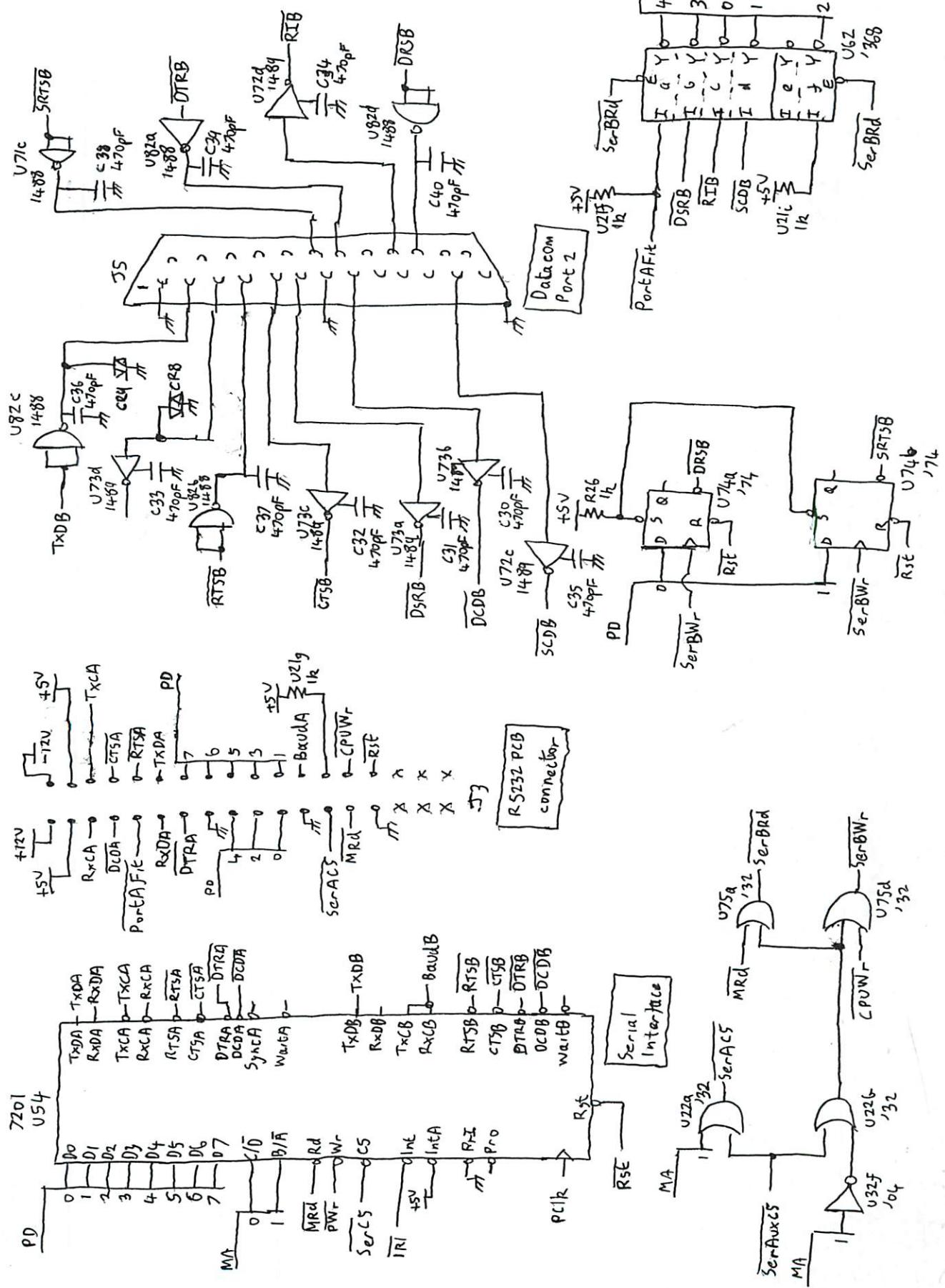


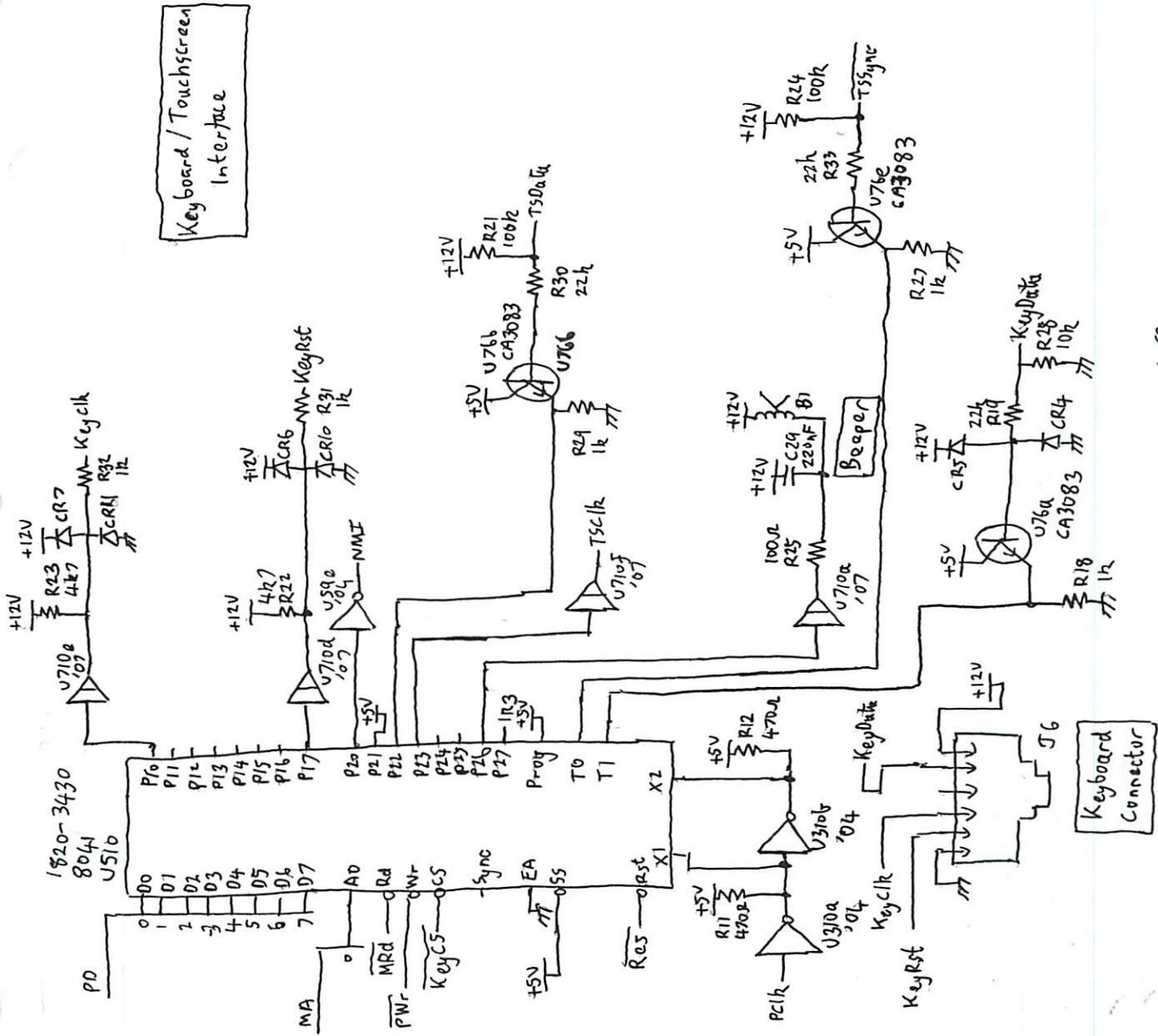
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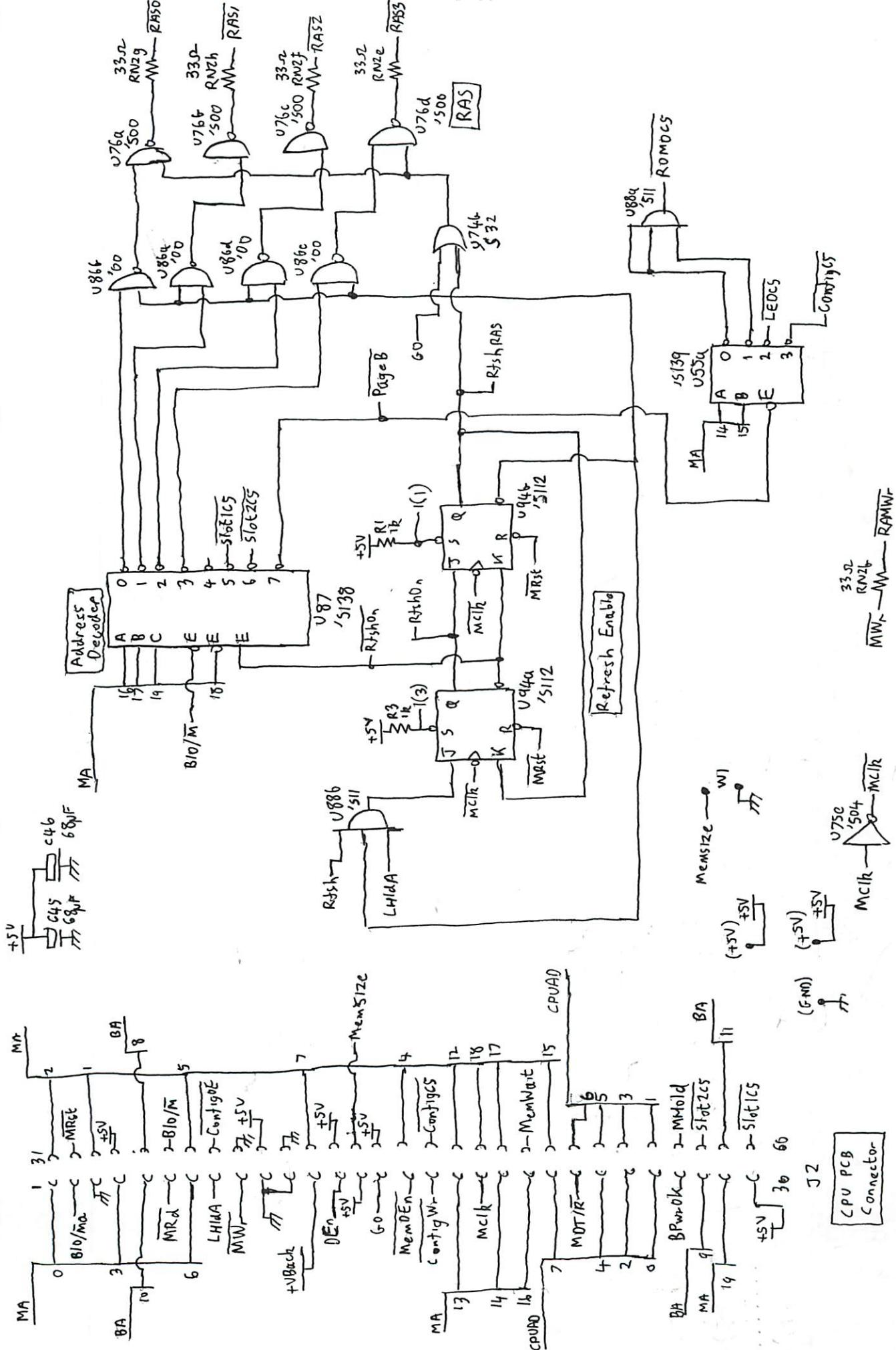
HPI150 CPU PCB Sheet 5

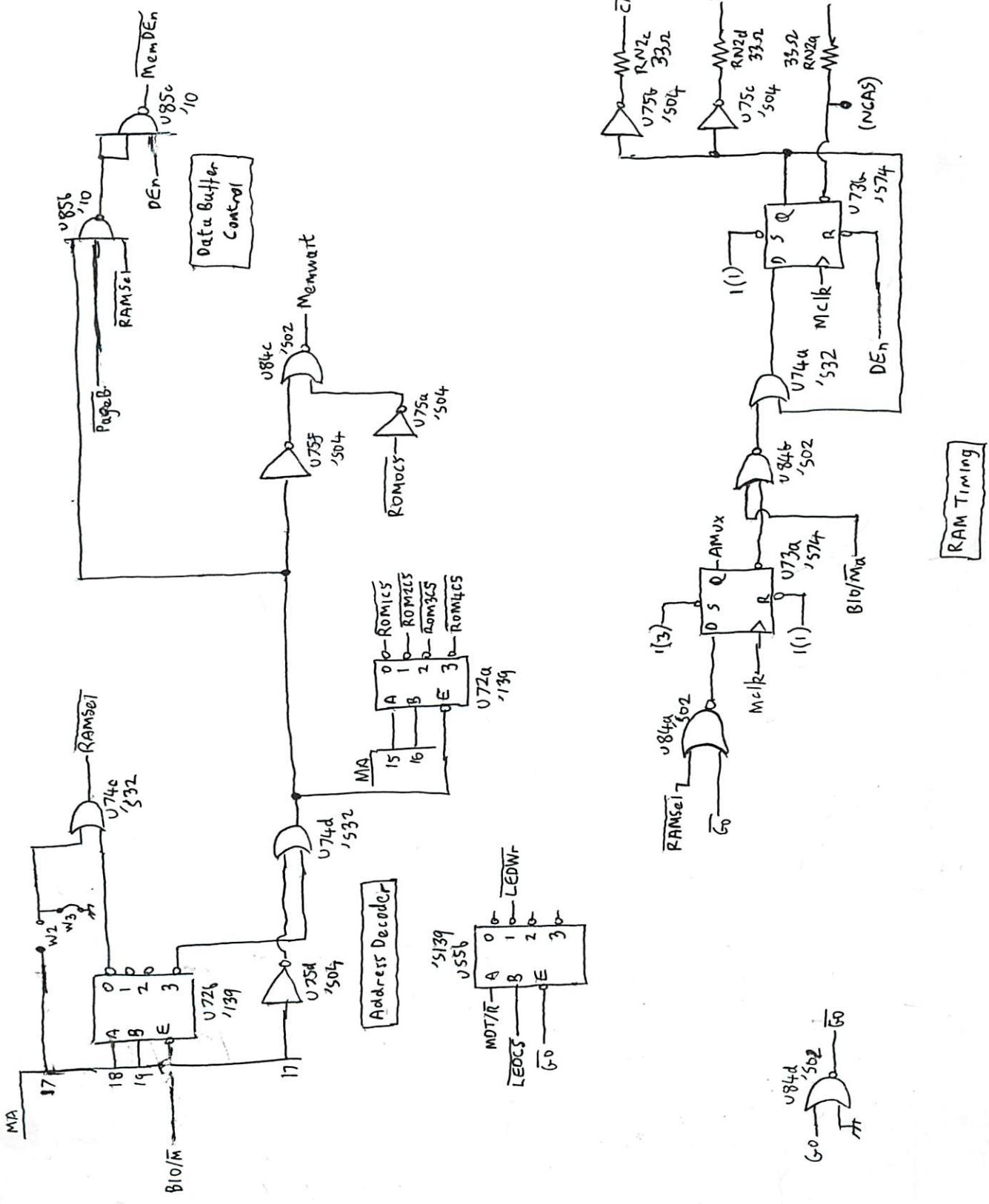
J2

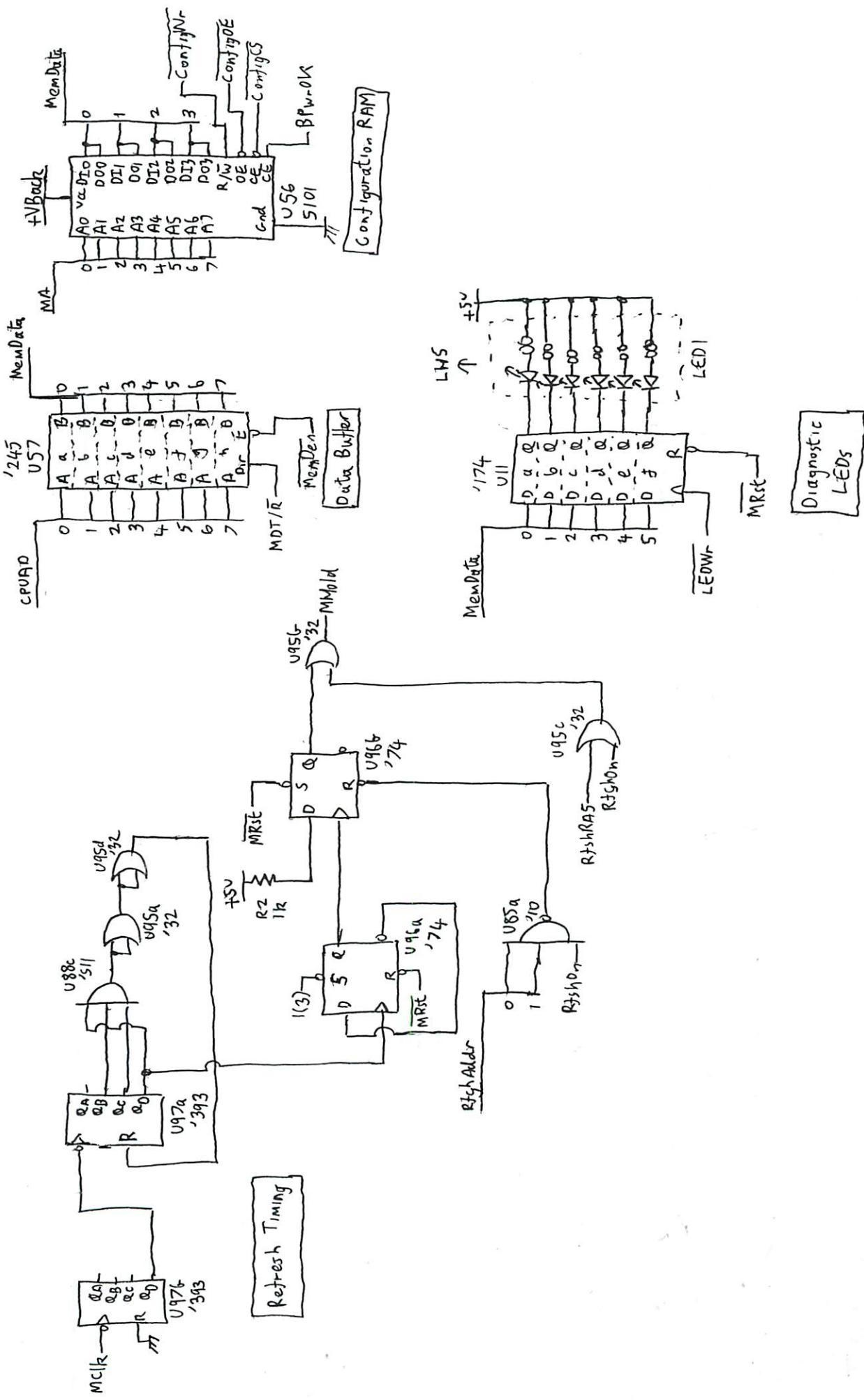


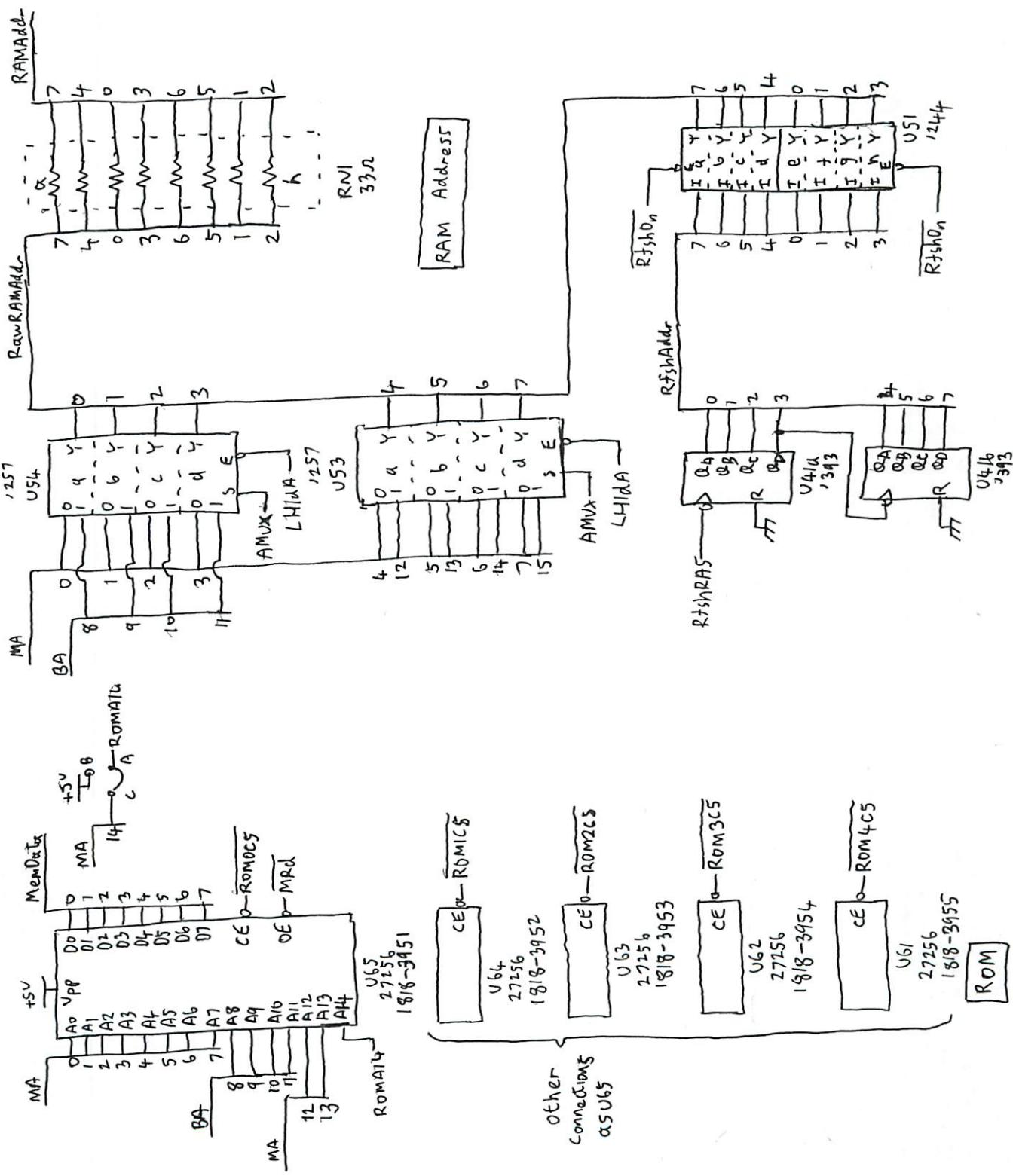


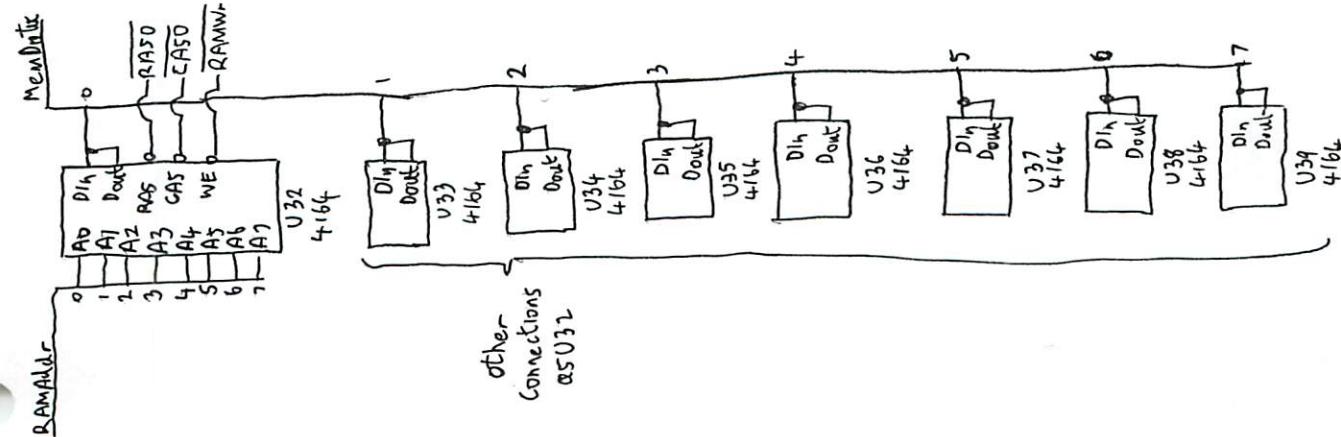
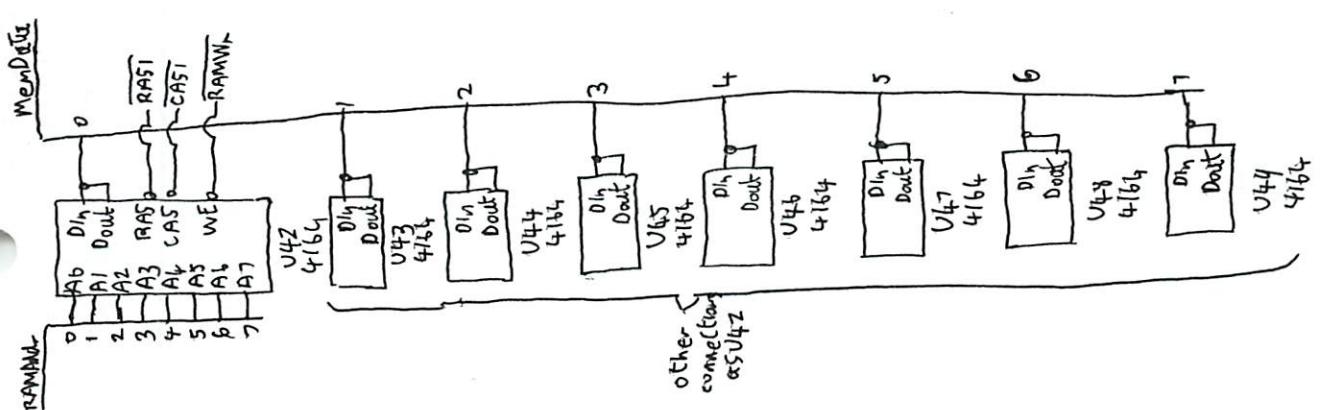
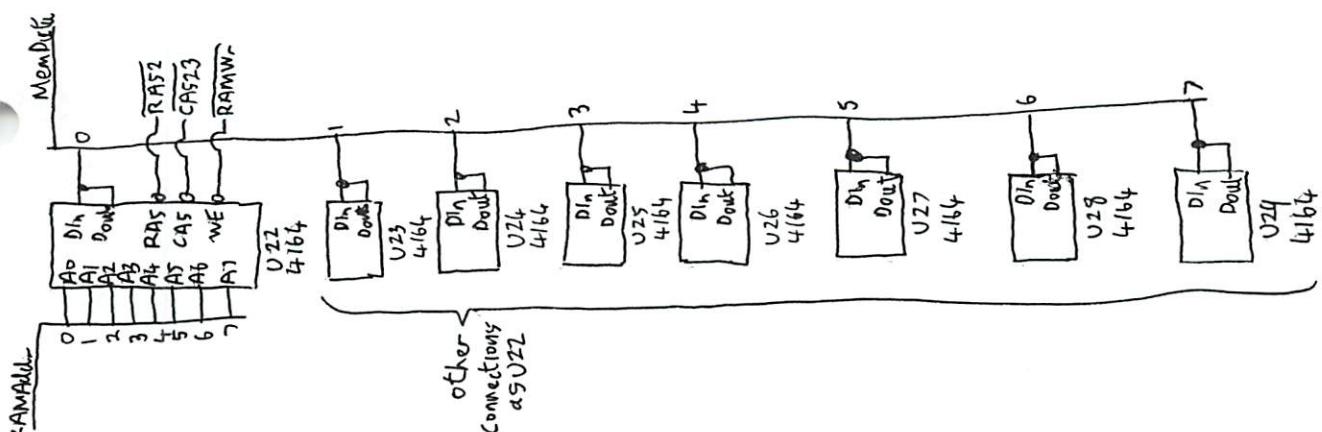
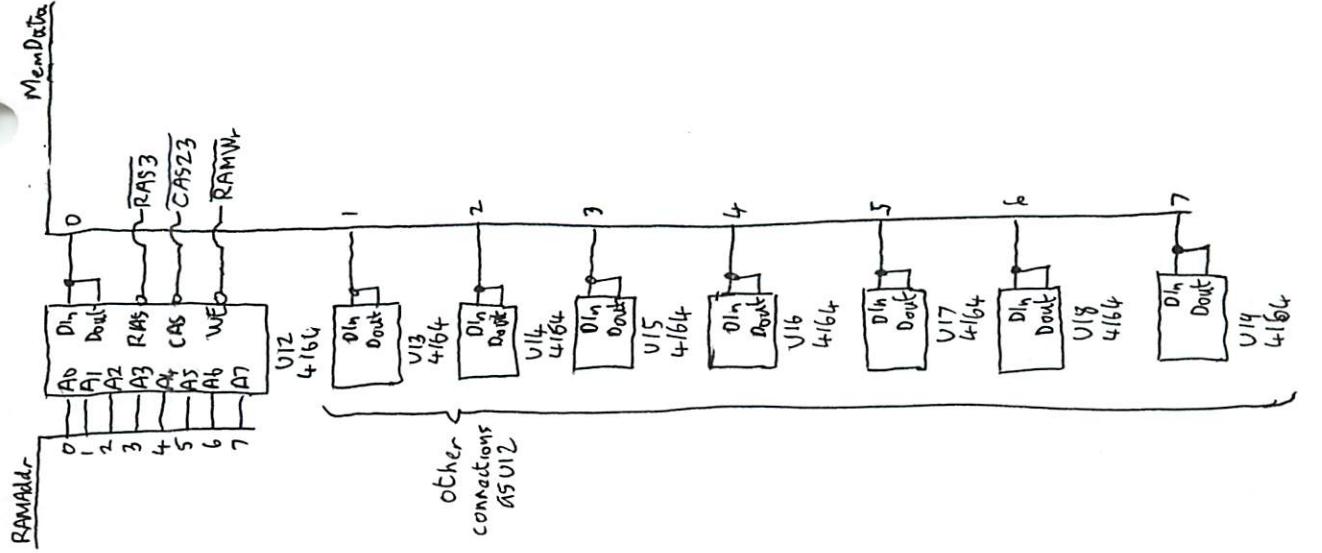


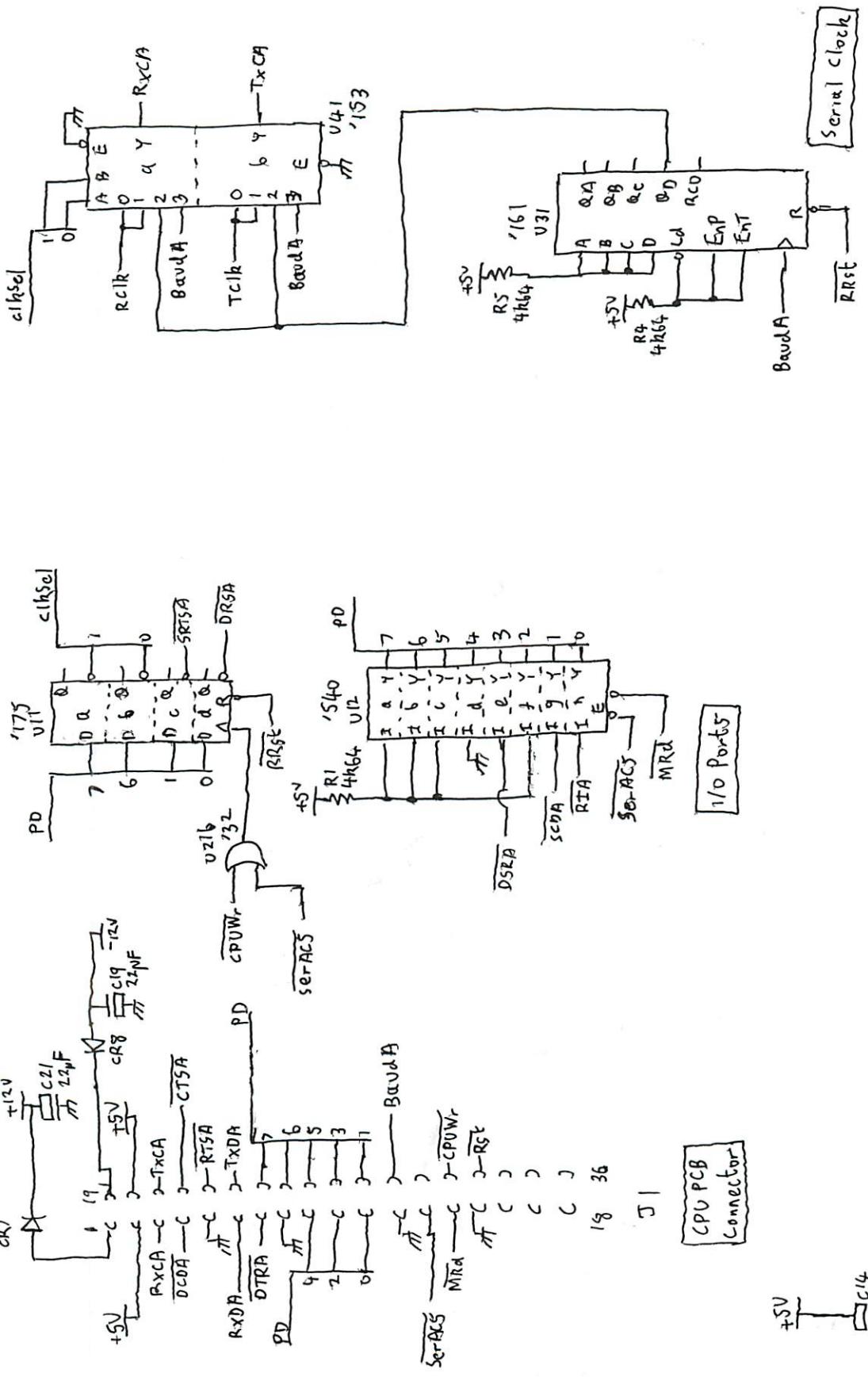


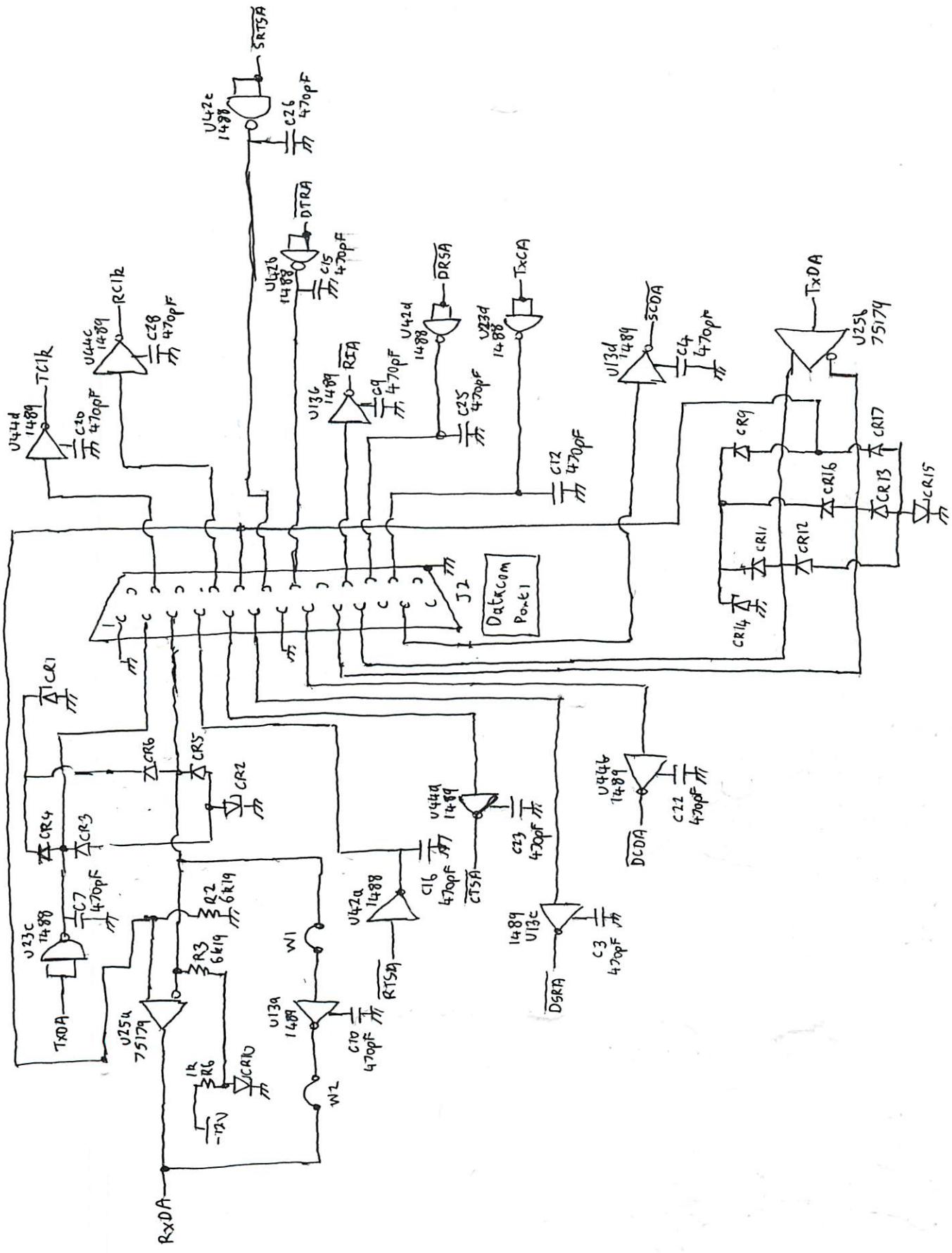


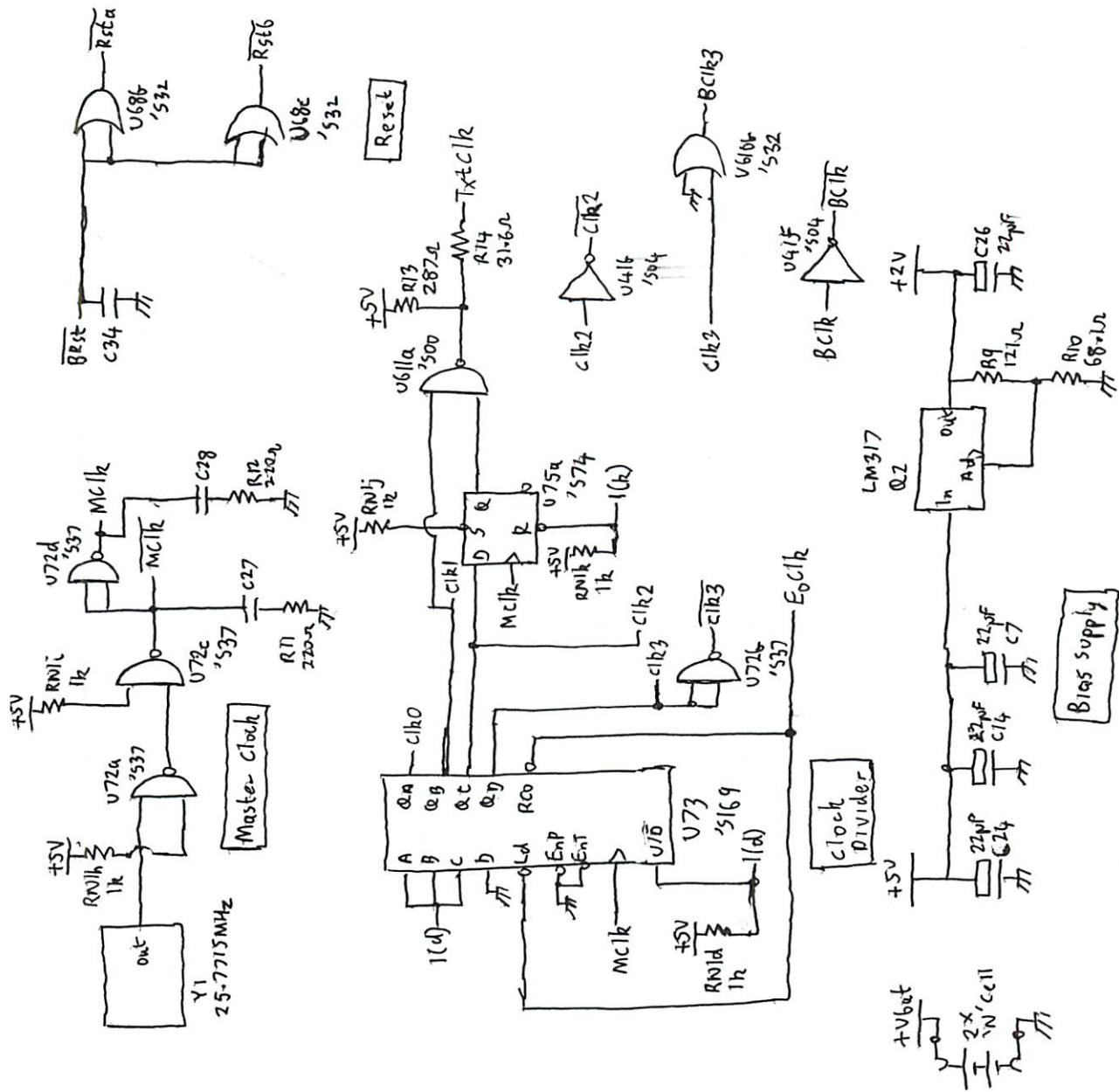
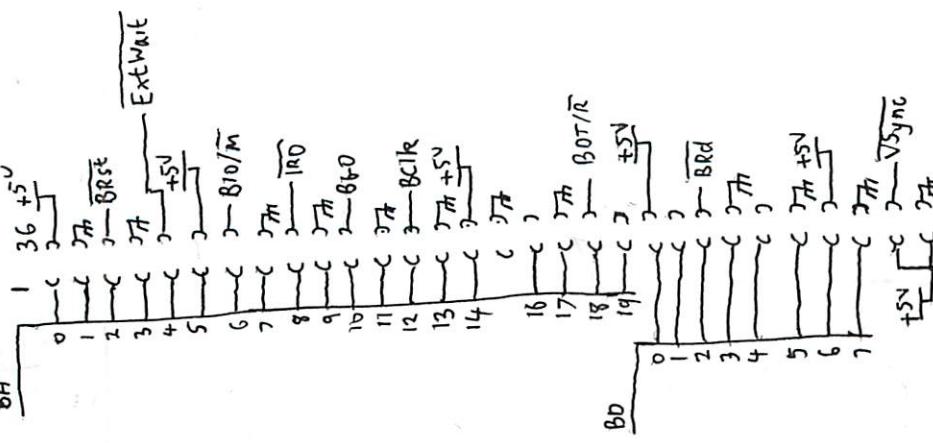
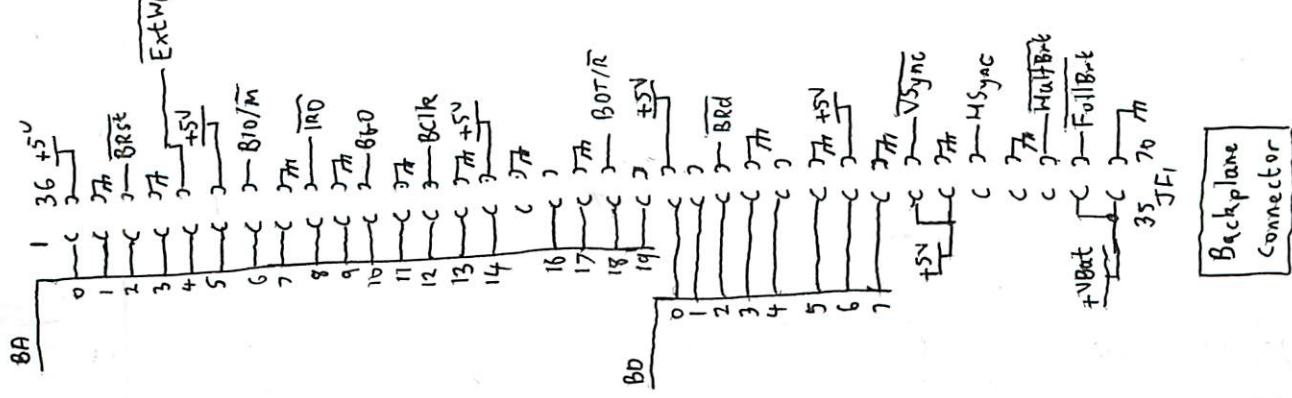


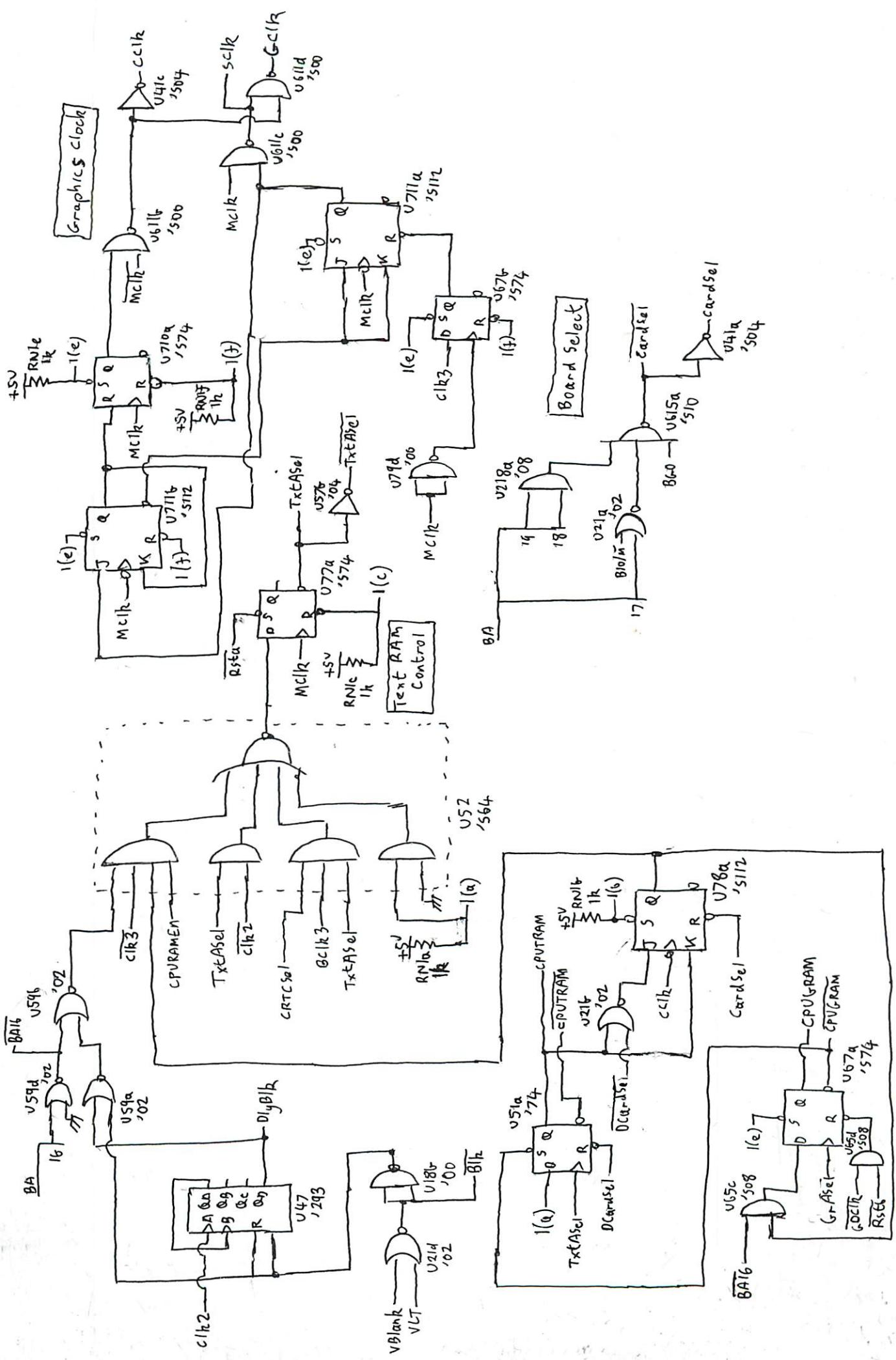


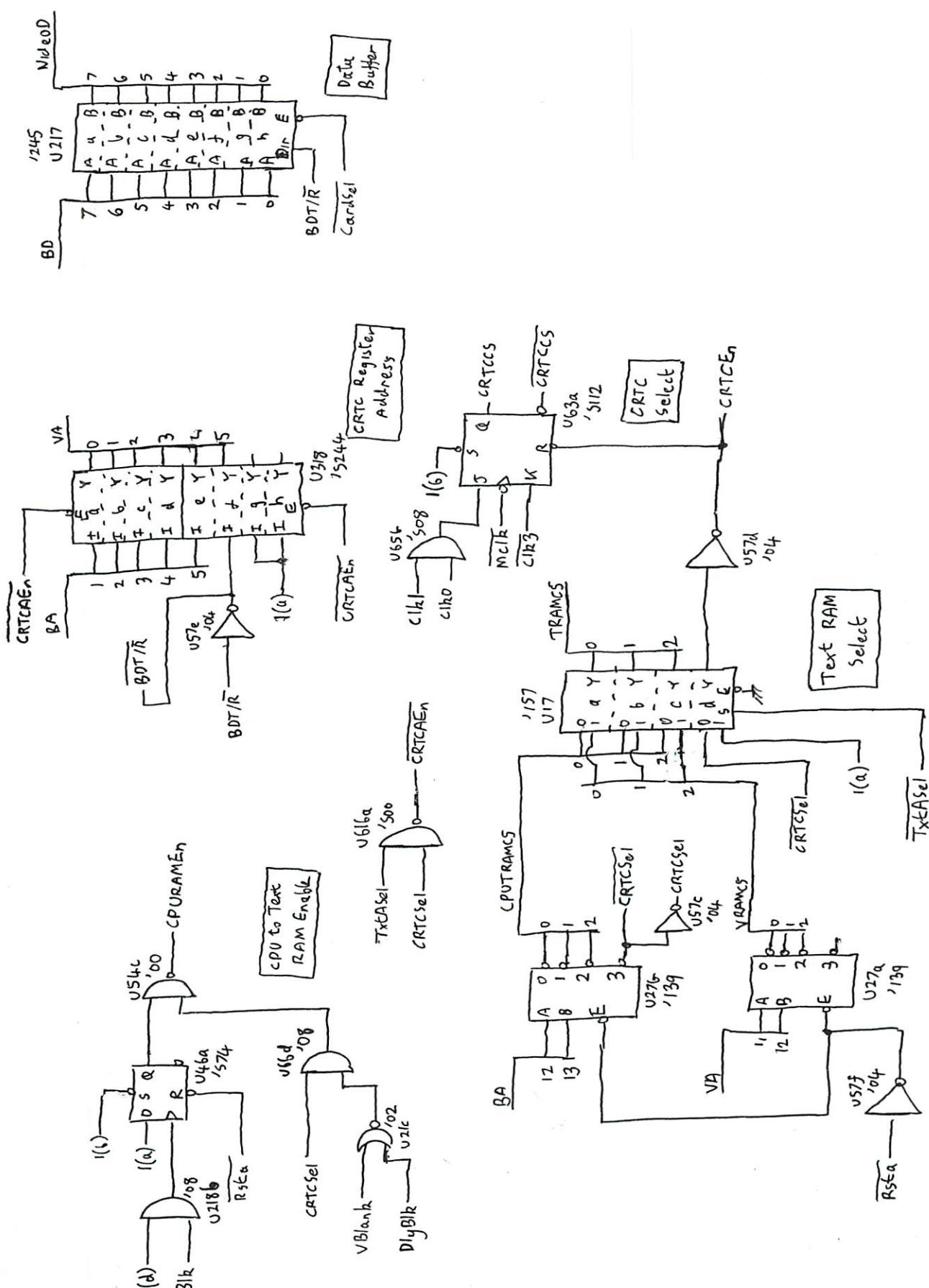


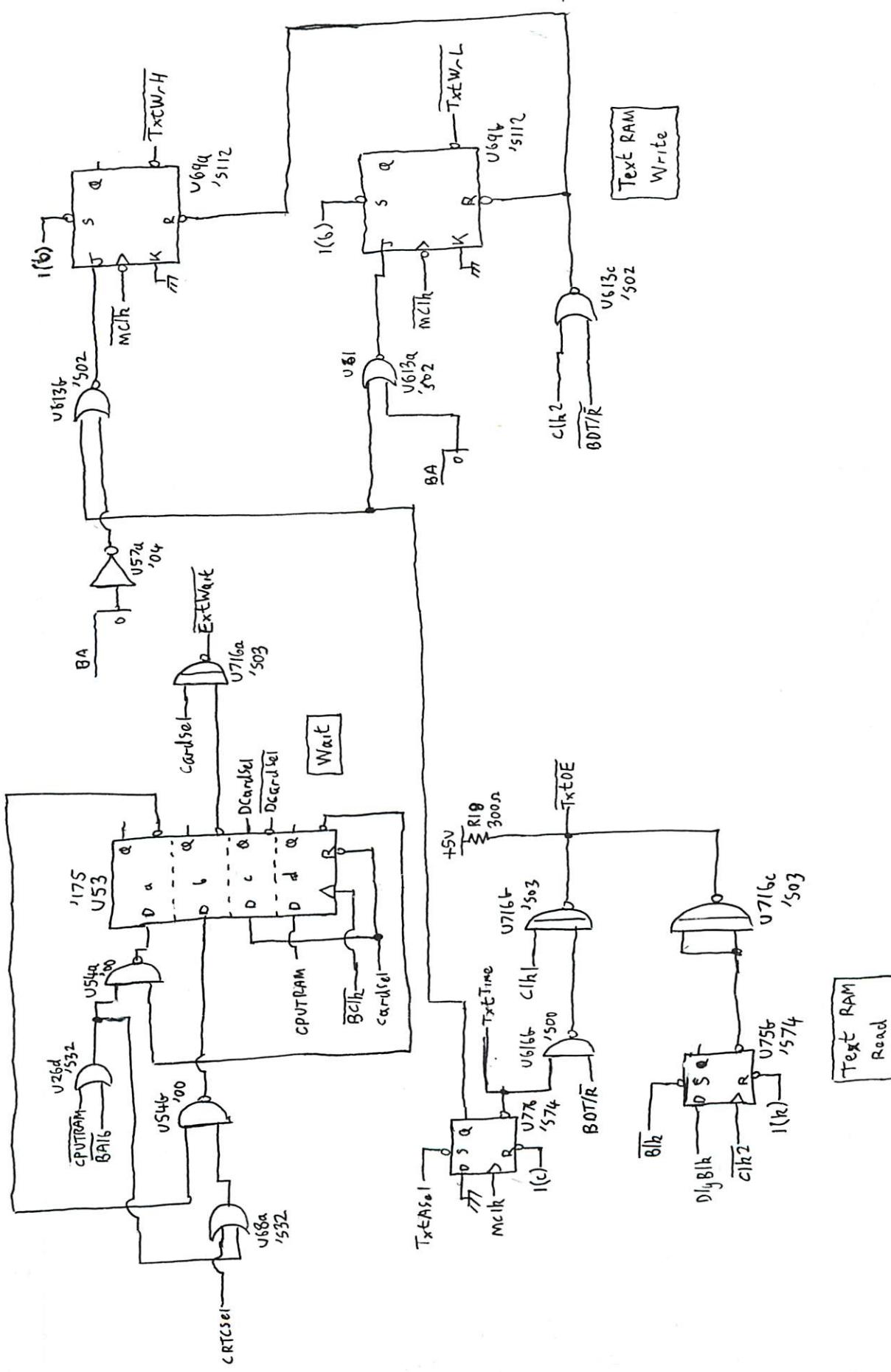


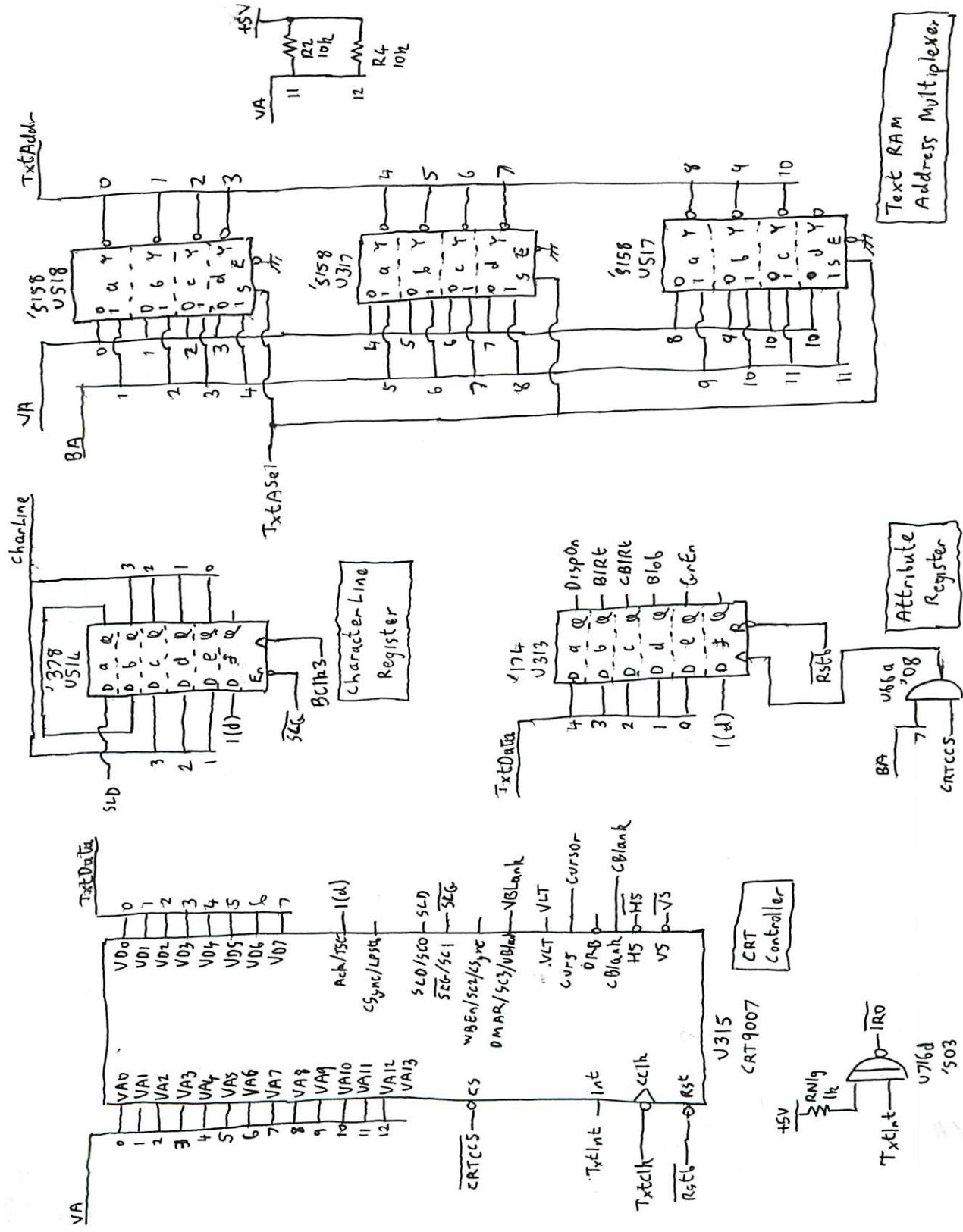


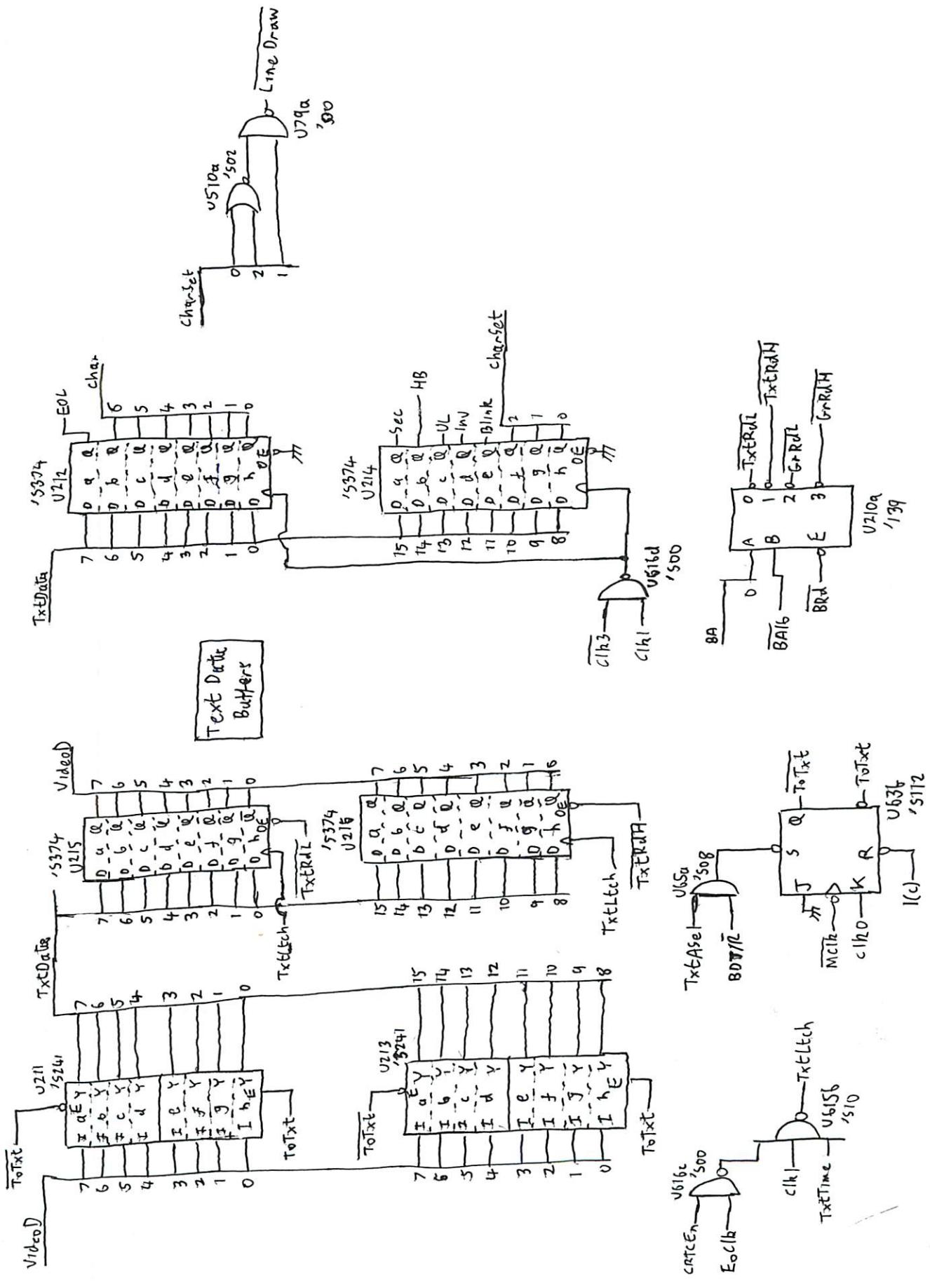


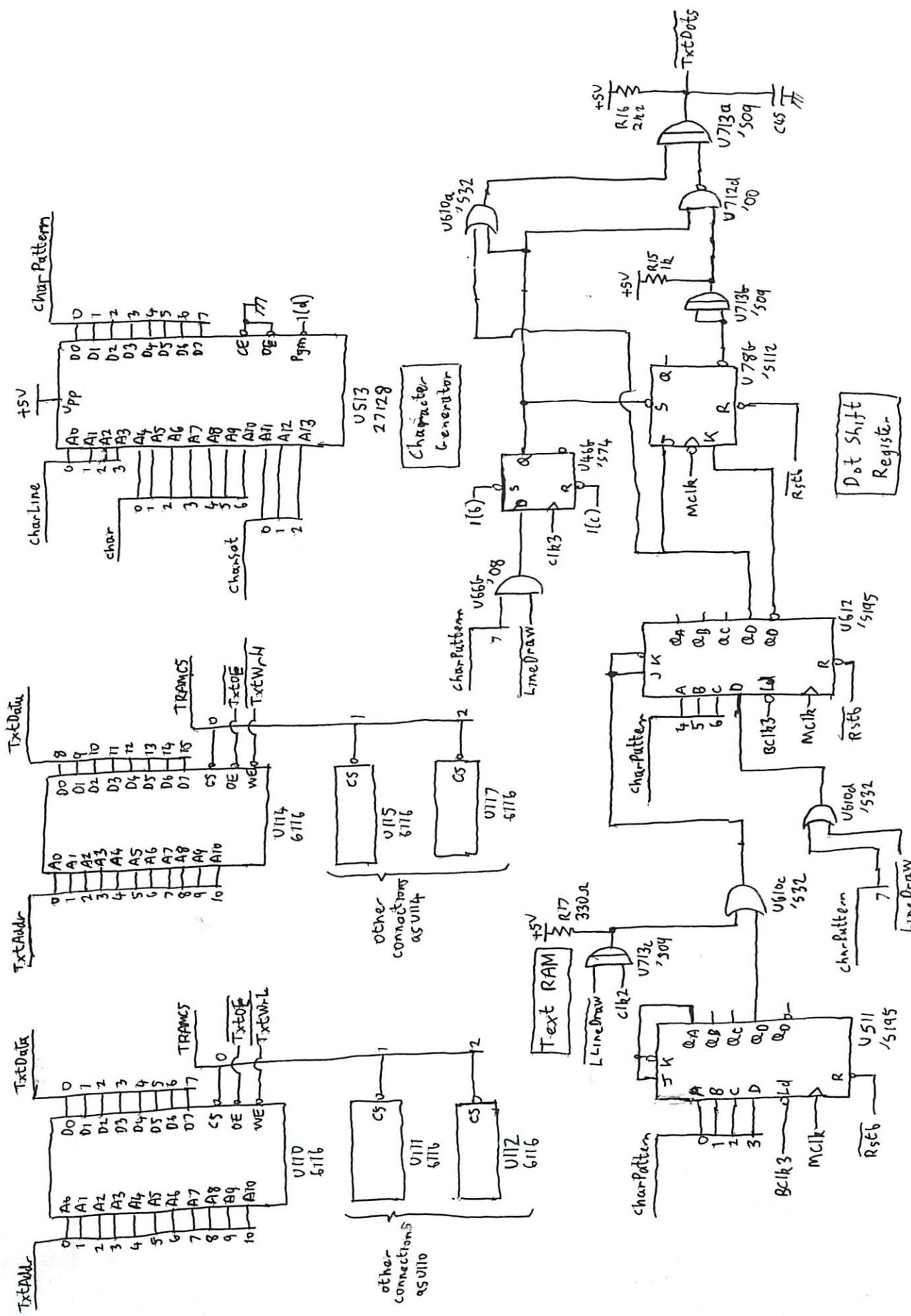


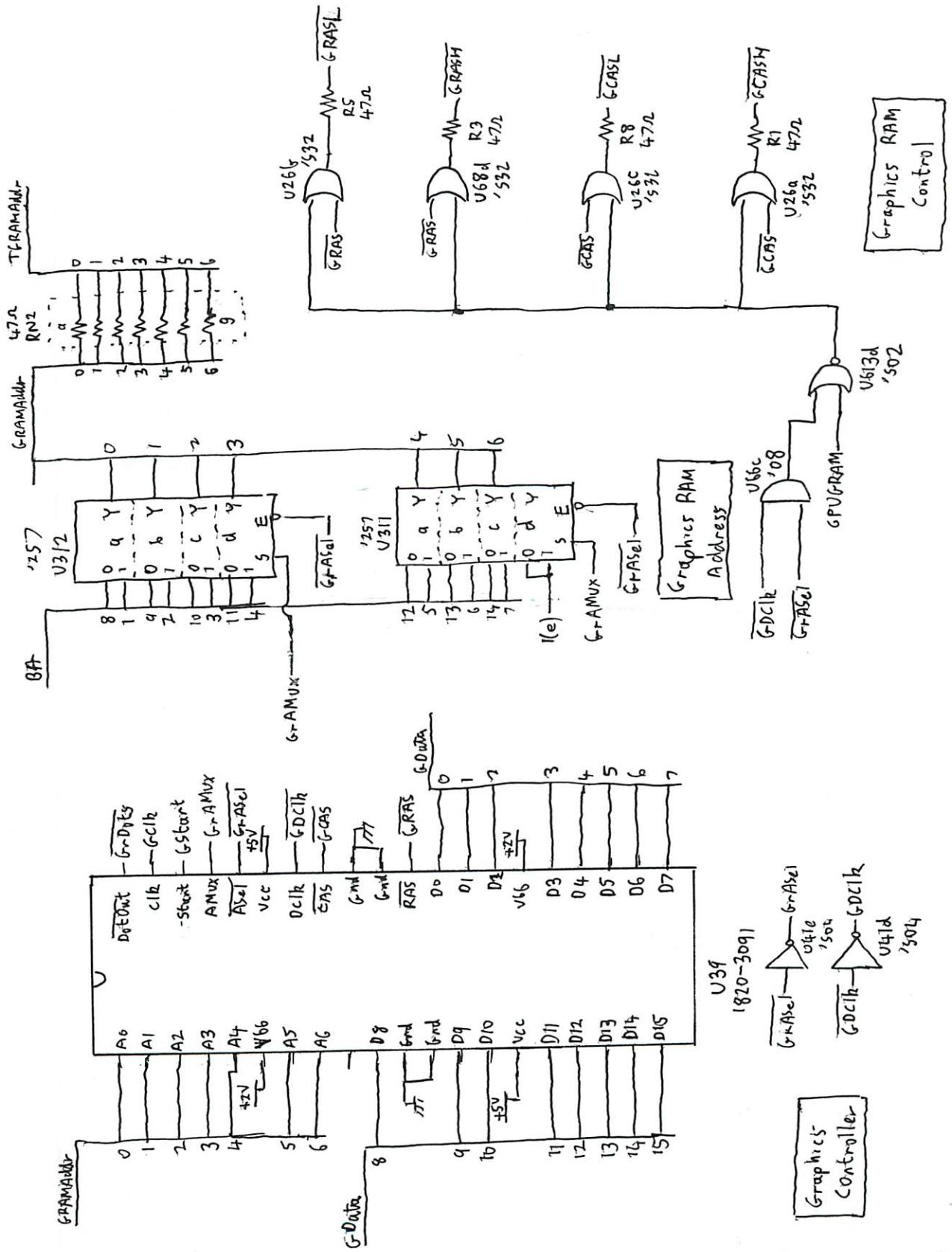


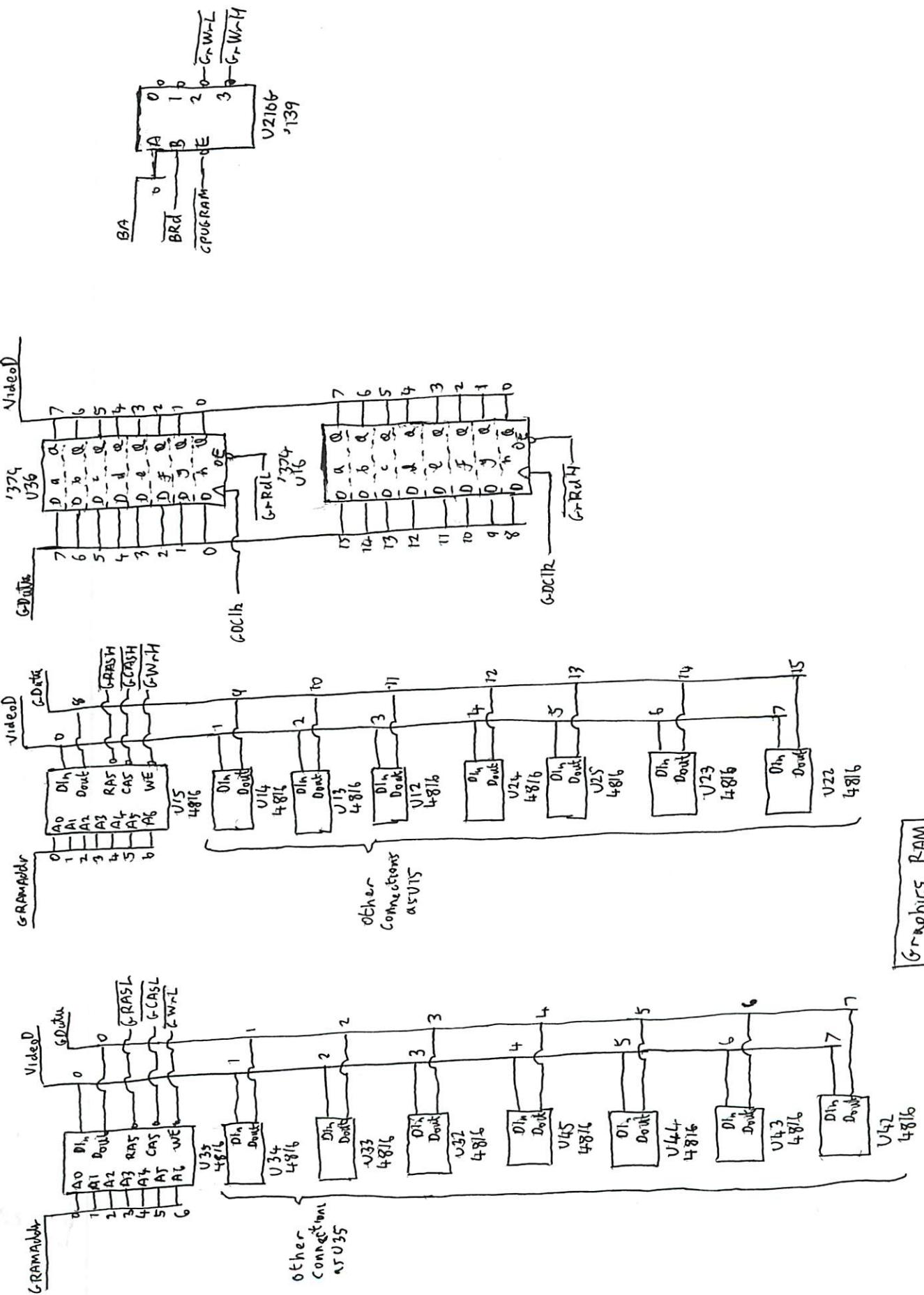


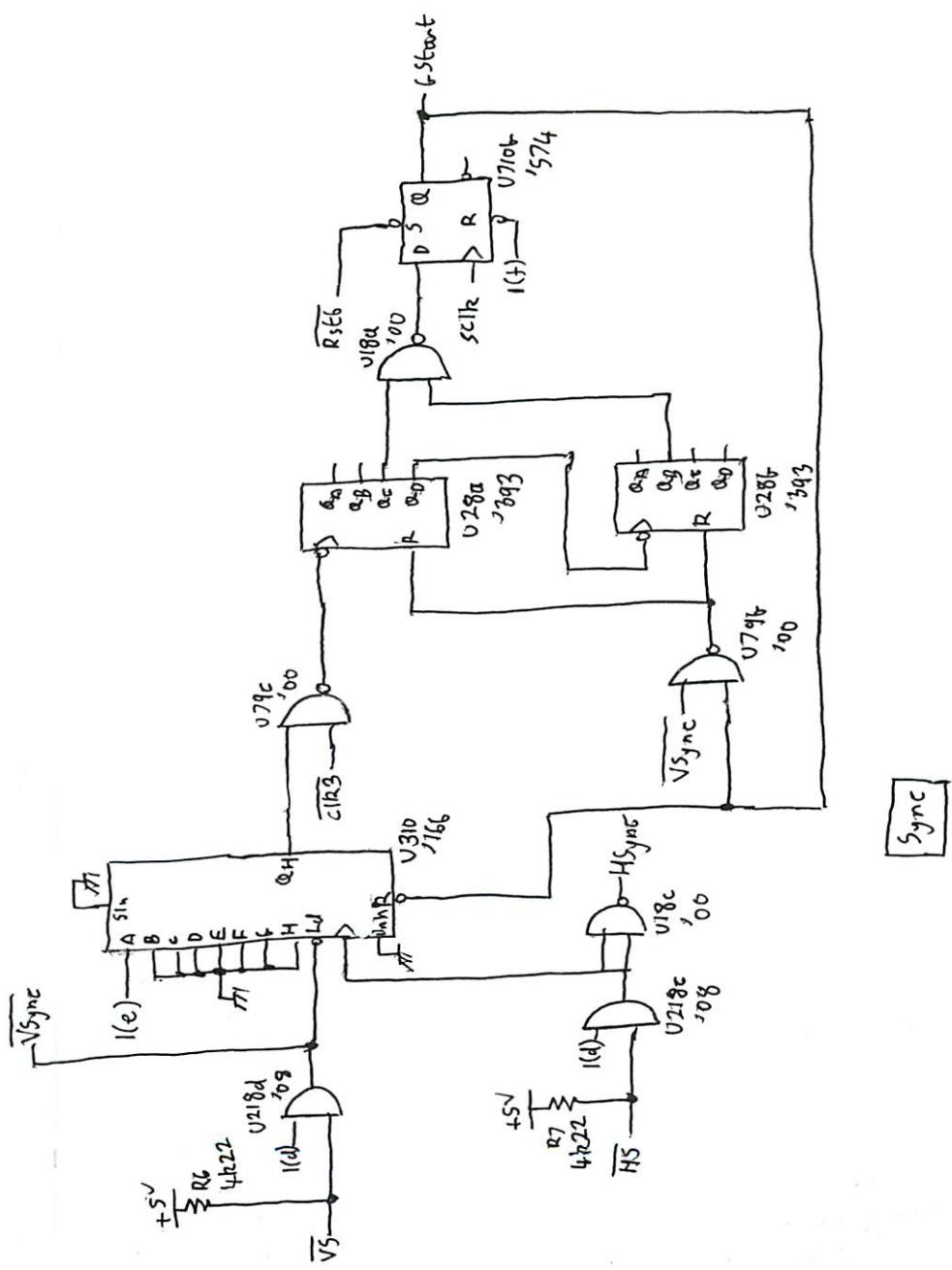


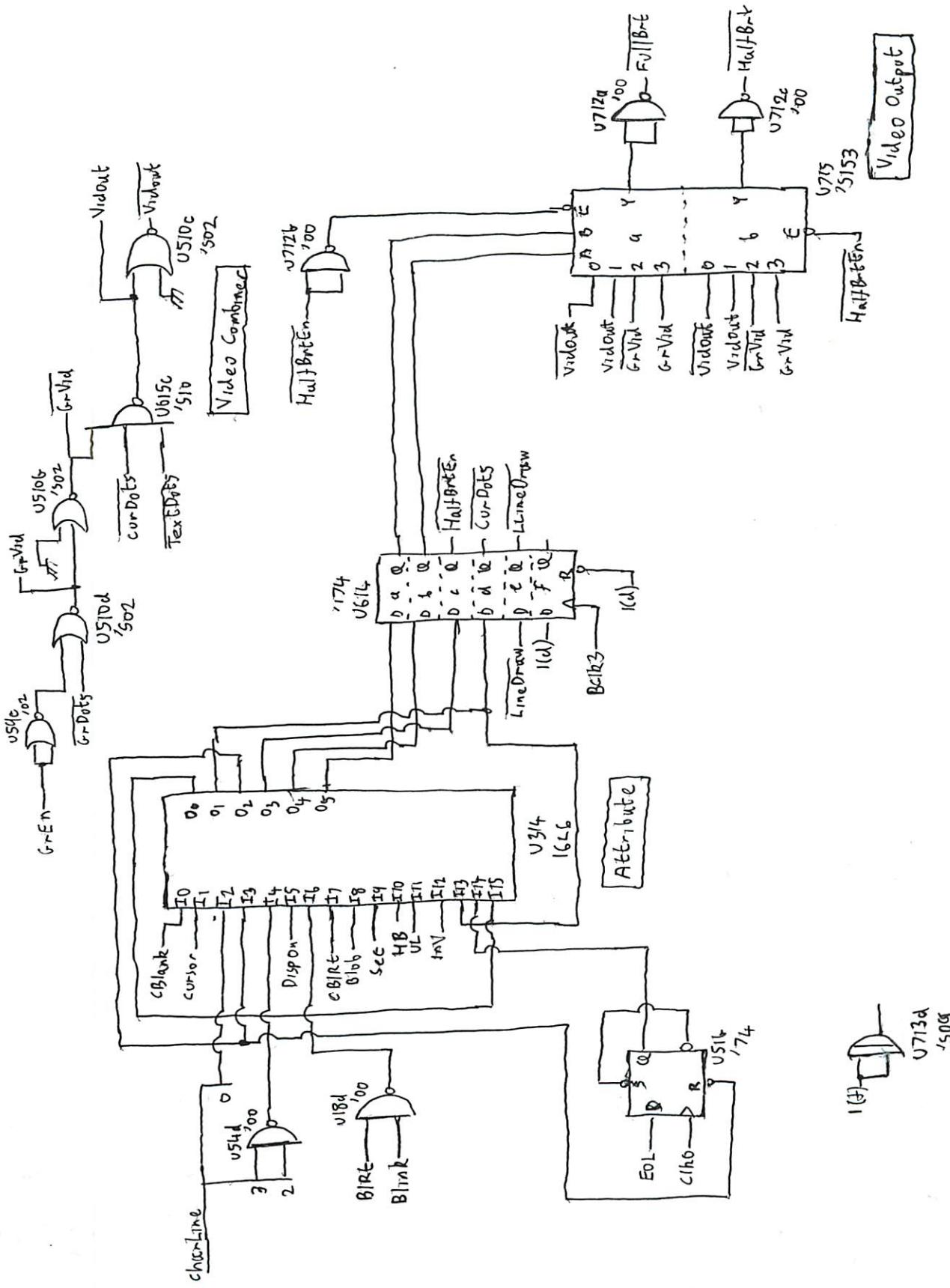


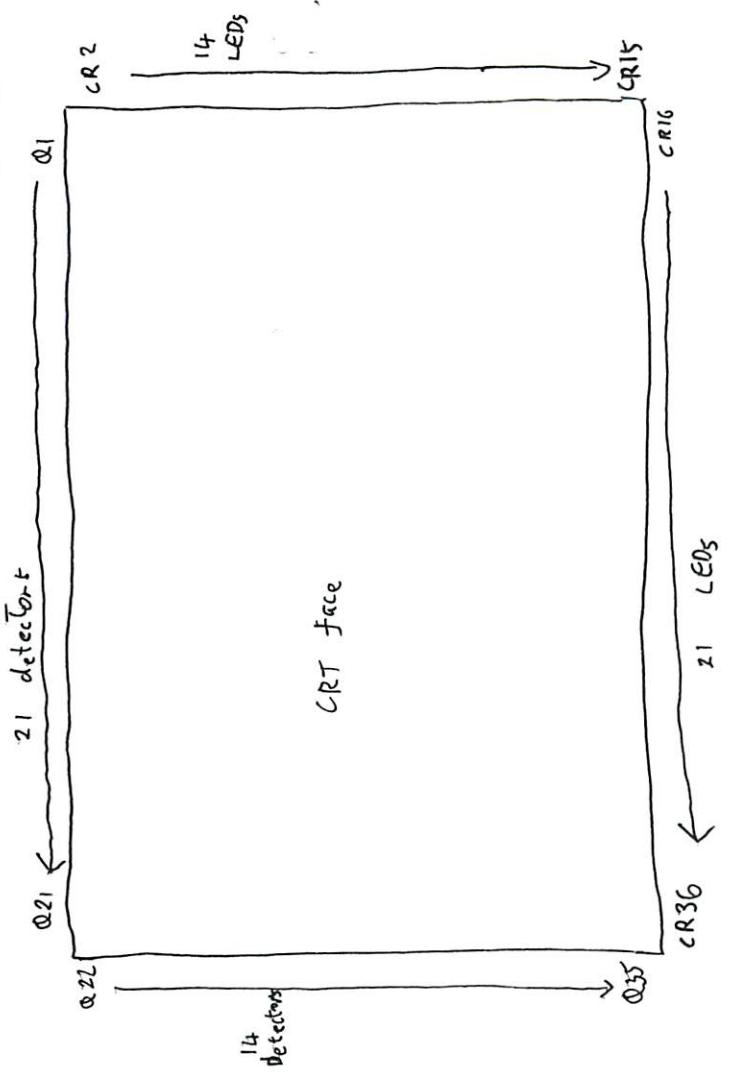




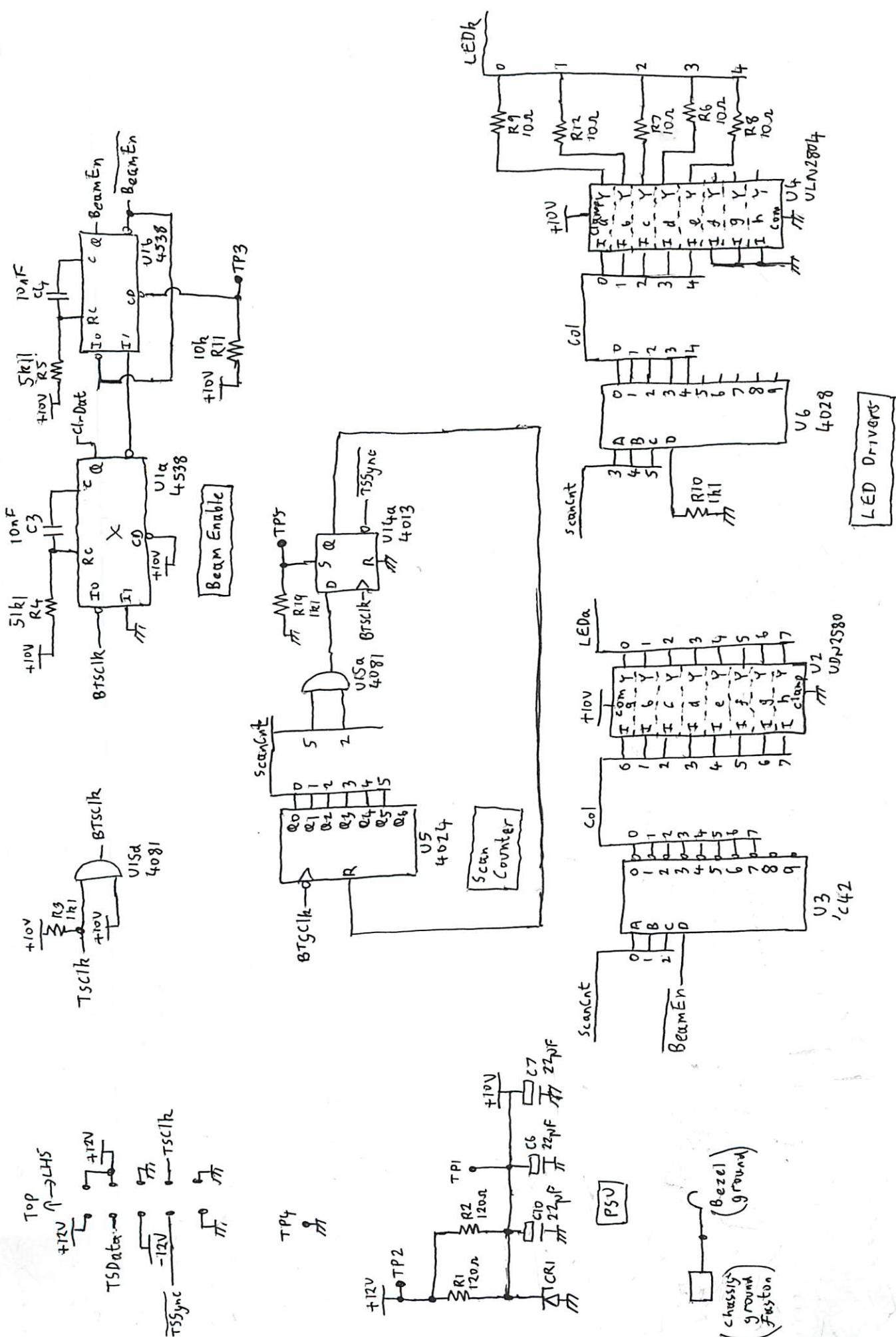


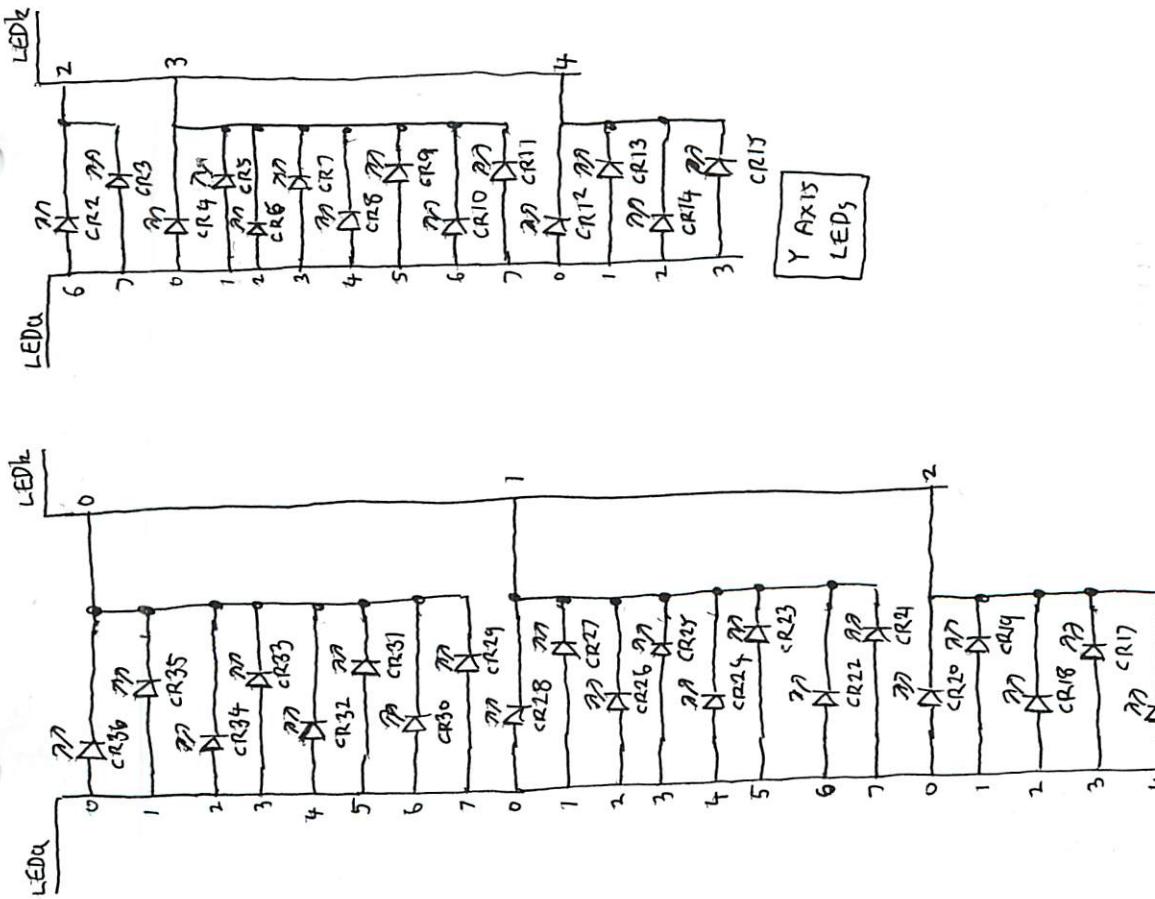
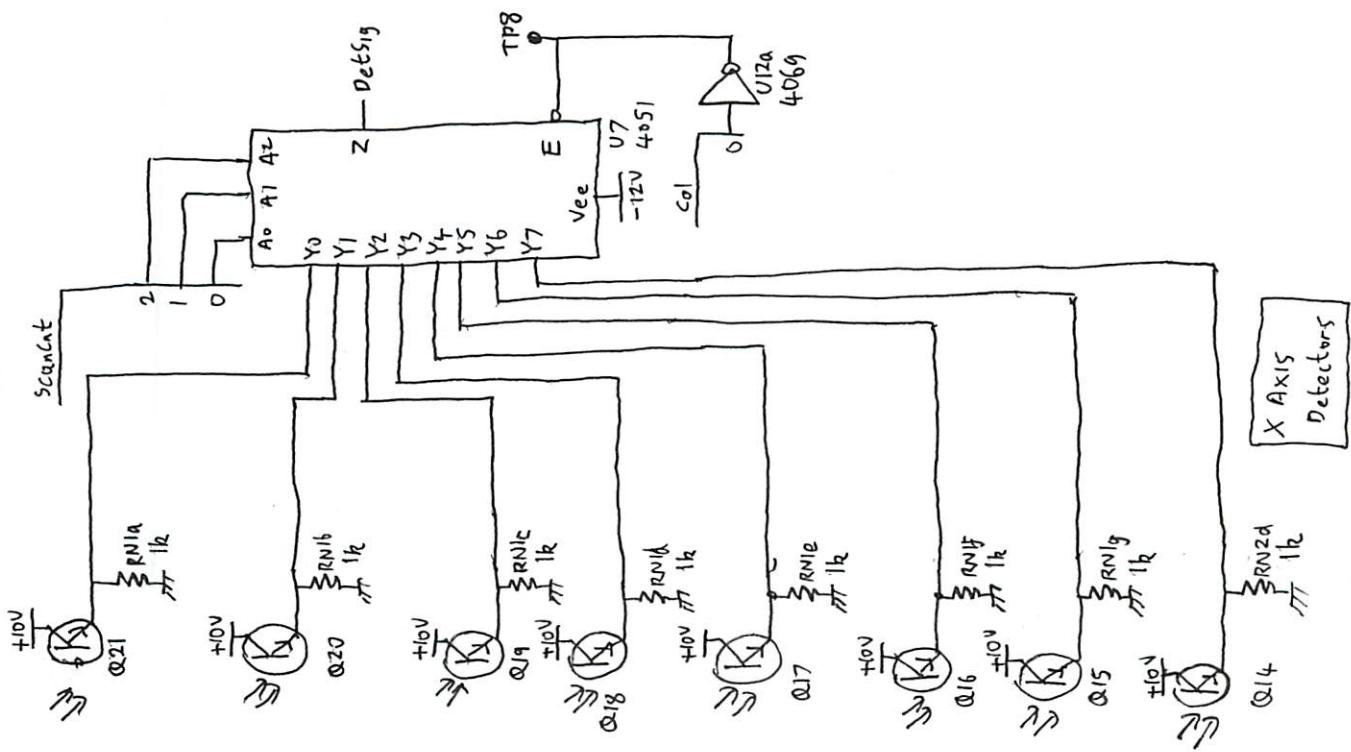






HP150 Touchscreen Layout (front view)



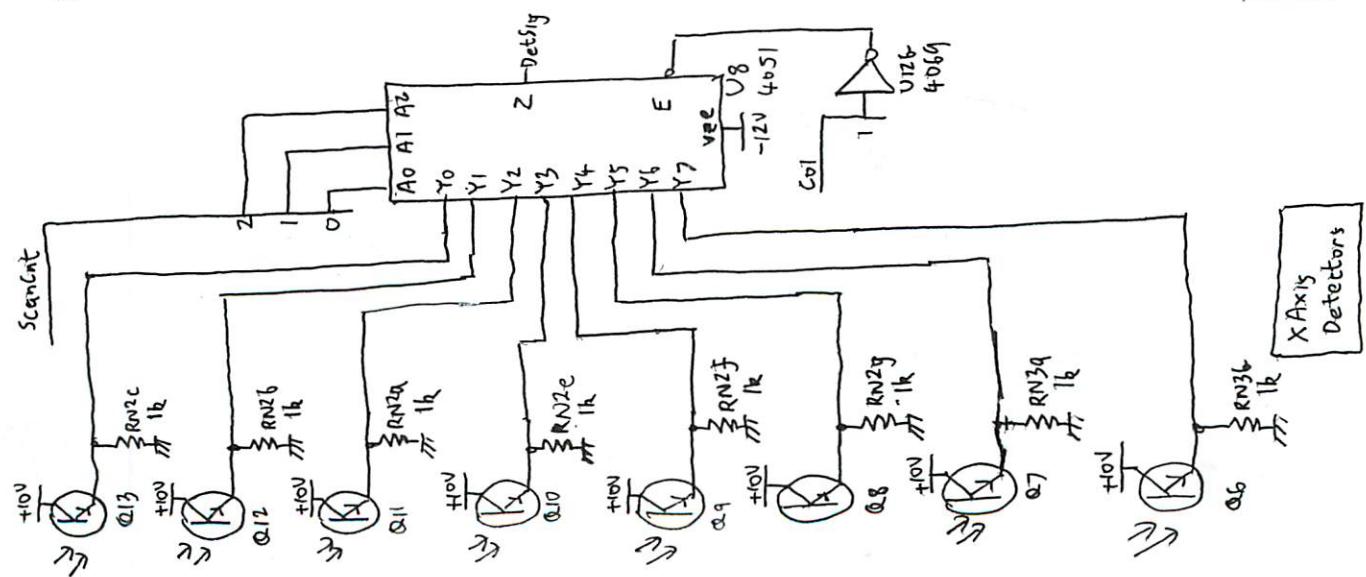
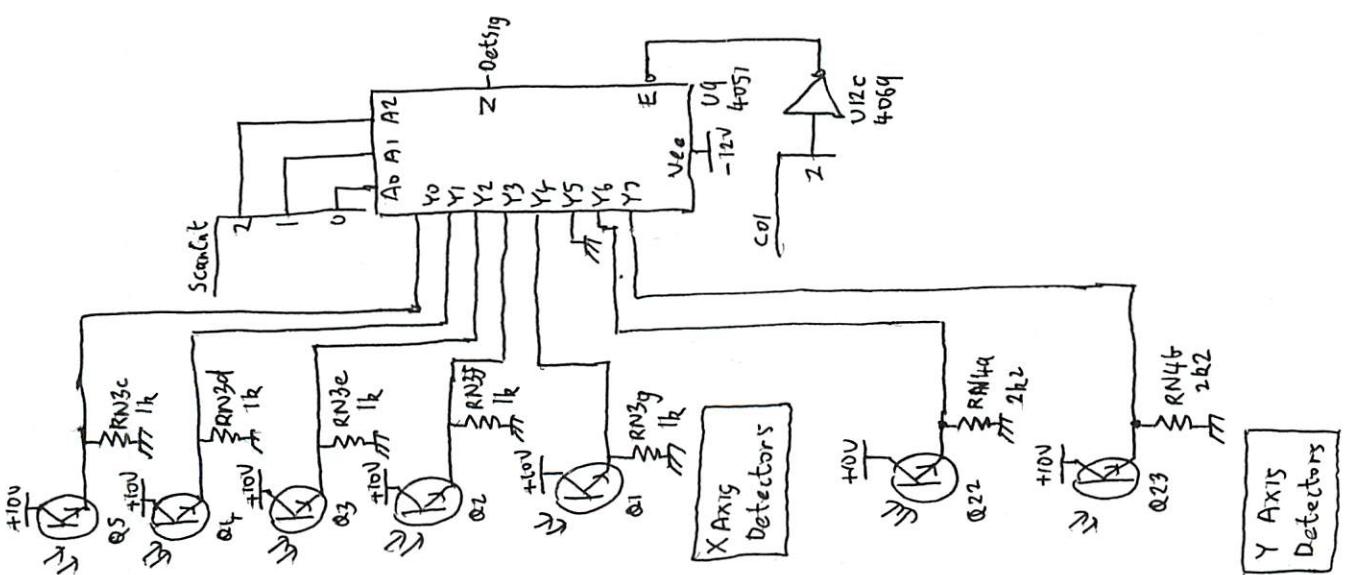
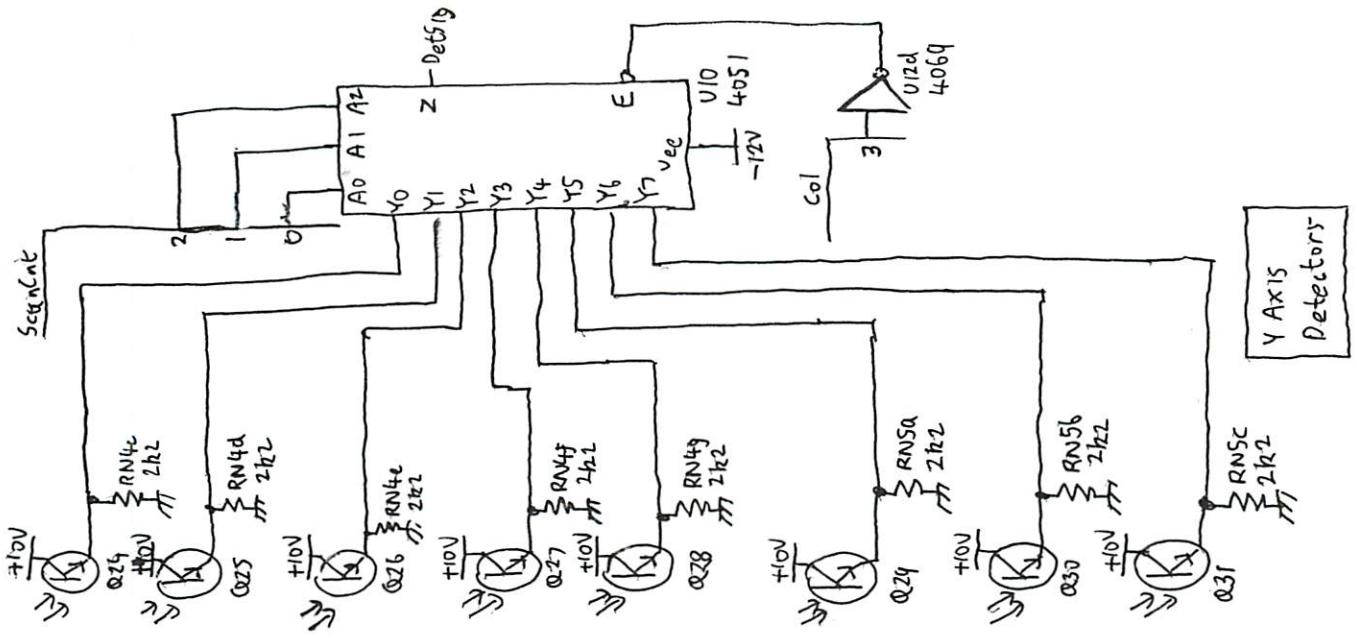


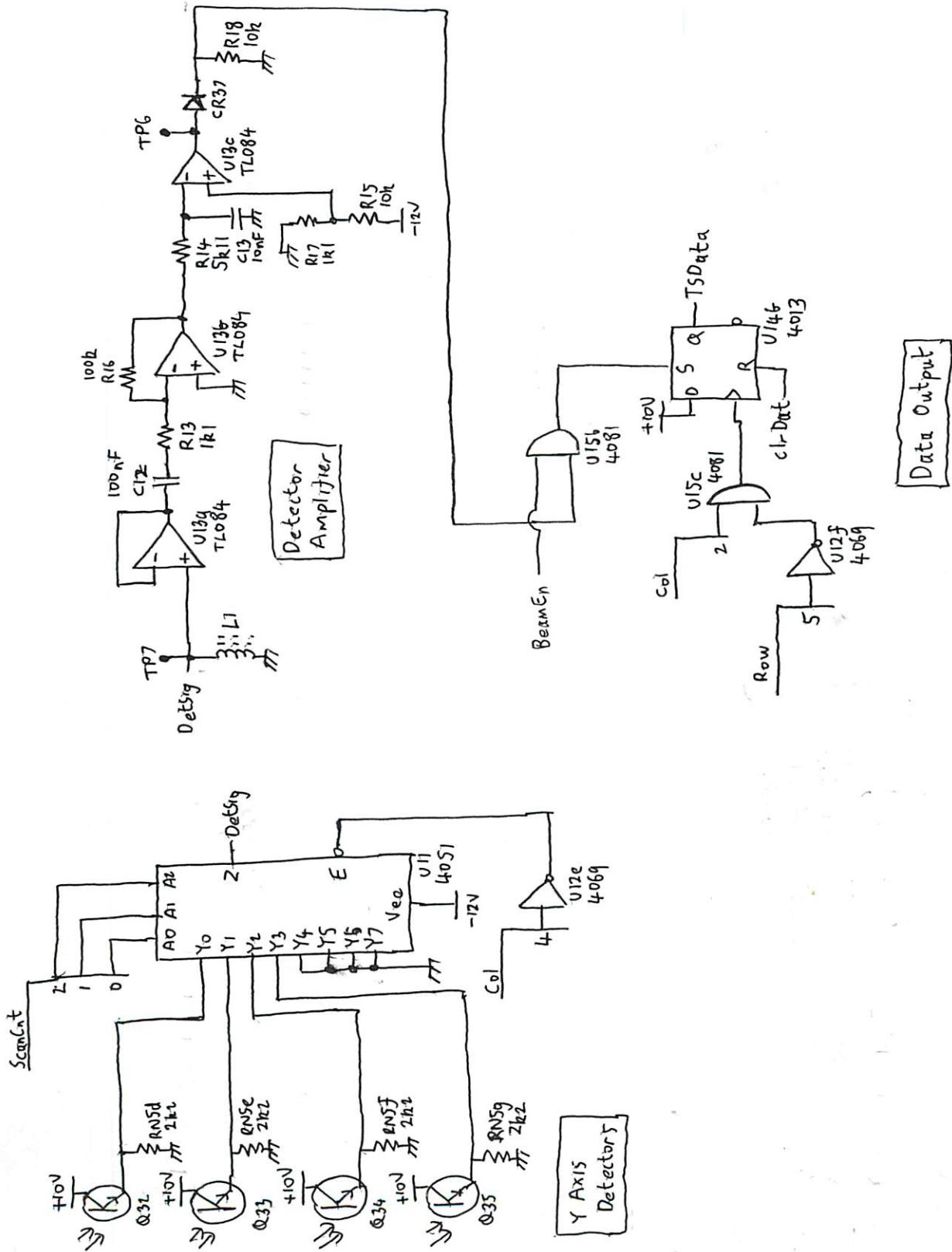
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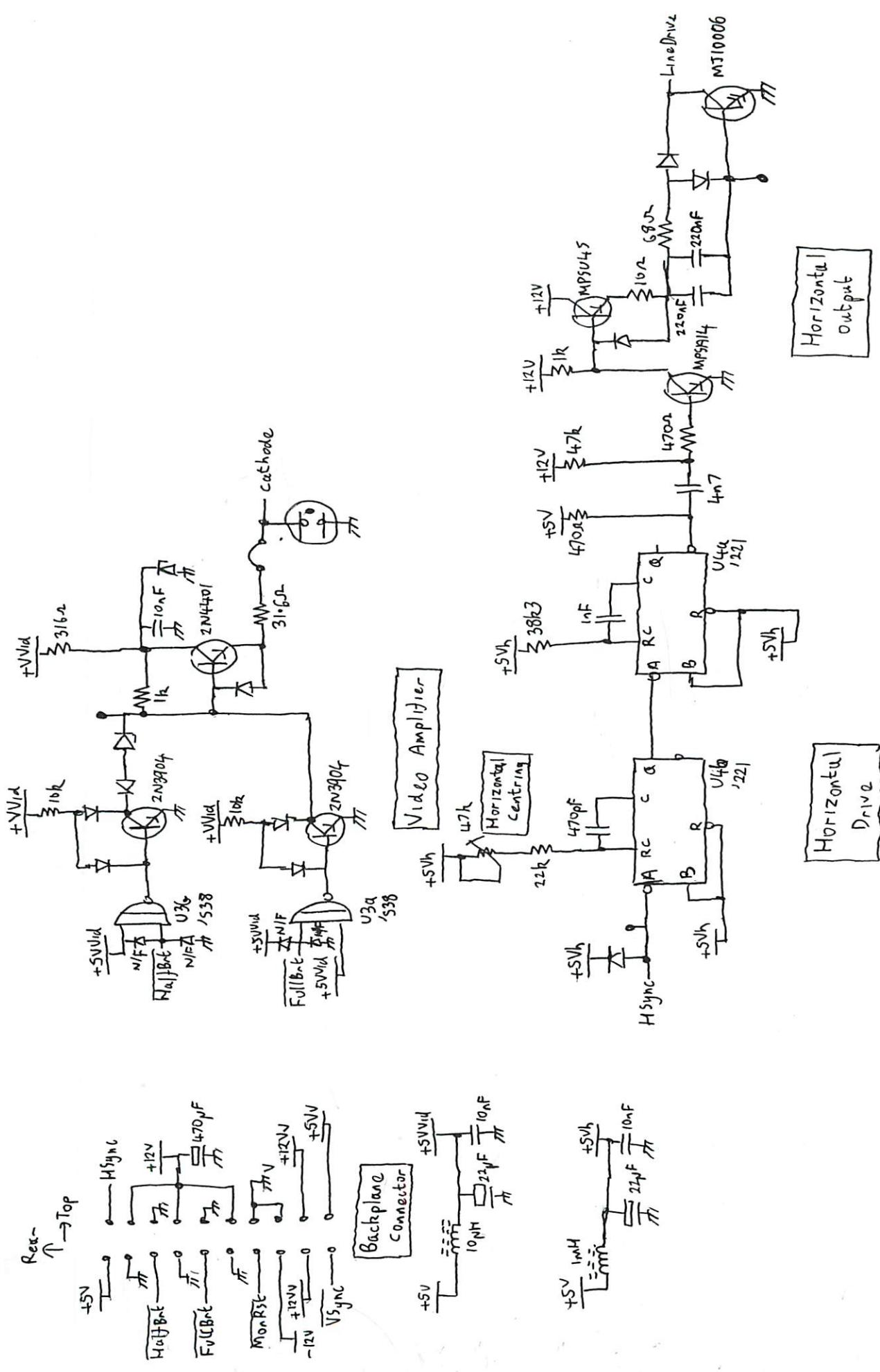
Touch screen PCB sheet ②

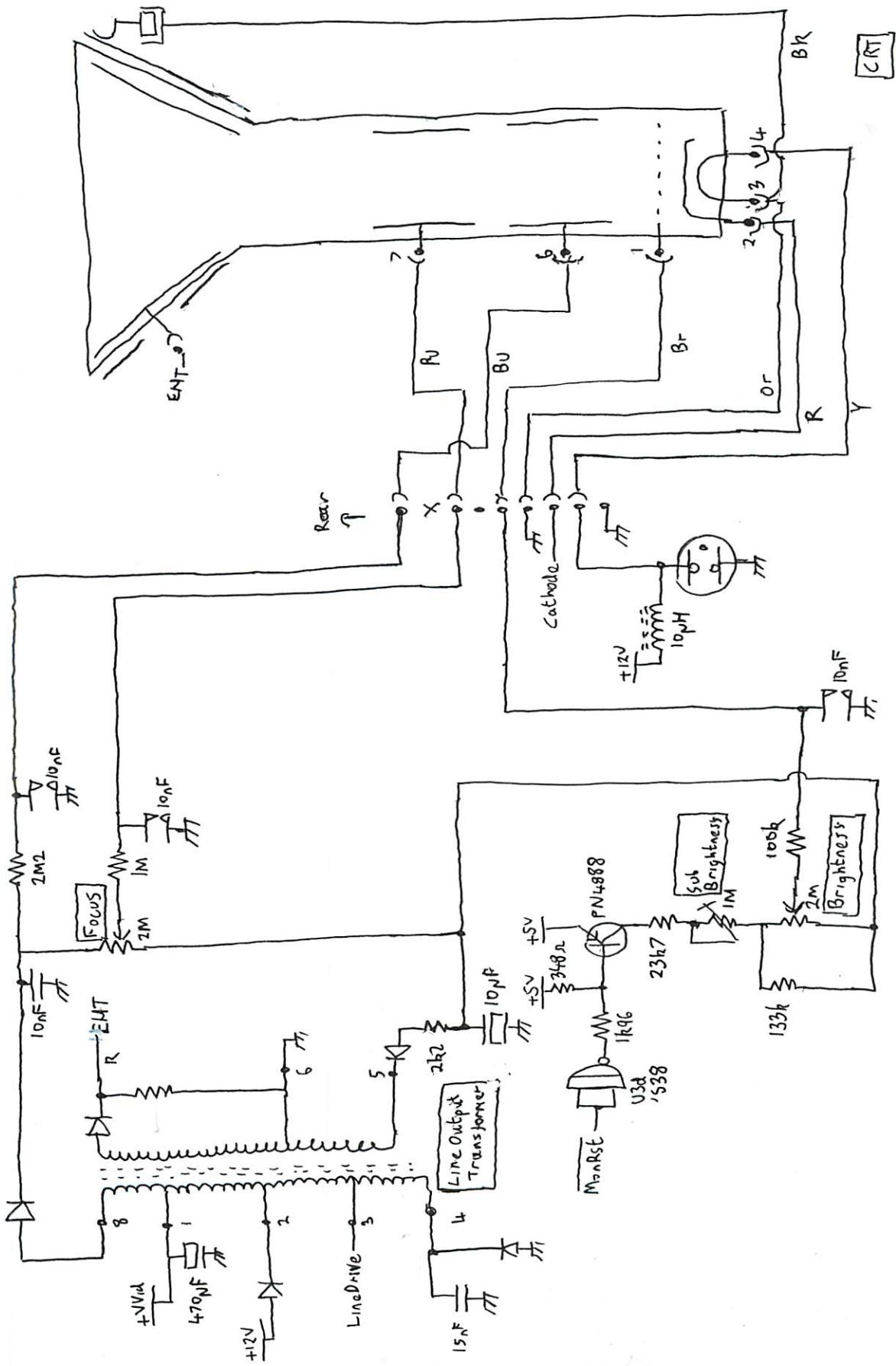
HPI50 Touch screen PCB

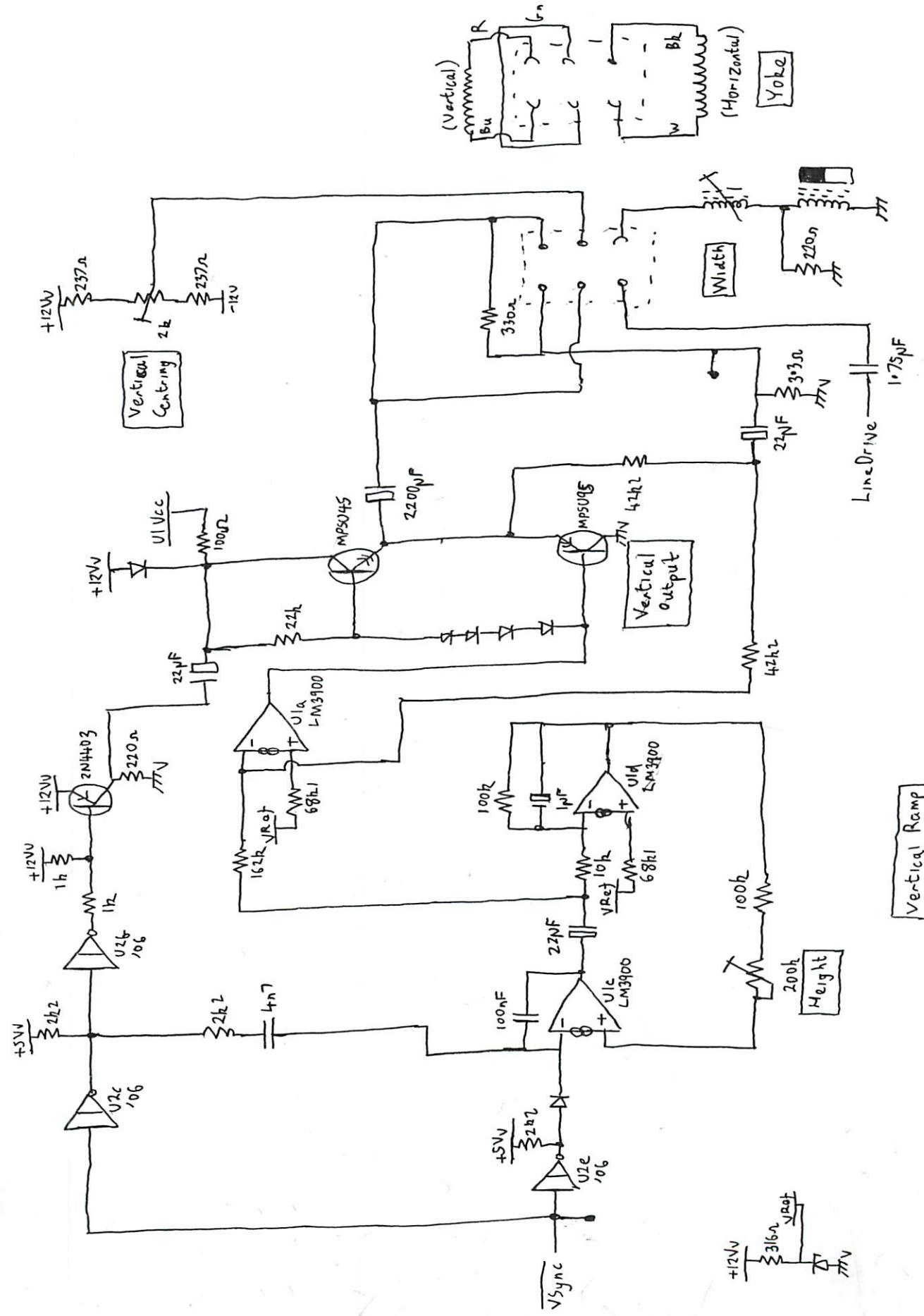
HP150

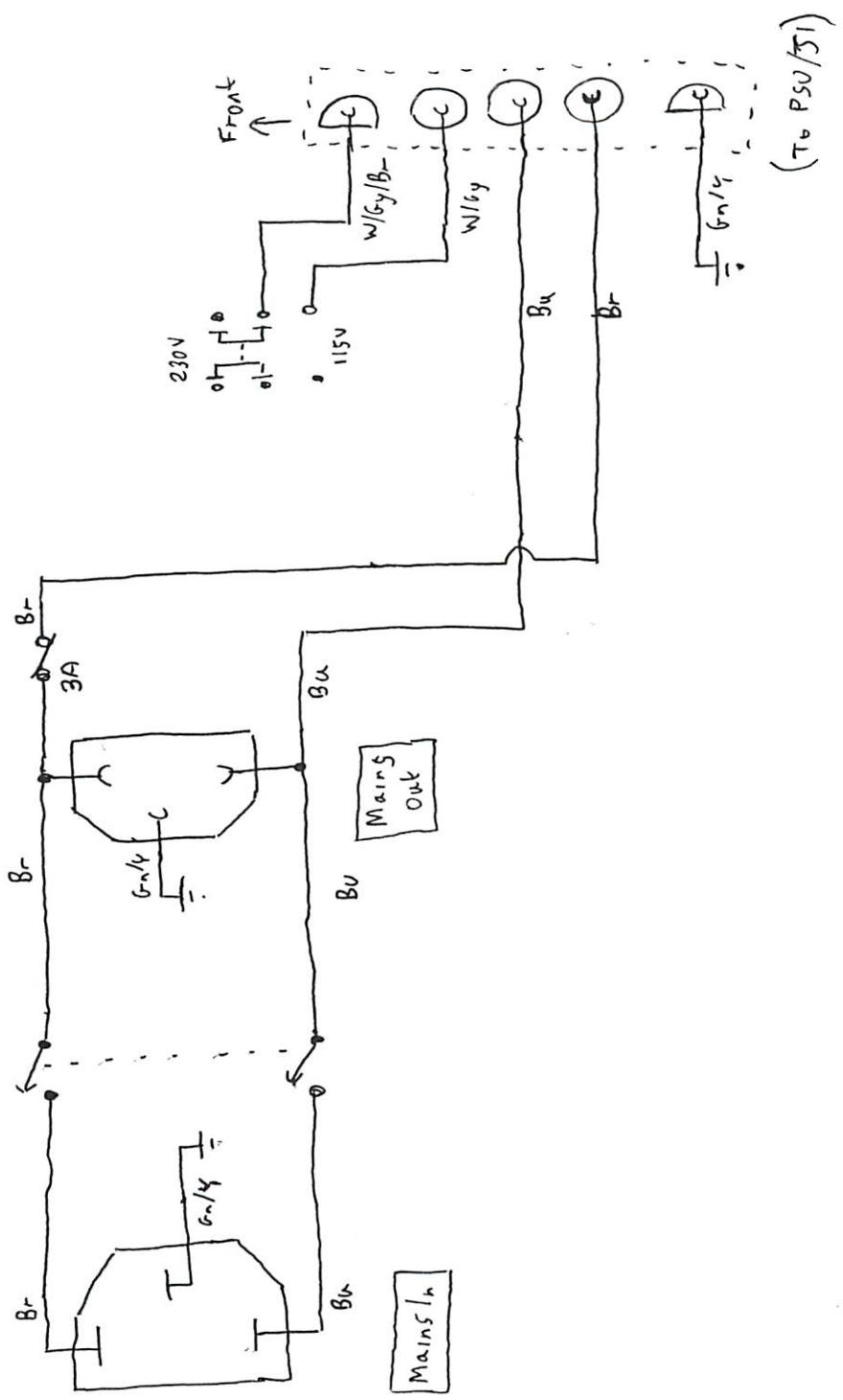




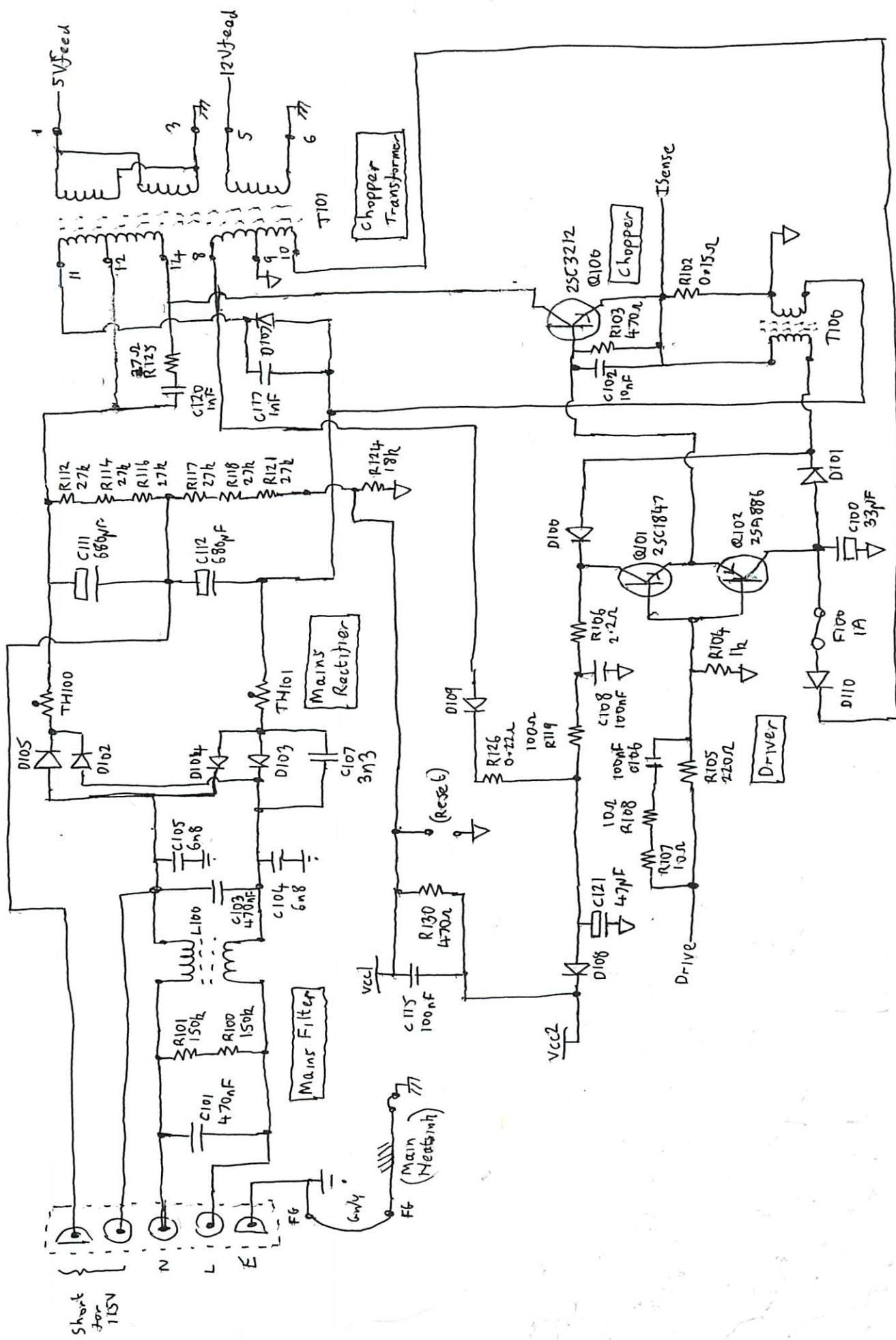


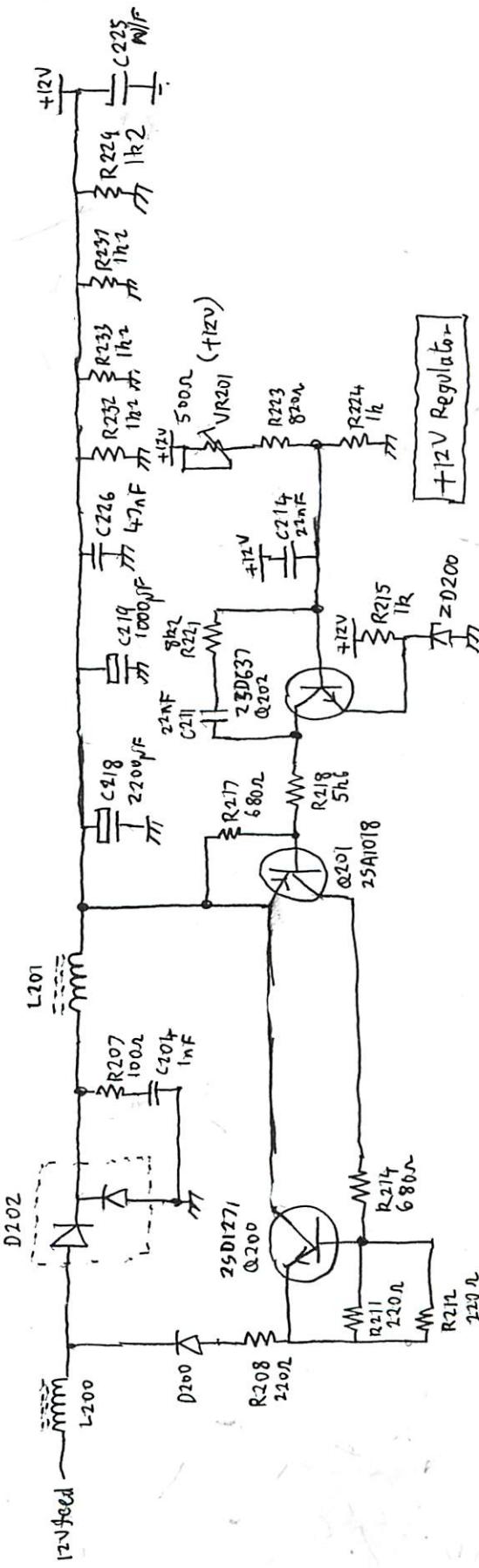
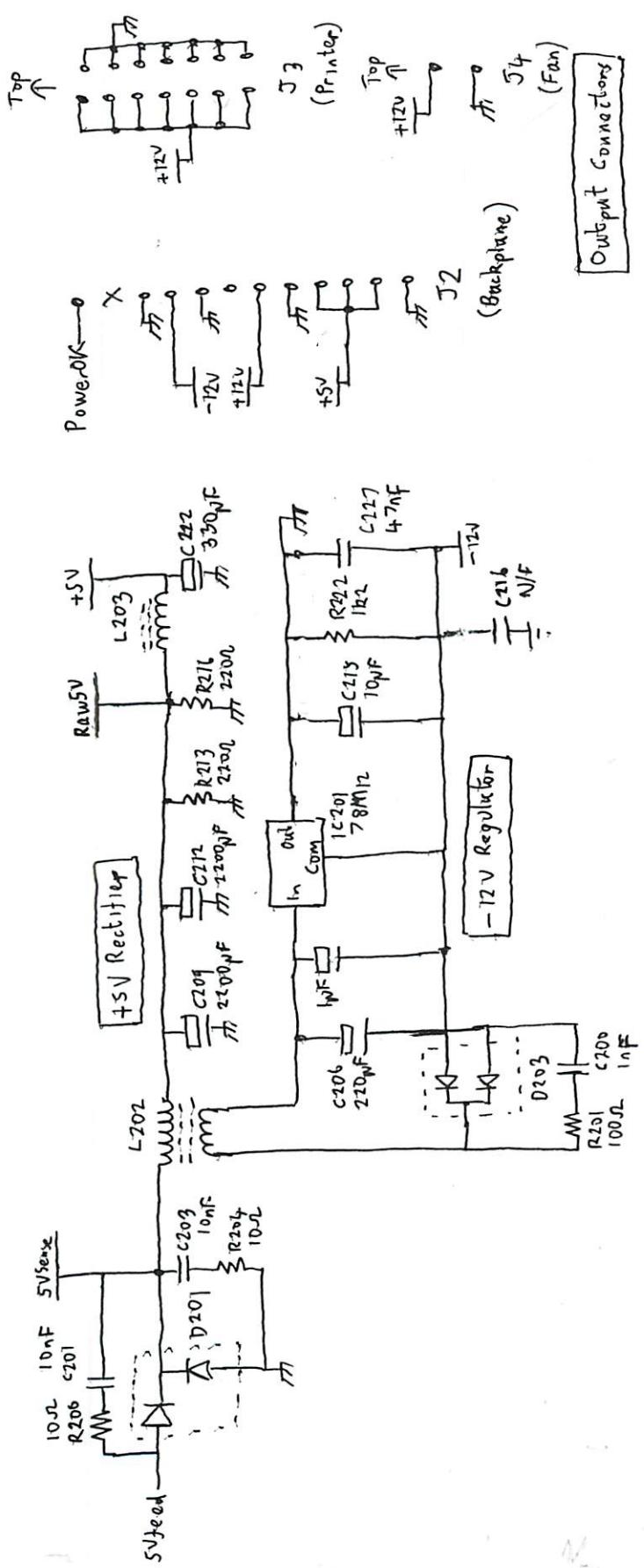






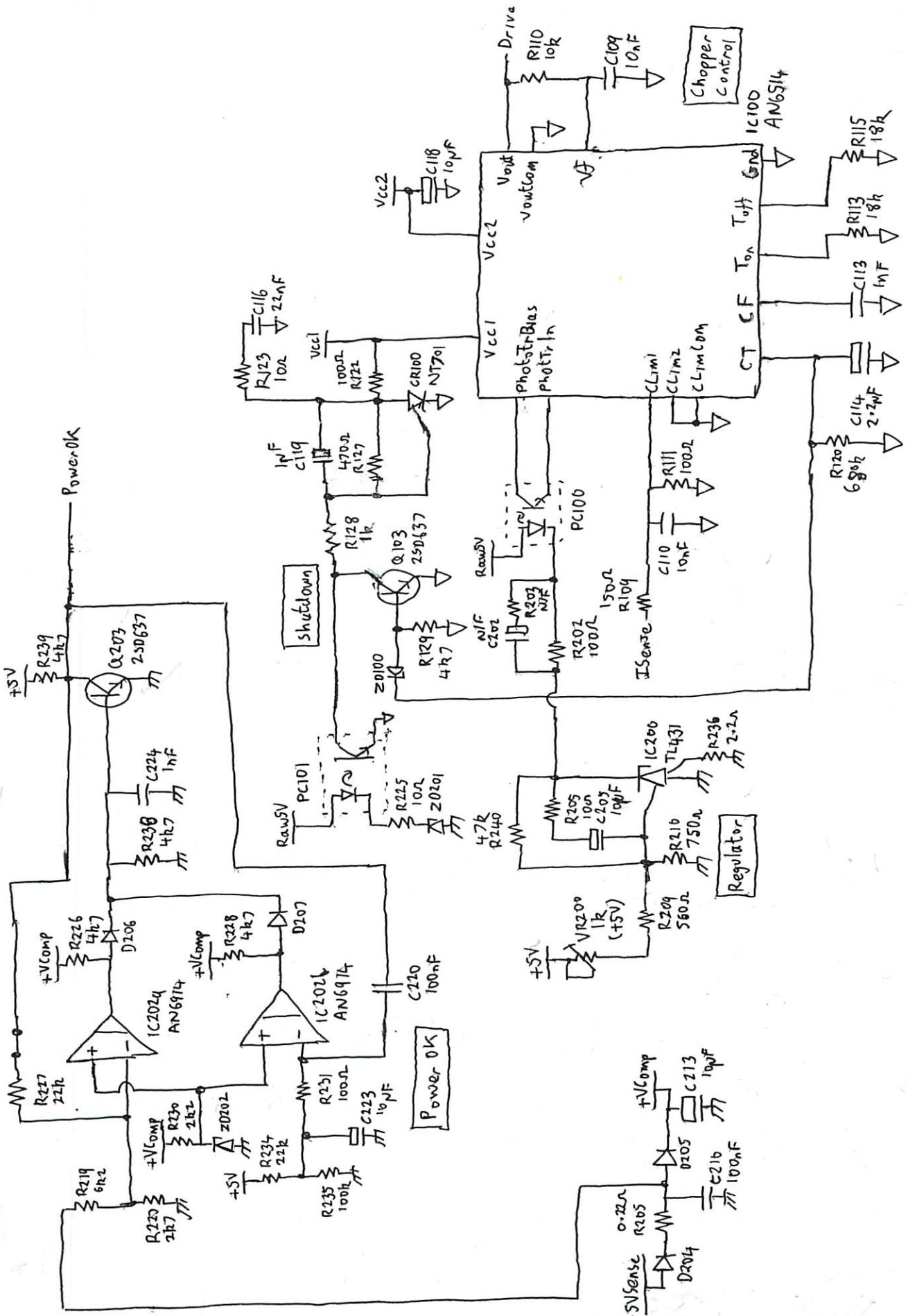
HP150 Mains Input Brackets





45611-80214

HP150 PSU Sheet (2)



45617-80214

HPI50 PSU Sheet(3)