

Entrada
 L_0, L_1, L_2, L_3

Circuito
 lógico para
 validar
 Prossassar

Luz

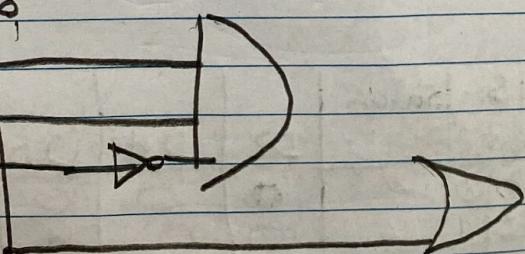
$\overline{L_3}$	$\overline{L_2}$	$\overline{L_1}$	$\overline{L_0}$	$\overline{L_3}$	L_2	L_1	L_0	V
0	0	0	0	0	0	0	0	-
0	0	0	1	0	0	0	1	1
0	0	1	0	0	0	1	0	-
0	0	1	1	0	0	1	1	1
0	1	0	0	0	1	0	0	-
0	1	0	1	0	1	0	1	1
0	1	1	0	0	1	1	0	1
0	1	1	1	0	1	1	1	1
1	0	0	0	1	0	0	1	1
1	0	0	1	1	0	1	1	-
1	0	1	0	1	0	0	0	-
1	0	1	1	1	0	1	1	1
1	1	0	0	1	1	0	0	-
1	1	0	1	1	1	0	1	1
1	1	1	0	1	1	1	0	-
1	1	1	1	1	1	1	1	1

L_2

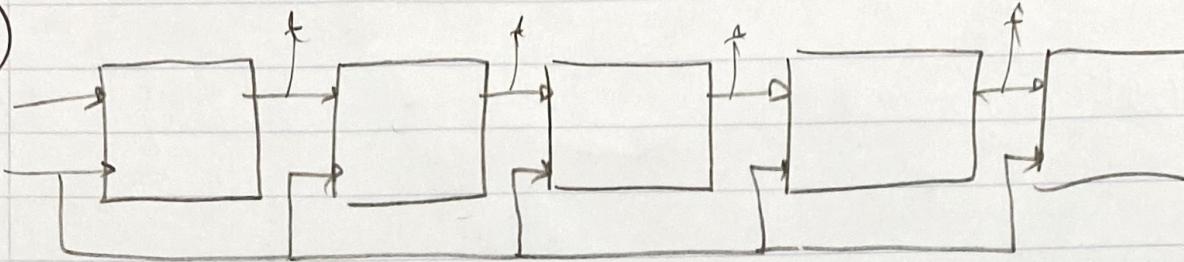
0	0	0	0
0	0	0	1
1	1	1	0
1	1	1	1

$$F = L_0 + L_1 L_2 \overline{L_3}$$

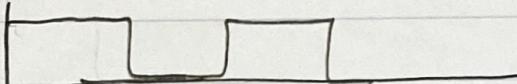
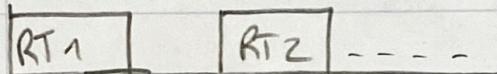
L_3 L_2 L_1 L_0



(4)



Supondo que os resultados dos testes são apresentados
ciclicamente



$\overbrace{\text{clk} \times 5}$

$$\text{clk} \frac{1}{2T}$$