

Posicionamentos TP4

①

1. $I=0 \Rightarrow$ Beg(4 bits PM) \Rightarrow LED AMPL

$I=1 \Rightarrow \{6,8\} \Rightarrow$ LED VARIAC

$10 \leq BCD \leq 15 \Rightarrow$ inválidos em BCD

Decimais	BCD				LED AMPL	LED VARIAC
	b_3	b_2	b_1	b_0		
0	0	0	0	0	1	0
1	0	0	0	1	0	0
2	0	0	1	0	1	0
3	0	0	1	1	0	0
4	0	1	0	0	1	0
5	0	1	0	1	0	0
6	0	1	1	0	1	1
7	0	1	1	1	0	0
8	1	0	0	0	1	1
9	1	0	0	1	0	0
10	1	0	1	0	0	0
11					X	X
12						
13						
14						
15						

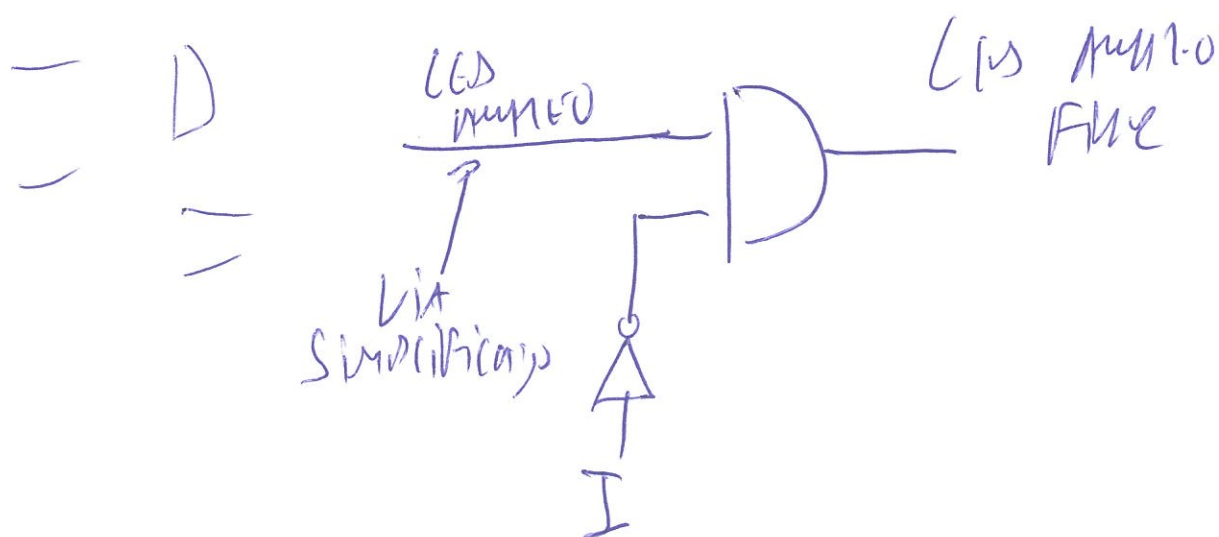
I → Semi USMO como "ENABLE"
DO ABUSMO, VCC ALIMENTA
e)

(2)

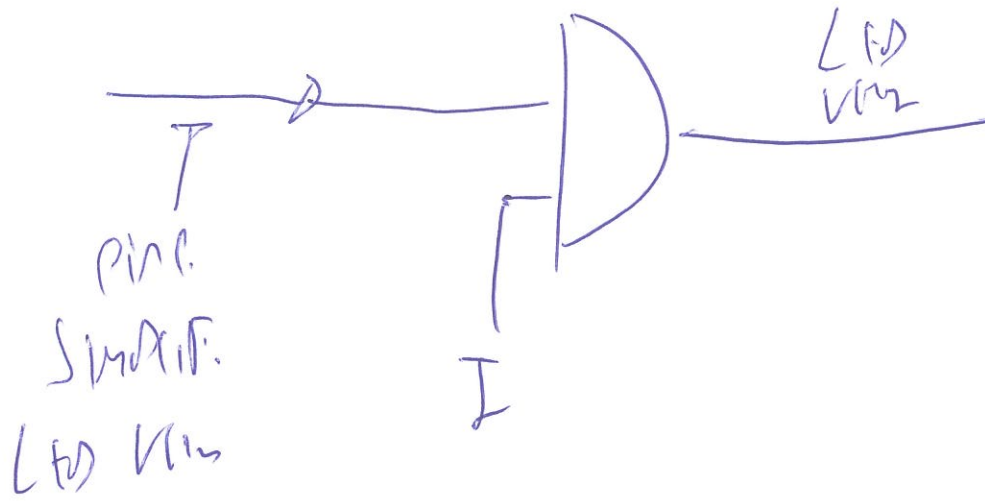
$$b) L_A = \overline{B_3} \overline{B_2} \overline{B_1} \overline{B_0} + \overline{B_3} \overline{B_2} B_1 \overline{B_0} + \overline{B_3} B_2 \overline{B_1} \overline{B_0} + \overline{B_3} B_2 B_1 \overline{B_0}$$

$$L_V = \overline{B_3} B_2 B_1 \overline{B_0} + B_3 \overline{B_2} \overline{B_1} \overline{B_0}$$

e) USM MAS DE KMAPUCH E:



(f) V_{ENDF}



⋮

g)

