ECSEL Joint Undertaking
Electronic Components and Systems for European Leadership

ANDANTE

AI for New Devices And Technologies at the Edge

D2.8 Scorecard update based on realized project results

Deliverable No.	D2.8	Due Date	*31-Oct-2023*
Type	Report	**Dissemination Level**	*Confidential*
Version	1.0	**Status**	Final
Description	This deliverable provides scorecard update based on realized project results identifying strengths of each technology device and critical gaps that might remain. Tasks 2.1 and 2.2.		
Work Package	WP2 – New memory technologies for AI applications.		

Abstract (Published Summary)

This deliverable presents the final scorecards for the non-volatile memory technologies developed within WP2. In the studied space, three main applications have been retained as potential fits for the developed technologies:

- Embedded low power, non-volatile, read intensive memory.

- Last level cache applications.

- Analog devices for machine learning applications.

In these scorecards, we indicate tentative targets for the technology cells in terms of operation conditions, performance, power, and reliability. The first two scorecards include statistical data coming from deliverable 2.7 results. The last scorecard sticks to median results but adds the multibit aspects of analog technology.

In addition, an extra table adds considerations on manufacturability and cost across these various technologies.