





waiting time = Completion time _ Cpu burst _ arrival time

$$P_0: (0-3-2=5)$$

$$P_{l}: 7-7-0 = 0$$

$$p_4$$
: $15 - 1 - 4 = 10$
 p_4 : $19 - 4 - 5 = 10$

R.R. 2=3
@t=0 P1 P1 → runs
@ t=2 PL Po P(5)
@ t=3 Pi is preempted, Pi(4)
[P.(4) PL(6) Pol3) P. 7 -> Yuns
@ t=4 PUP1(4) P2(4) 10(2)
@ t=5 Pales Ps(1) Pr(4) Pr(4) Pr(1)
Po terminates Pr Yuns
@ t=9 Pz is preempted,
Prev P4(4) P3(1) P1(4)
@ t=12 Pris preempted, P3 runs
@ t= B Py terminates , P& Yuns
PICH PRUD PRUGO
(+= 16

arrival rate
$$\frac{2}{2}$$
 Ci

Cpu Tho FCES 80% · Veduces # of 20%

80% EX H · maintelus 500d arrival rate ZC: + ZI; f())
Qo: if there is no proveses in O. RR. . good response (no-1) · 8 /2.) n. , Q. Q=8 (n,-1) - 6, arrival time con bust Po (20), Yun po_ teo 121 10 16 PZ 12 24 Po preempted -13 20 10 P1(16), run P.(15) P2(24) P1(14)

Schedule > t=18 72(24) P(8) 12(15) P3(10) P2(22) t= 26 . P3 (10) P2(16) P18) P(10) P3 (2) P2(16) P1(8) Ps(2) P2(16) P(6) Po(5) Ps(2) Pr(16) Po150 P2(4) PO(5)

PICZ

Pres Pors

Poterminates t =)1:

Ps terminates t= 66

Prterminage t = 75

Head of ready onene