Process Table PCB [0] PCBLiJ PCB [NJ

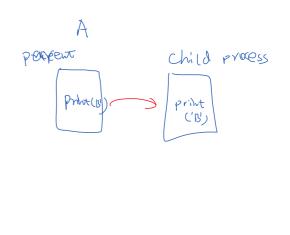
Construct a PCB, means to identity a pas in the process Table, with a tag ' available'

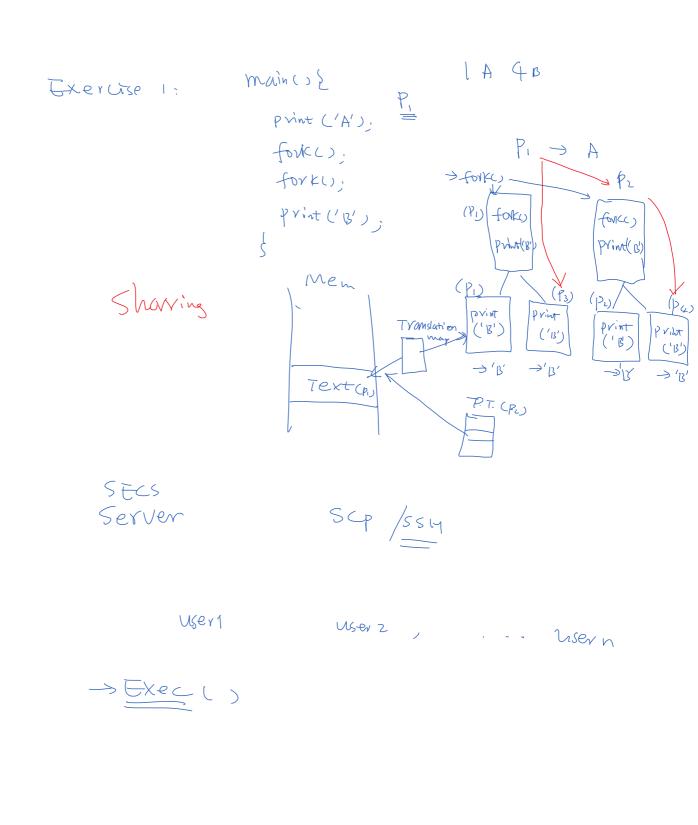
Mache ) { Forker childis chiplication of Advess increasing

every thing below the fork() logical adares

E. 5.1. manne > } -> print ('A'), Vepresevot ation of print (B) Code blocks before and after fork w.

parent process





process P<sub>1</sub>

5 C.s.

Pr Pr Pr Pr Pr Pr Large process

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large process

PI Pulpupus PI Pulpulpu

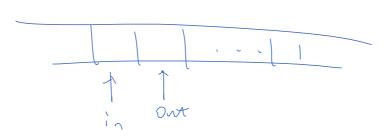
9 C.s

breaks pr into

a set of

Sman processes

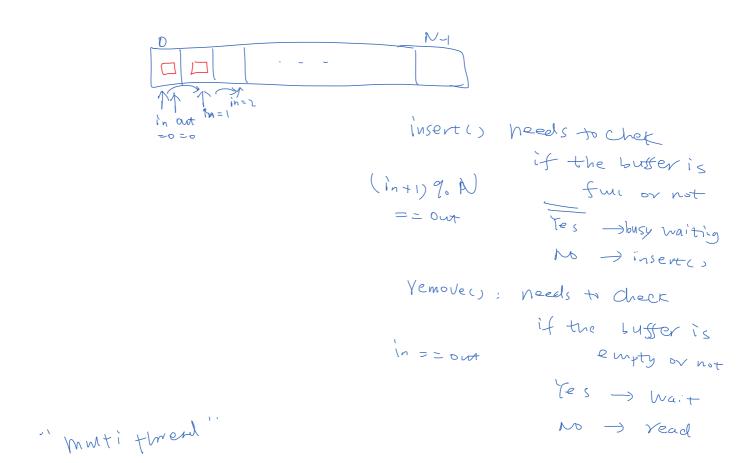
Pzi Pzz Prs --



in % buffer lite == Dux

in (in +1) 7, Buffers, 2e

**N** /. ...



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