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|---|--|---|--|---|--|---|---|
| 1 | | 2 | | 3 | | 4 | |
| A | | | | | | | A |
| B | | | | | | | B |
| C | | | | | | | C |

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|---|--|------------------|--|---------------------|--|
| Diseño de la placa PCB de conversor de voltaje reductor | | | | | |
| UCB | | | | | |
| Sheet: | | | | | |
| File: Tarea1.kicad_pcb | | | | | |
| Title: Step down voltage converter | | | | | |
| Size: A5 | | Date: 2024-04-21 | | Rev: Fabrizio Yucra | |
| KiCad E.D.A. 8.0.1 | | Id: 1/1 | | | |

Diseño de la placa PCB de conversor de voltaje reductor

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