

### Ethernet DMA current host receive buffer address register (ETH\_DMACHRBAR)

Address offset: 0x1054

Reset value: 0x0000 0000

The current host receive buffer address register points to the current receive buffer address being read by the DMA.

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
HRBAP																															
r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r	r

Bits 31:0 **HRBAP**: Host receive buffer address pointer

Cleared On Reset. Pointer updated by DMA during operation.

## 33.8.5 Ethernet register maps

[Table 196](#) gives the ETH register map and reset values.

**Table 196. Ethernet register map and reset values**

Offset	Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0x00	ETH_MACCR	Reserved							CSTF	eserved	WD	JD	Reserved	IFG				CSD	Reserved	FES	ROD	LM	DM	IPCO	RD	Reserved	APCS	BL		DC	TE	RE	Reserved
	Reset value								0		0	0		0	0	0	0	0		0	0	0	0	0	0		0	0	0	0	0	0	
0x04	ETH_MACFFR	RA	Reserved																				HPF	SAF	SAIF	PCF	BFD	PAM	DAIF	HM	HU	PM	
	Reset value	0																					0	0	0		0	0	0	0	0	0	0
0x08	ETH_MACHTHR	HTH[31:0]																															
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x0C	ETH_MACHTLR	HTL[31:0]																															
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x10	ETH_MACMIAR	Reserved															PA				MR				CR				M	W	M	B	
	Reset value																0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x14	ETH_MACMIIDR	Reserved															MD																
	Reset value																0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x18	ETH_MACFCR	PT															Reserved							ZQPD	Reserved	PLT		UPFD	RFCE	TFCE	FCB/BPA		
	Reset value																							0		0	0	0	0	0	0	0	0
0x1C	ETH_MACVLANTR	Reserved															VLANTC	VLANTI															
	Reset value																0																
0x28	ETH_MACRWÜFFR	Frame filter reg0\Frame filter reg1\Frame filter reg2\Frame filter reg3\Frame filter reg4...\Frame filter reg7																															
	Reset value	0																															

Table 196. Ethernet register map and reset values (continued)

Offset	Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0		
0x2C	ETH_MACPMTCSR	WFFRPR	Reserved																				GU	Reserved		WFR	MPR	Reserved		WFE	MPE	PD			
	Reset value	0																					0			0	0			0	0	0			
0x34	ETH_MACDBG	Reserved						TFF	TFNEGU	Reserved		TFWA	TFRS	MTP	MTFCS	MMTEA	Reserved						RFLL	Reserved		RFRCS	RFWRA	Reserved		MSFRWCS	MMRPEA				
	Reset value							0	0			0	0	0	0	0	0							0	0			0	0	0	0	0	0		
0x38	ETH_MACSR	Not applicable																Reserved						TSTS	Reserved		MMCTS	MMCRS	MMCS	PMTS	Reserved				
	Reset value																							0			0	0	0	0					
0x3C	ETH_MACIMR	Not applicable																Reserved						TSTIM	Reserved						PMTIM	Reserved			
	Reset value																							0							0				
0x40	ETH_MACA0HR	MO	Reserved																MACA0H																
	Reset value	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
0x44	ETH_MACA0LR	MACA0L																																	
	Reset value	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
0x48	ETH_MACA1HR	AE	SA	MBC[6:0]						Reserved								MACA1H																	
	Reset value	0	0	0	0	0	0	0	0									1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
0x4C	ETH_MACA1LR	MACA1L																																	
	Reset value	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1		
0x50	ETH_MACA2HR	AE	SA	MBC						Reserved								MACA2H																	
	Reset value	0	0	0	0	0	0	0	0									1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
0x54	ETH_MACA2LR	MACA2L																																	
	Reset value	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
0x58	ETH_MACA3HR	AE	SA	MBC						Reserved								MACA3H																	
	Reset value	0	0	0	0	0	0	0	0									1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
0x5C	ETH_MACA3LR	MACA3L																																	
	Reset value	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
0x100	ETH_MMCCR	Reserved																								MCFHP		MCP	McF	ROR	CSR	CR			
	Reset value																									0	0	0	0	0	0	0			
0x104	ETH_MMCRIR	Reserved																RGUFS	Reserved								RFAES	RFCES	Reserved						
	Reset value																	0									0	0							
0x108	ETH_MMCTIR	Reserved										TGFS	Reserved				TGFMSCS	TGFSCS	Reserved																
	Reset value											0					0	0																	
0x10C	ETH_MMCRIMR	Reserved																RGUFM	Reserved								RFAEM	RFCEM	Reserved						
	Reset value																	0									0	0							

Table 196. Ethernet register map and reset values (continued)

Offset	Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0													
0x110	ETH_MMCTIMR	Reserved										TGFM	Reserved				TGFMSCM	TGFSCM	Reserved																											
	Reset value											0					0	0																												
0x14C	ETH_MMCTGFS CCR	TGFSCC																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x150	ETH_MMCTGF MSCCR	TGFMSCC																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x168	ETH_MMCTGF CR	TGFC																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x194	ETH_MMCRFC ECR	RFCEC																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x198	ETH_MMCRFAE CR	RFAEC																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x1C4	ETH_MMCRGU FCR	RGUFC																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x700	ETH_PTPTSCR	Reserved														TSPFFMAE	TSCNT	TSSMRME	TSSEME	TSSIPV4FE	TSSIPV6FE	TSSPTPOEFE	TSPTPPSV2E	TSSSR	TSSARFE	Reserved	TTSARU	TSITE	TSSTU	TSSTI	TSFCU	TSE														
	Reset value															0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0										
0x704	ETH_PTPSSIR	Reserved																				STSSI																								
	Reset value																					0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x708	ETH_PTPTSHR	STS[31:0]																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x70C	ETH_PTPTSLR	STPNS	STSS																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x710	ETH_PTPTSHU R	TSUS																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x714	ETH_PTPTSLU R	TSUPNS	TSUSS																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x718	ETH_PTPTSAR	TSA																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x71C	ETH_PTPTTHR	TTSH																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													
0x720	ETH_PTPTTLR	TTSL																																												
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0													

Table 196. Ethernet register map and reset values (continued)

Offset	Register	31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0												
0x728	ETH_PTPTSSR	Reserved																														TSTTR	TSSO												
	Reset value																															0	0												
0x72C	ETH_PTPPPSC R	Reserved																														PPS FREQ													
	Reset value																															0	0	0											
0x1000	ETH_DMABMR	Reserved				MB	AAB	FPM	USP	RDP				FB	PM	PBL				EDFE	DSL				DA	SR																			
	Reset value					0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1											
0x1004	ETH_DMATPDR	TPD																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												
0x1008	ETH_DMARPDR	RPD																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												
0x100C	ETH_DMARDLA R	SRL																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												
0x1010	ETH_DMATDLA R	STL																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												
0x1014	ETH_DMASR	Reserved	TSTS	PMTS	MMCS	Reserved	EBS		TPS		RPS		NIS	AIS	ERS	FBES	Reserved	ETS	RWTS	RPSS	RBUS	RS	TUS	ROS	TJTS	TBUS	TPSS	TS																	
	0		0	0	0		0	0	0	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0																
0x1018	ETH_DMAOMR	Reserved				DTCEFD	RSF	DFRF	Reserved	TSF	FTF	Reserved	TTC			ST	Reserved	FEF	FUGF	Reserved	RTC		OSF	SR	Reserved																				
	0	0	0	0	0	0	0	0		0	0		0	0	0	0		0	0		0	0	0	0	0	0	0																		
0x101C	ETH_DMAIER	Reserved												NISE	AISE	ERIE	FBEIE	Reserved	ETIE	RWTIE	RPSIE	RBUIE	RIE	TUIE	ROIE	TJTIE	TBUIE	TPSIE	TIE																
	Reset value													0	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0															
0x1020	ETH_DMAMFB OCR	Reserved	OFOC	MFA										OMFC	MFC																														
	0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0														
0x1024	ETH_DMARSWTR	Reserved																				RSWTC																							
	Reset value																					0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0x1048	ETH_DMACHTDR	HTDAP																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												
0x104C	ETH_DMACHRDR	HRDAP																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												
0x1050	ETH_DMACHTBAR	HTBAP																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												
0x1054	ETH_DMACHRBAR	HRBAP																																											
	Reset value	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0												

Refer to [Section 2.3: Memory map](#) for the register boundary addresses.