## **OPERATING SYSTEM**

System Introduction OS development with PC Simulator 'Bochs'



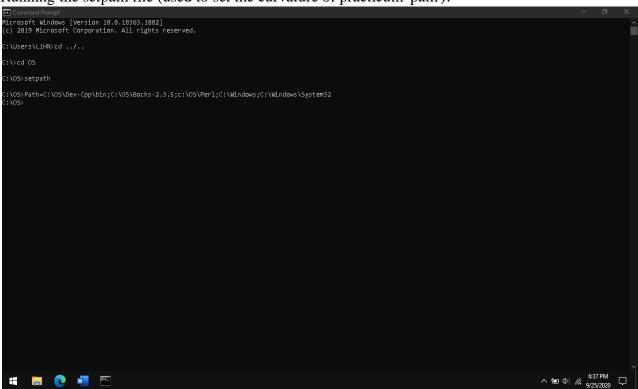
# By: FADHLIH HASAN SETIAWAN L200194209

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# INFORMATION TECHNOLOGY FACULTY OF COMMUNICATION AND INFORMATICS UNIVERSITY OF MUHAMMADIYAH SURAKARTA 2019/2020 SCHOOL YEAR

### Work steps:

- 1. Run the command prompt program.
- 2. Reset directory and enter directory 'C: \ OS' using the 'cd ../ ..' commands and 'cd OS'.
- 3. Running the setpath file (used to set the curvature of practicum 'path').



4. Run the makefile program in the fp.disk file using the 'make fp.disk' command (running this command means that we will compile the source code for the program 'boot.asm', as output file 'boot.bin' and its contents copied to in the bootsector floppy image file 'floppya.img').

5. Check the directory using the 'dir' command.

```
© Command Prompt

#Ecrosoft Valindows (Version 10.0.18363.18362)

(c) 2819 #Ecrosoft Corporation. All rights reserved.

C:\Users\LiMbccd.../..

C:\Vad 05

C:\Users\LiMbccd.../..

C:\Vad 05

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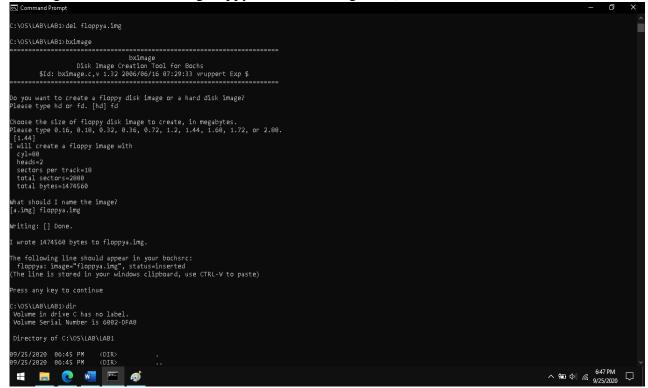
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- 6. Delete the floppies image using the command 'del floppya.img'.
- 7. Call 'bximage', then select type fd, and the file is named 'floppya.img' with the default capacity option (bximage is useful for creating floppy disk based image files).



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ES Command Prompt

CTOSS_LABAL_HABS_DET.

Volume in Circly co has no label.

Volume Serial Number is G692-GFAB

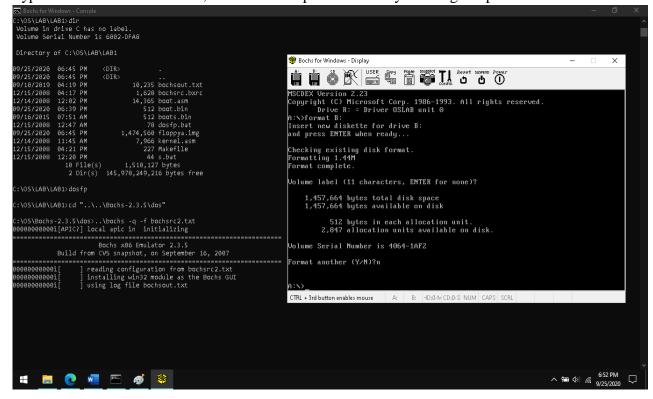
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Volume Serial Number is G692-GFAB

SUP_CTOSS_LABAL_HABS_DET.

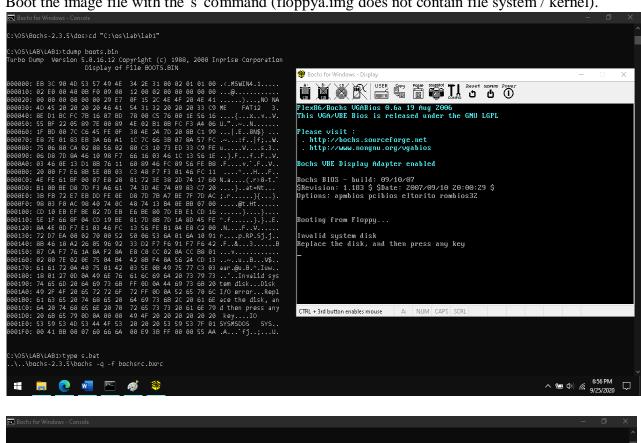
SUP_CTOSS
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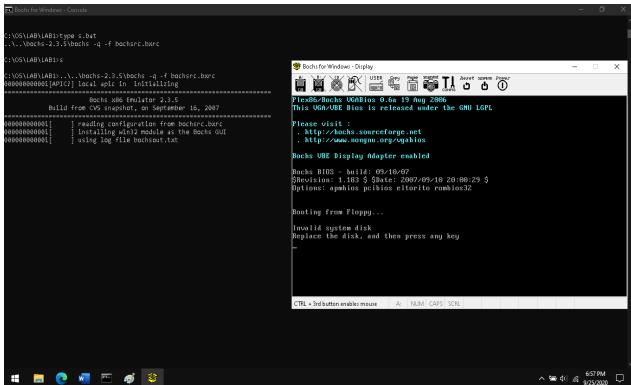
- 8. Run the dosfp command (used to format the image file that was created earlier).
- 9. Type 'format B:' then enter 2x, then close the pc simulator by clicking the power button.



- 10. To view the bootsector data, use the command 'tdump boots.bin'.
- 11. View the file 's.bat' with the command 'type s.bat'.

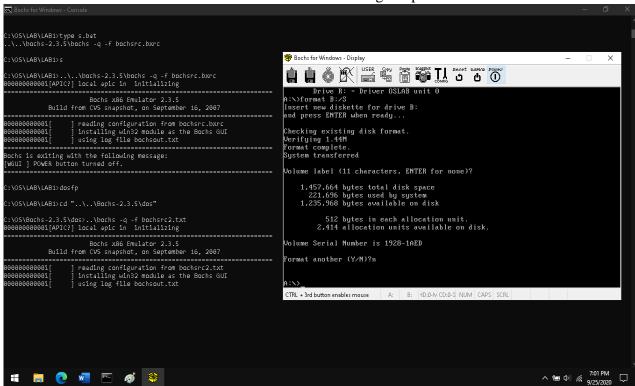
12. Boot the image file with the 's' command (floppya.img does not contain file system / kernel).



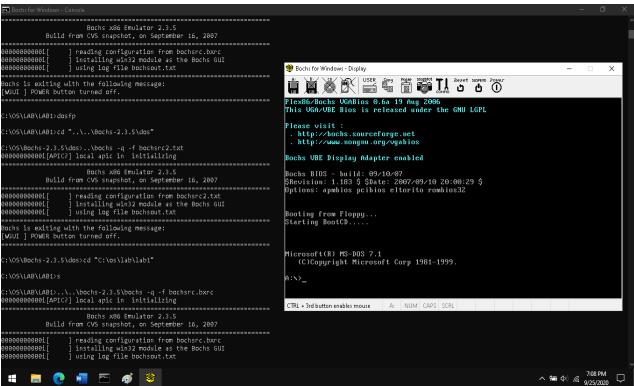


13. To load the file system, call 'dosfp'.

14. Format with the command 'format B: / S' then close it using the power button.



15. Double check with the 's' command.



### **Assignment:**

1. What is the meaning of 'ASCII' code, make standard ASCII table not need to be extended, write ASCII code in decimal, binary and hexadecimal number format and encoded characters and symbols.

### Answer:

ASCII stands for (American Standard Code for Information Interchange), and the meaning of ASCII itself is an international standard in letter and symbol codes such as Hex and Unicode but ASCII is more universal, for example 124 is for the character "|".

**Table of ASCII** 

Tuble of riseri											
Binary	Dec	Hex	Character	Binary	Dec	Hex	Character	Binary	Dec	Hex	Character
0100000	<u>32</u>	<u>20</u>	©	0100000	<u>64</u>	<u>40</u>	@	1100000	<u>96</u>	<u>60</u>	`
0100001	<u>33</u>	<u>21</u>	!	0100001	<u>65</u>	<u>41</u>	A	1100001	<u>97</u>	<u>61</u>	a
0100010	<u>34</u>	<u>22</u>	"	1000010	<u>66</u>	<u>42</u>	В	1100010	<u>98</u>	<u>62</u>	b
<u>0100011</u>	<u>35</u>	<u>23</u>	#	<u>1000011</u>	<u>67</u>	<u>43</u>	C	<u>1100011</u>	<u>99</u>	<u>63</u>	С
<u>0100100</u>	<u>36</u>	<u>24</u>	\$	<u>1000100</u>	<u>68</u>	<u>44</u>	D	<u>1100100</u>	<u>100</u>	<u>64</u>	d
<u>0100101</u>	<u>37</u>	<u>25</u>	%	<u>1000101</u>	<u>69</u>	<u>45</u>	E	<u>1100101</u>	<u>101</u>	<u>65</u>	e
<u>0100110</u>	<u>38</u>	<u>26</u>	&	<u>1000110</u>	<u>70</u>	<u>46</u>	F	<u>1100110</u>	<u>102</u>	<u>66</u>	f
<u>0100111</u>	<u>39</u>	<u>27</u>	6	<u>1000111</u>	<u>71</u>	<u>47</u>	G	<u>1100111</u>	<u>103</u>	<u>67</u>	g
<u>0101000</u>	<u>40</u>	<u>28</u>	(	<u>1001000</u>	<u>72</u>	<u>48</u>	Н	<u>1101000</u>	<u>104</u>	<u>68</u>	h
<u>0101001</u>	<u>41</u>	<u>29</u>	)	<u>1001001</u>	<u>73</u>	<u>49</u>	I	<u>1101001</u>	<u>105</u>	<u>69</u>	i
<u>i0101010</u>	<u>42</u>	<u>2A</u>	*	<u>1001010</u>	<u>74</u>	<u>4A</u>	J	<u>1101010</u>	<u>106</u>	<u>6A</u>	j
<u>0101011</u>	<u>43</u>	<u>2B</u>	+	<u>1001011</u>	<u>75</u>	<u>4B</u>	K	<u>1101011</u>	<u>107</u>	<u>6B</u>	k
0101100	<u>44</u>	<u>2C</u>	,	<u>1001100</u>	<u>76</u>	<u>4C</u>	L	<u>1101100</u>	<u>108</u>	<u>6C</u>	1
<u>0101101</u>	<u>45</u>	<u>2D</u>	-	<u>1001101</u>	<u>77</u>	<u>4D</u>	M	<u>1101101</u>	<u>109</u>	<u>6D</u>	m
<u>0101110</u>	<u>46</u>	<u>2E</u>	•	<u>1001110</u>	<u>78</u>	<u>4E</u>	N	<u>1101110</u>	<u>110</u>	<u>6E</u>	n
<u>0101111</u>	<u>47</u>	<u>2F</u>	/	<u>1001111</u>	<u>79</u>	<u>4F</u>	О	<u>1101111</u>	<u>111</u>	<u>6F</u>	0
<u>0110000</u>	<u>48</u>	<u>30</u>	0	<u>101000</u>	<u>80</u>	<u>50</u>	P	<u>1110000</u>	<u>112</u>	<u>70</u>	p
<u>0110001</u>	<u>49</u>	<u>31</u>	1	<u>1010001</u>	<u>81</u>	<u>51</u>	Q	<u>1110001</u>	<u>113</u>	<u>71</u>	q
<u>0110010</u>	<u>50</u>	<u>32</u>	2	<u>1010010</u>	<u>82</u>	<u>52</u>	R	<u>1110010</u>	<u>114</u>	<u>72</u>	r
<u>0110011</u>	<u>51</u>	<u>33</u>	3	<u>1010011</u>	<u>83</u>	<u>53</u>	S	<u>1110011</u>	<u>115</u>	<u>73</u>	S
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<u>0110101</u>	<u>53</u>	<u>35</u>	5	<u>1010101</u>	<u>85</u>	<u>55</u>	U	<u>1110101</u>	<u>117</u>	<u>75</u>	u
<u>0110111</u>	<u>54</u>	<u>36</u>	6	<u>1010110</u>	<u>86</u>	<u>56</u>	V	<u>1110110</u>	<u>118</u>	<u>76</u>	v
<u>0110111</u>	<u>55</u>	<u>37</u>	7	<u>1010111</u>	<u>87</u>	<u>57</u>	W	<u>1110111</u>	<u>119</u>	<u>77</u>	W
<u>0111000</u>	<u>56</u>	<u>38</u>	8	<u>1011000</u>	<u>88</u>	<u>58</u>	X	<u>1111000</u>	<u>120</u>	<u>78</u>	X
<u>0111001</u>	<u>57</u>	<u>39</u>	9	<u>1011001</u>	<u>89</u>	<u>59</u>	Y	<u>1111001</u>	<u>121</u>	<u>79</u>	У
<u>0111010</u>	<u>58</u>	<u>3A</u>	:	<u>1011010</u>	<u>90</u>	<u>5A</u>	Z	<u>1111010</u>	<u>122</u>	<u>7A</u>	Z
<u>0111011</u>	<u>59</u>	<u>3B</u>	:	<u>1011011</u>	<u>91</u>	<u>5B</u>	[	<u>1111011</u>	<u>123</u>	<u>7B</u>	{
<u>0111100</u>	<u>60</u>	<u>3C</u>	<	<u>1011100</u>	<u>92</u>	<u>5C</u>	\	<u>1111100</u>	<u>124</u>	<u>7C</u>	
<u>0111101</u>	<u>61</u>	<u>3D</u>	=	<u>1011101</u>	<u>93</u>	<u>5D</u>	]	<u>1111101</u>	<u>125</u>	<u>7D</u>	}
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0111111	<u>63</u>	<u>3F</u>	?	<u>1011111</u>	<u>95</u>	<u>5F</u>	_				

2. Look for the complete list of intel x86 family assembly commands (from books as well as the internet). This command list can be used as a guide to understanding the "boot.asm" and "karnel.asm" programs.

### Answer:

### 1. ACALL (Absolute Call).

ACALL functions to call program sub routines.

### 2. ADD (Add Immediate Data).

ADD functions to add 8 bits of data directly into the contents of the accumulator and store the results in the accumulator.

### 3. ADDC (Add Carry Plus Immediate Data to Accumulator).

ADDC functions to add the contents of the carry flag (0 or 1) to the contents of the accumulator. 8 bit direct data is added to the accumulator.

### 4. AJMP (Absolute Jump)

AJMP is an absolute jump command. Jumps in 2 KB start at the address following the AJMP command. AJMP functions to transfer control of the program to a location where the address is calculated in the same way as the ACALL command. The program counter is added twice where the AJMP command is a 2-byte command. The program counter is loaded with a10 - a0 11 bits, to form a 16-bit destination address.

### 5. ANL (logical AND memory to accumulator)

ANL functions to contain the contents of the data address with the contents of the accumulator.

### **6.** CJNE (Compare Indirect Address to Immediate Data)

CJNE functions to compare data directly with the memory location addressed by register R or Accumulator A. if not the same, the instruction will go to the code address. Format: CJNE R, # data, address code.

### 7. CLR (Clear Accumulator)

CLR functions to reset the accumulator data to 00H.

Format: CLR A.

### 8. CPL (Complement Accumulator)

CPL serves to complement the contents of the accumulator.

### 9. DA (Decimal Adjust Accumulator)

DA functions to adjust the contents of the accumulator to BCD equivalents, after adding two BCD numbers.

### 10. DEC (Decrement Indirect Address)

DEC functions to reduce the contents of the memory location designated by register R by 1, and the results are stored at that location.

### 11. DIV (Divide Accumulator by B)

DIV functions to divide the contents of the accumulator with the contents of register B. The accumulator contains the quotient, register B contains the remainder of the division.

### 12. DJNZ (Decrement Register And Jump Id Not Zero)

DJNZ functions to reduce the register value by 1 and if the result is 0 then the next instruction will be executed. If not 0, it will go to the address code.

### 13. INC (Increment Indirect Address)

INC functions to add memory contents by 1 and store it at that address.

### 14. JB (Jump if Bit is Set)

JB functions to read data by one bit, if the data is 1 then it will go to the code address and if it is 0 it will not go to the code address.

### 15. JBC (Jump if Bit Set and Clear Bit)

Bit JBC, functions as a test command rail specified by bit. If the bit is set, Jump is performed to the relative address and the specified bit in the command is cleared. The following program segment tests the least significant bit (LSB: Least Significant Byte), and if it is found that it is set, the program jumps to the READ location. JBC also functions to clean the LSB from the accumulator.

### 16. JC (Jump if Carry is Set)

The JC instruction is used to test the contents of the carry flag. If it contains 1, execution goes to the address code, if it contains 0, the next instruction will be executed.

### 17. JMP (Jump to sum of Accumulator and Data Pointer)

The JMP instruction is used to instruct the jump to a specific code address.

Format: JMP address code.

### 18. JNB (Jump if Bit is Not Set)

The JNB instruction is used to read data by one bit, if the data is 0 then it will go to the code address and if it is 1 it will not go to the code address.

Format: JNB address bits, address codes.

### 19. JNC (Jump if Carry Not Set)

JNC functions to test the Carry bit, and if it is not set, a jump will be made to the specified relative address.

### 20. JNZ (Jump if Accumulator Not Zero)

JNZ is the mnemonic for the jump if not zero instruction. In this case a jump will occur when the zero flag is "clear", and no jump will occur when the zero flag is set. Suppose that the JNZ 7800H is stored at location 2100H. If Z=0, the next instruction will come from location 7800H: and when Z=1, the program will descend to the next sequence instruction at location 2101H.

### 21. JZ (Jump if Accumulator is Zero)

JZ serves to test accumulator contents. If it is not zero, the jump is made to the relative address specified in the command.

### 22. LCALL (Long Call)

LCALL functions to allow calls to subroutines located anywhere in the 64K program memory. The LCALL operation goes like this:

- Adding to the program counter as many as 3, because the command is a 3-byte command.
- Add stack pointer by 1.
- Stores lower bytes of program counter on the stack.
- Adding a stack pointer.
- Stores a higher byte of the program on the stack.
- Load program counter with 16-bit destination address.

### 23. LJMP (Long Jump)

Long Jump functions to allow unconditional jump anywhere within the scope of program memory 64K. LCALL is a 3-byte command. The 16-bit destination address is specified directly in the command. This destination address is loaded into the program counter by the LJMP command.

### 24. MOV (Move From Memory)

MOV functions to move the contents of the accumulator / register or data from outside values or other addresses.

# 25. MOVC (Move From Codec Memory)

The MOVC instruction fills the accumulator with code bytes or constants from program memory. The byte address is the sum of the unsigned 8 bits in the accumulator and the 16-bit base register, which can be a data pointer or a program counter. This instruction does not affect any flags.