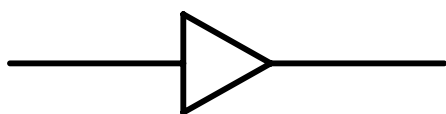


Buffer



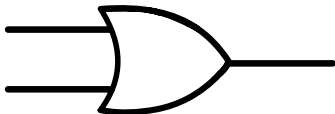
A	Q
0	0
1	1

AND



A	B	Q
0	0	0
0	1	0
1	0	0
1	1	1

OR



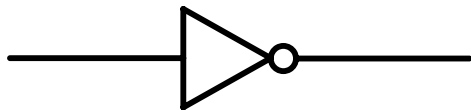
A	B	Q
0	0	0
0	1	1
1	0	1
1	1	1

XOR



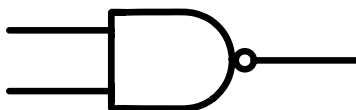
A	B	Q
0	0	0
0	1	1
1	0	1
1	1	0

NOT



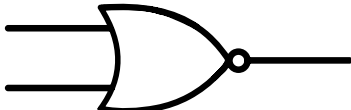
A	Q
0	1
1	0

NAND



A	B	Q
0	0	1
0	1	1
1	0	1
1	1	0

NOR

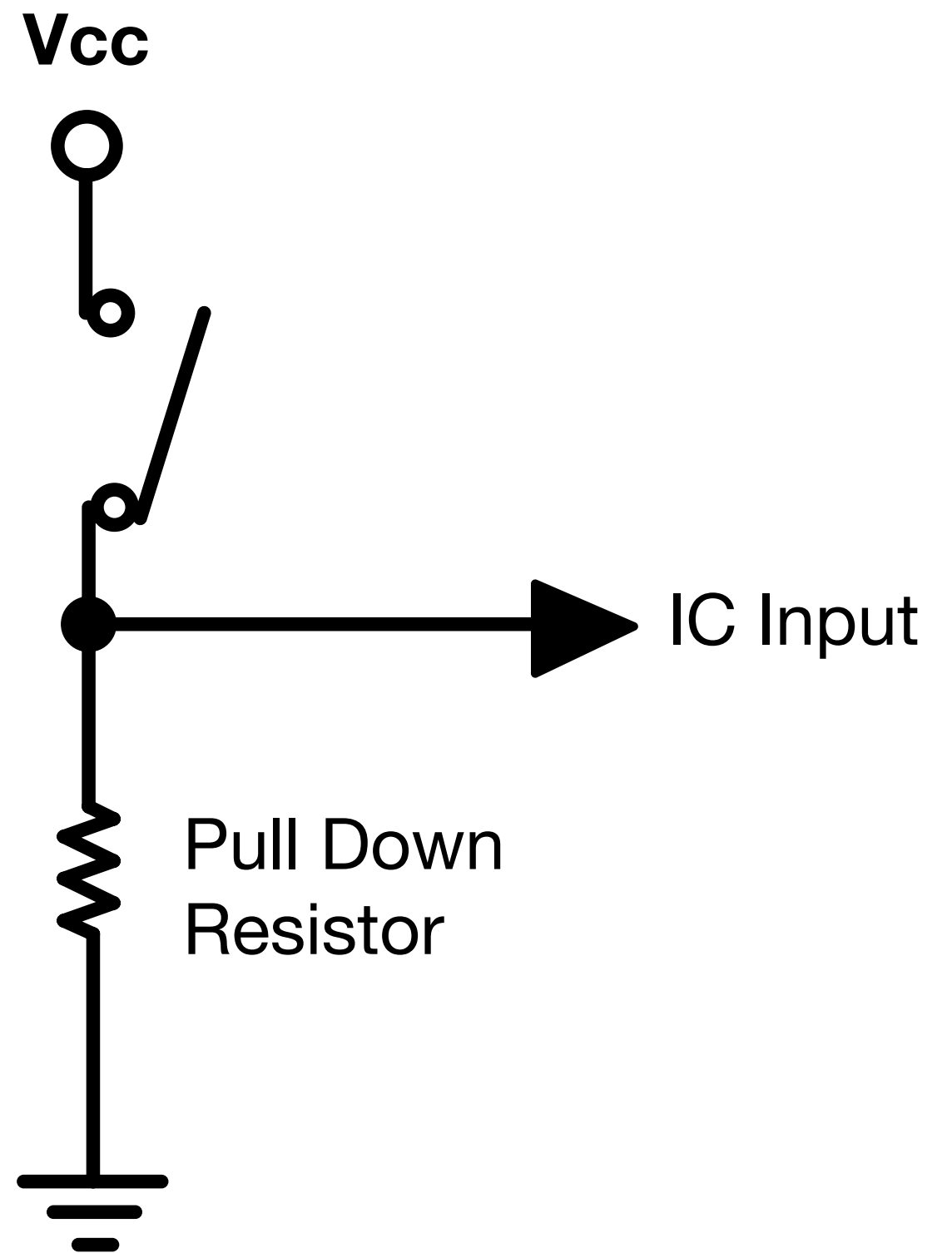
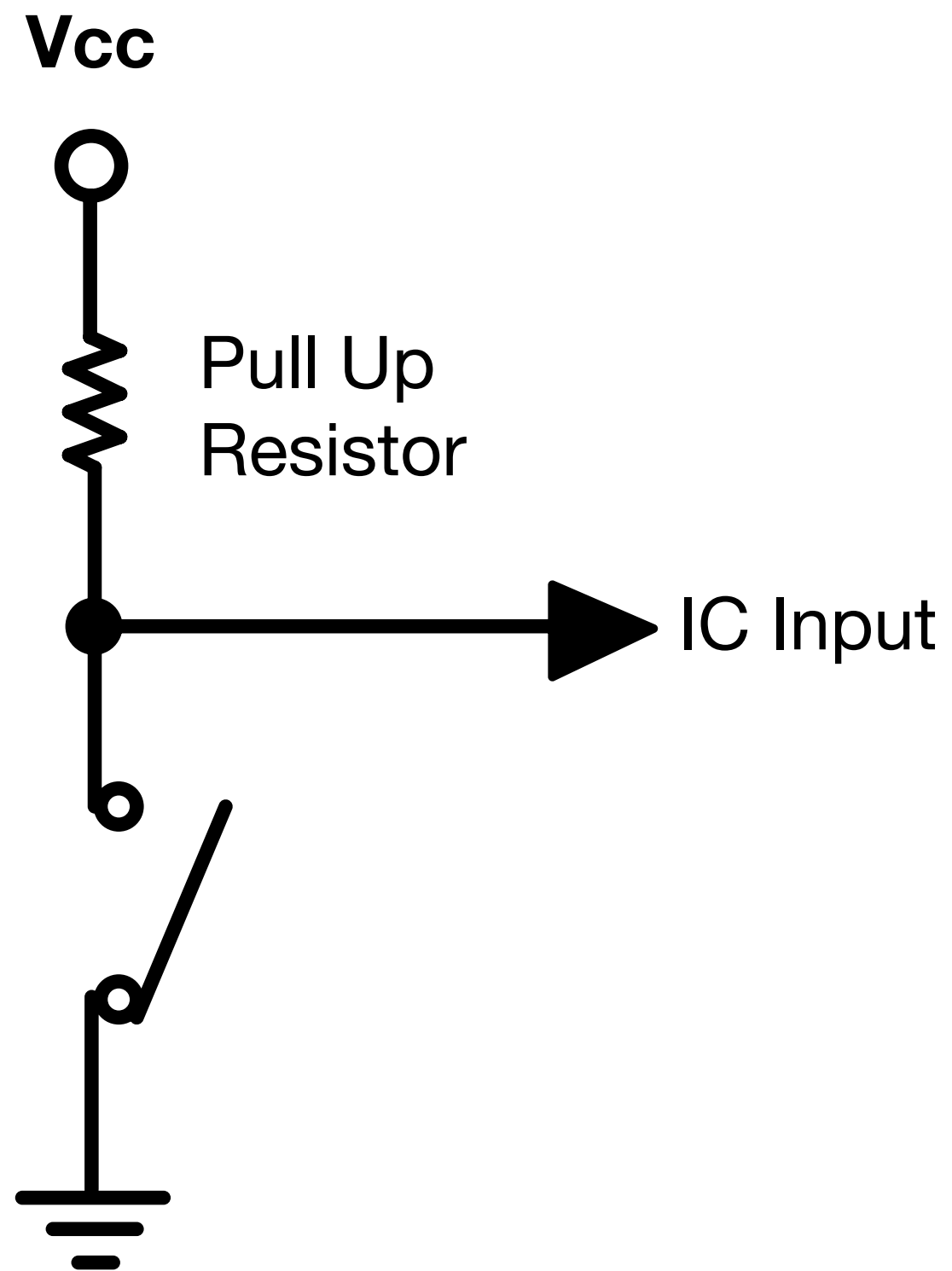


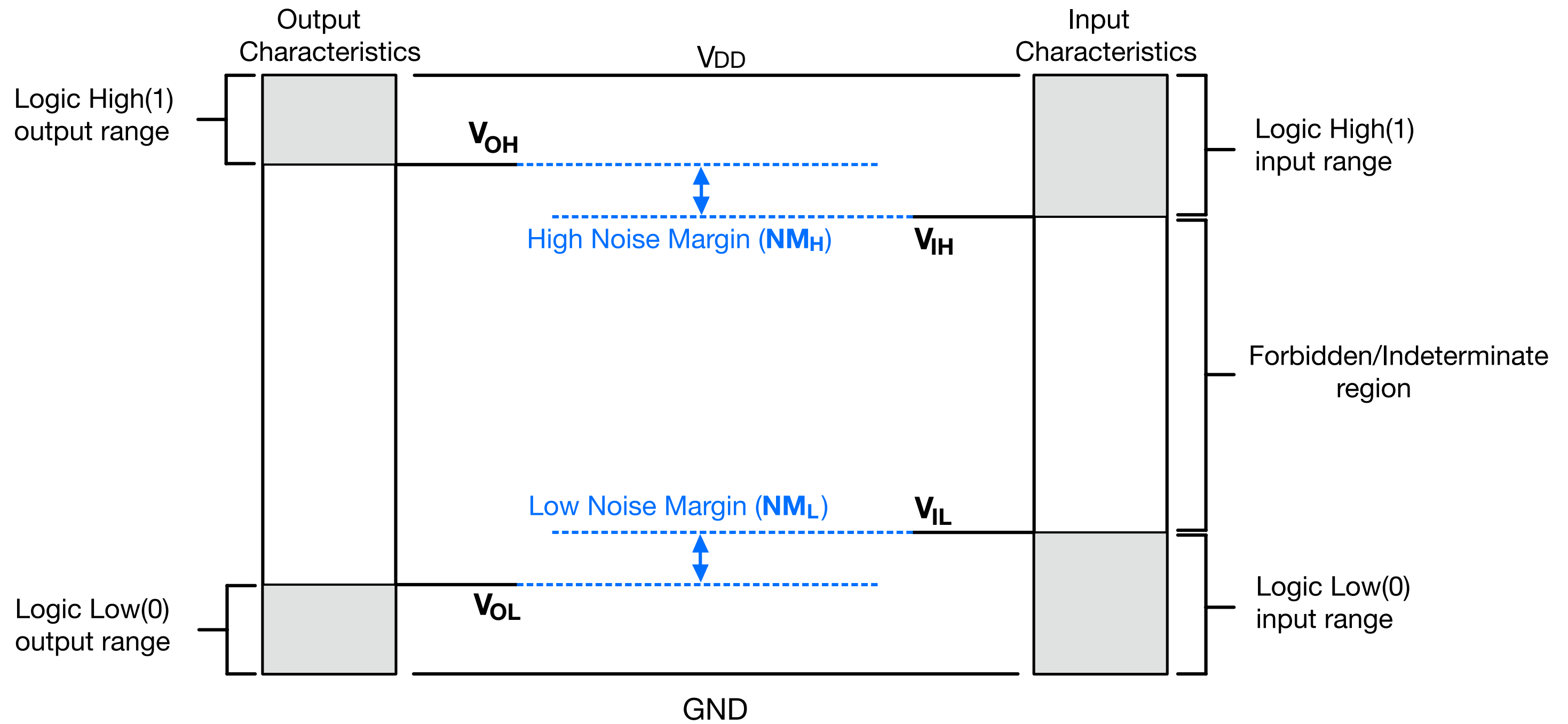
A	B	Q
0	0	1
0	1	0
1	0	0
1	1	0

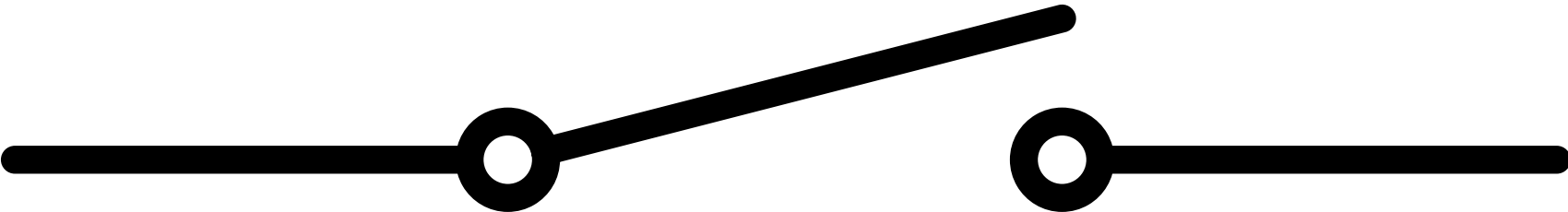
XNOR

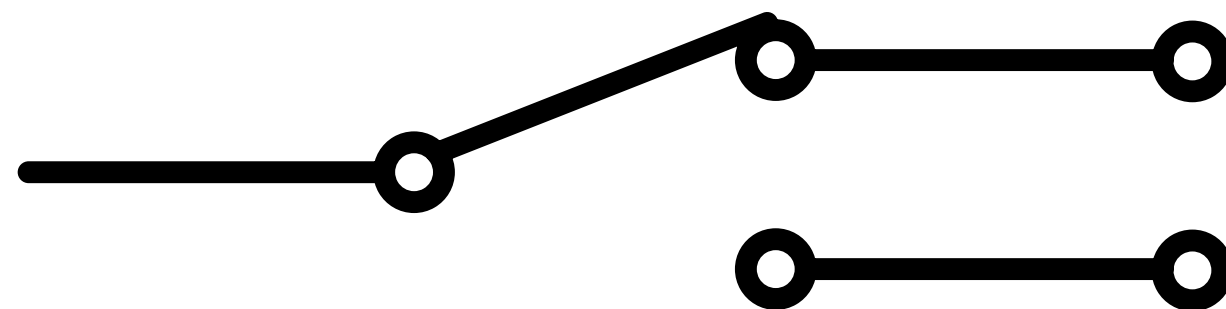
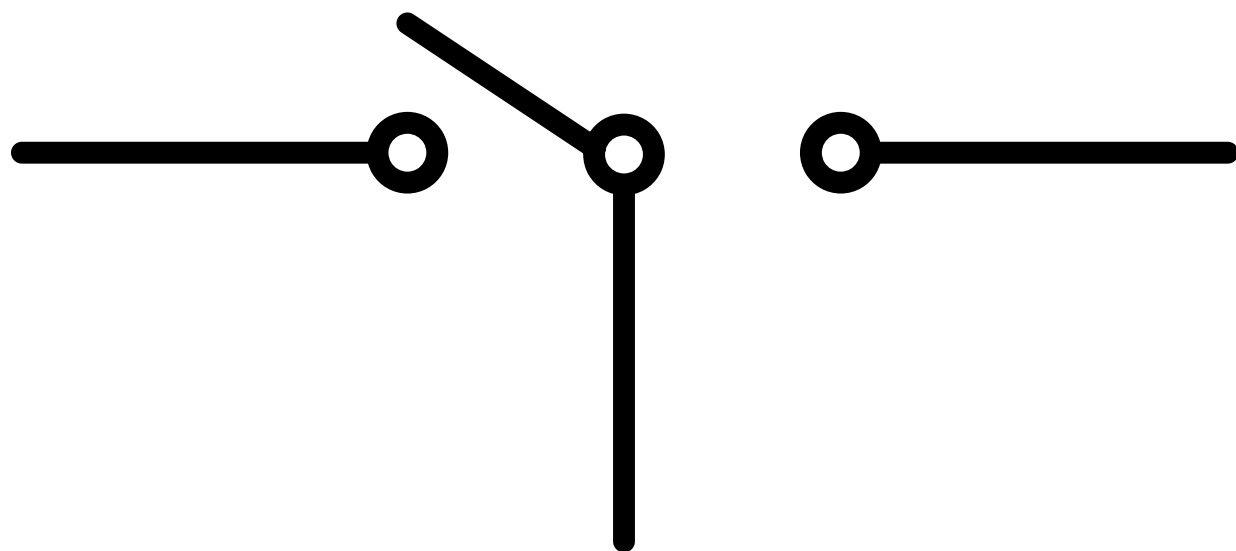


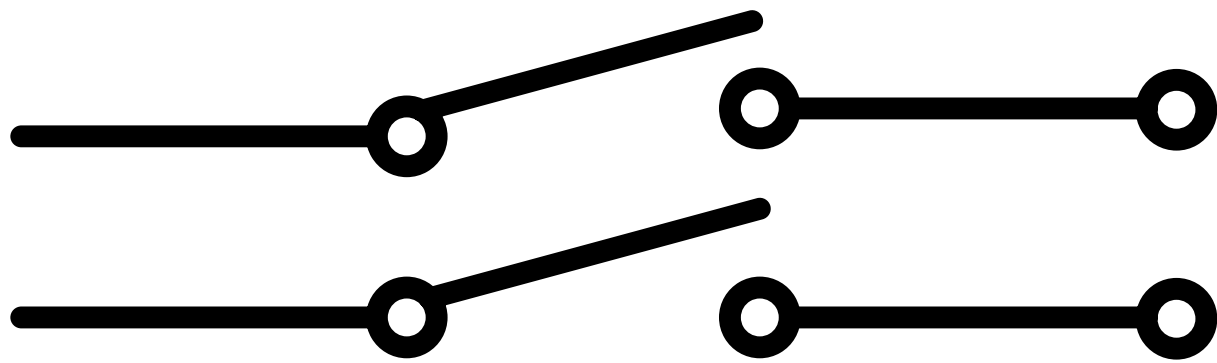
A	B	Q
0	0	1
0	1	0
1	0	0
1	1	1

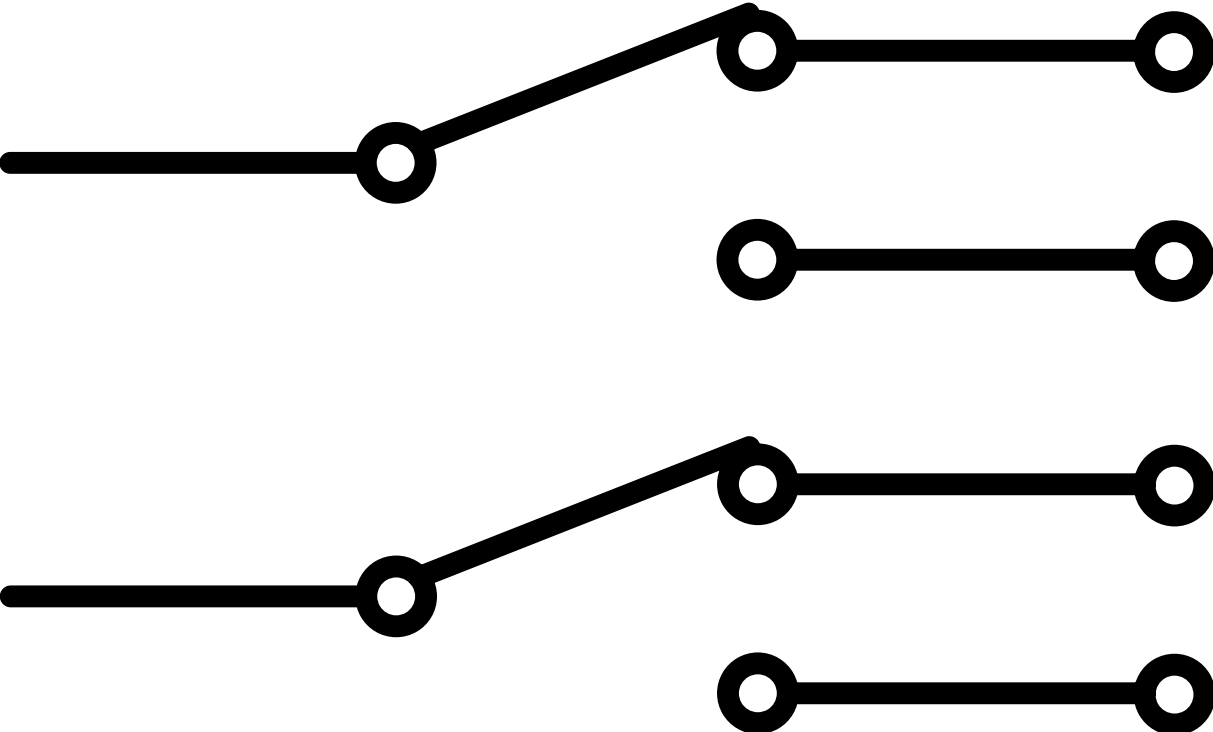
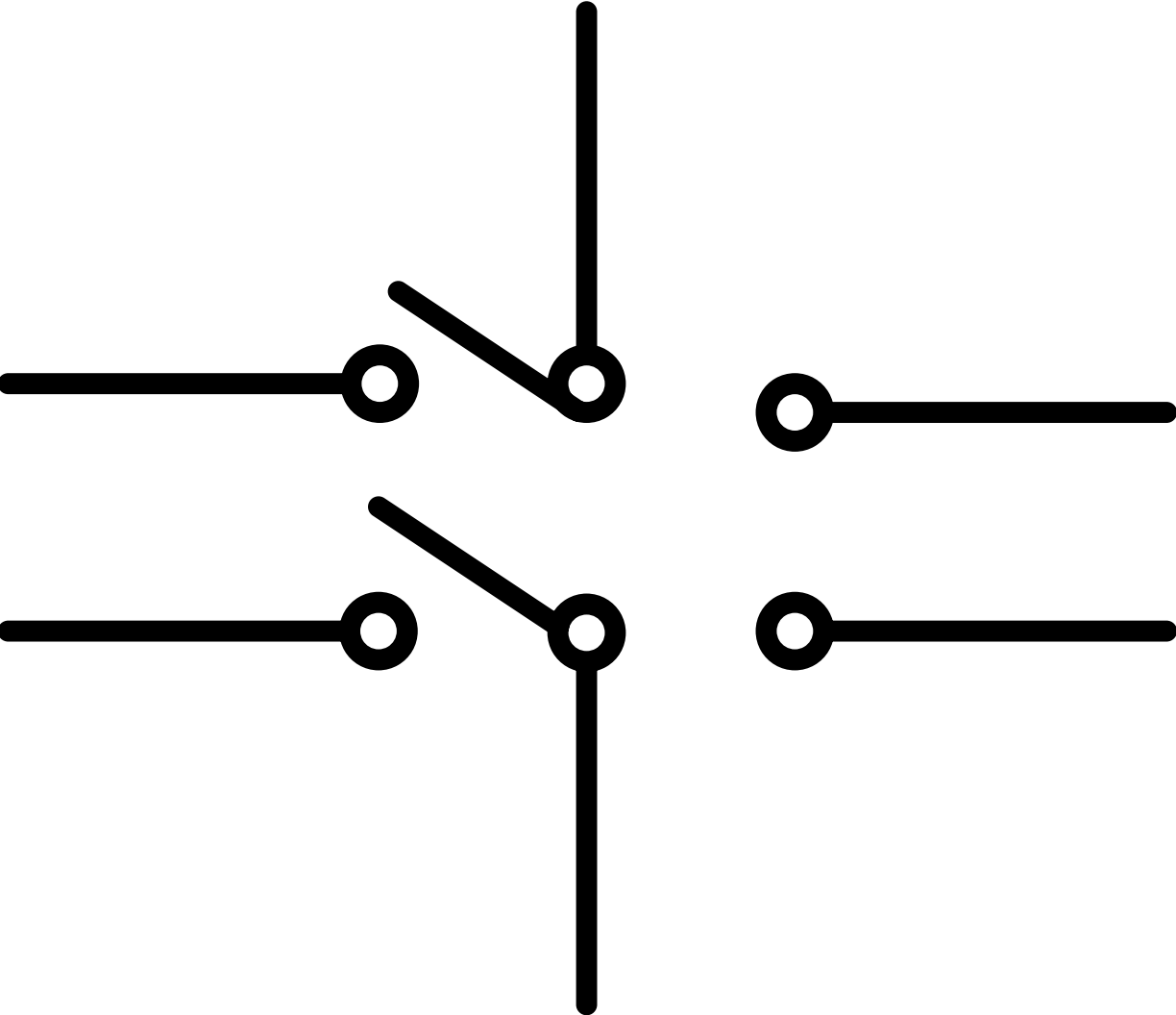


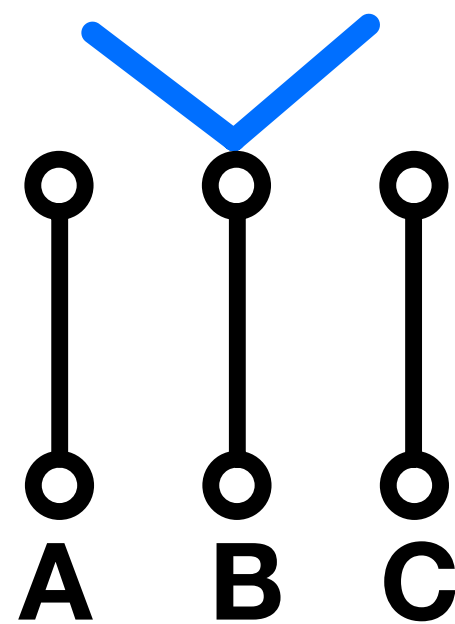
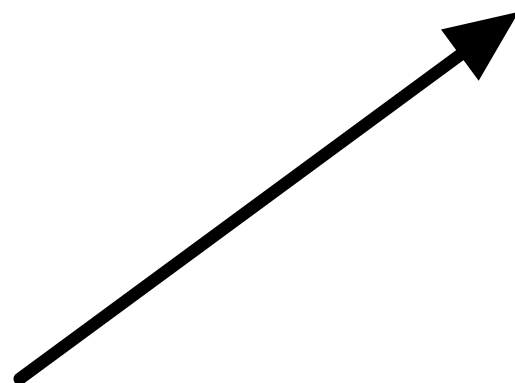
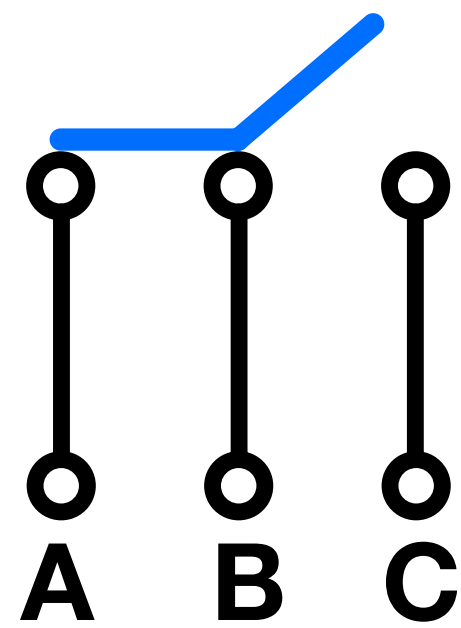






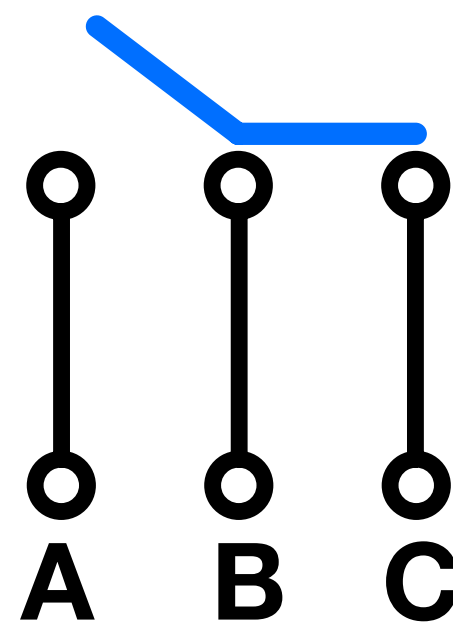




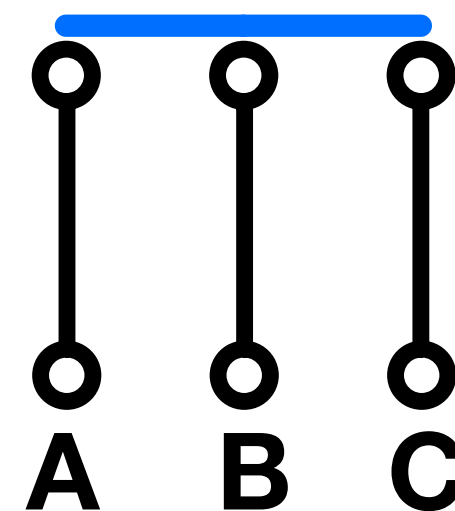
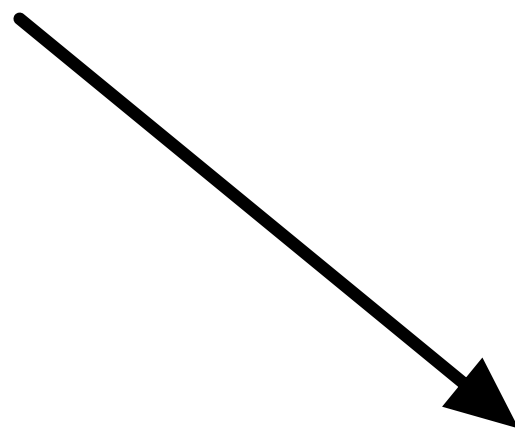


BREAK

BEFORE

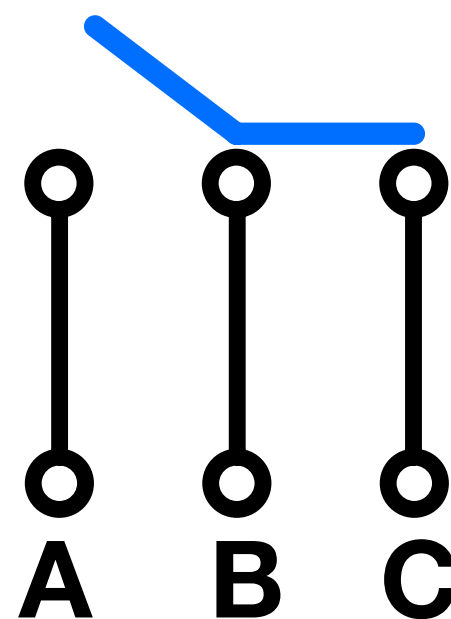


MAKE

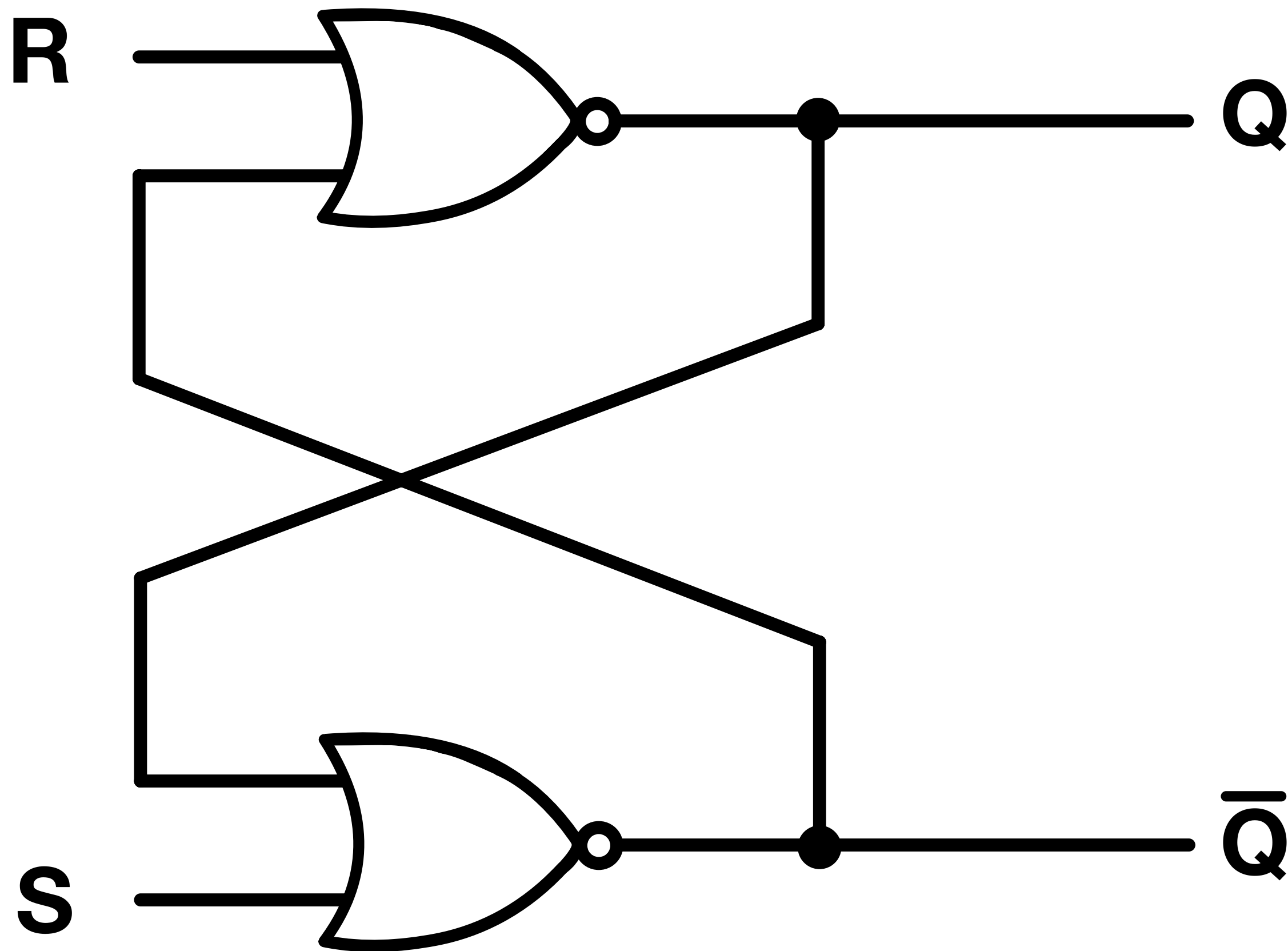


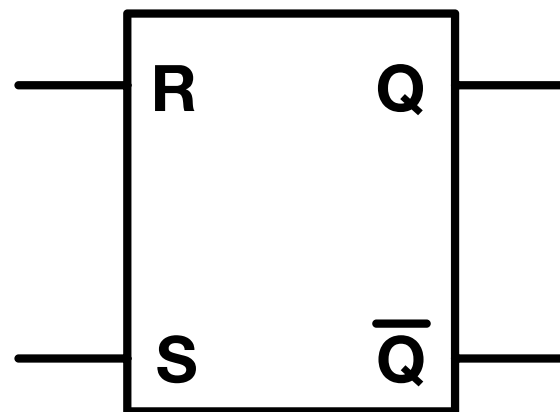
MAKE

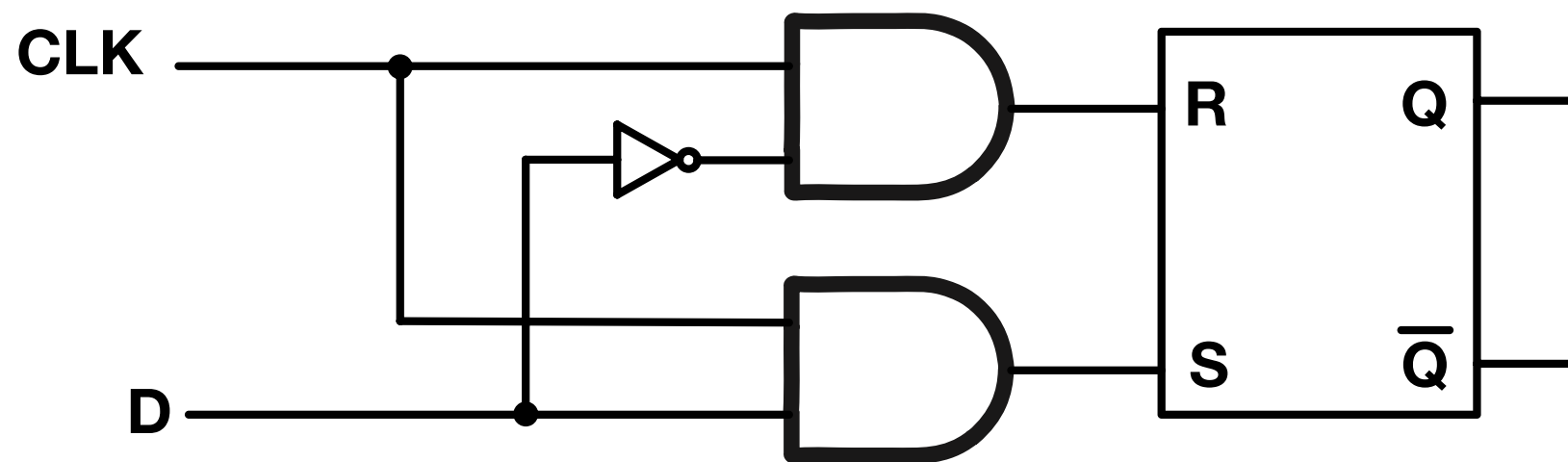
BEFORE

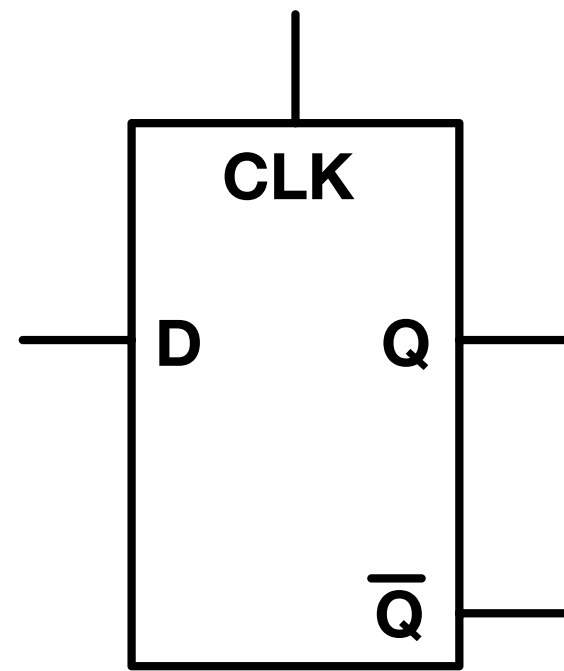


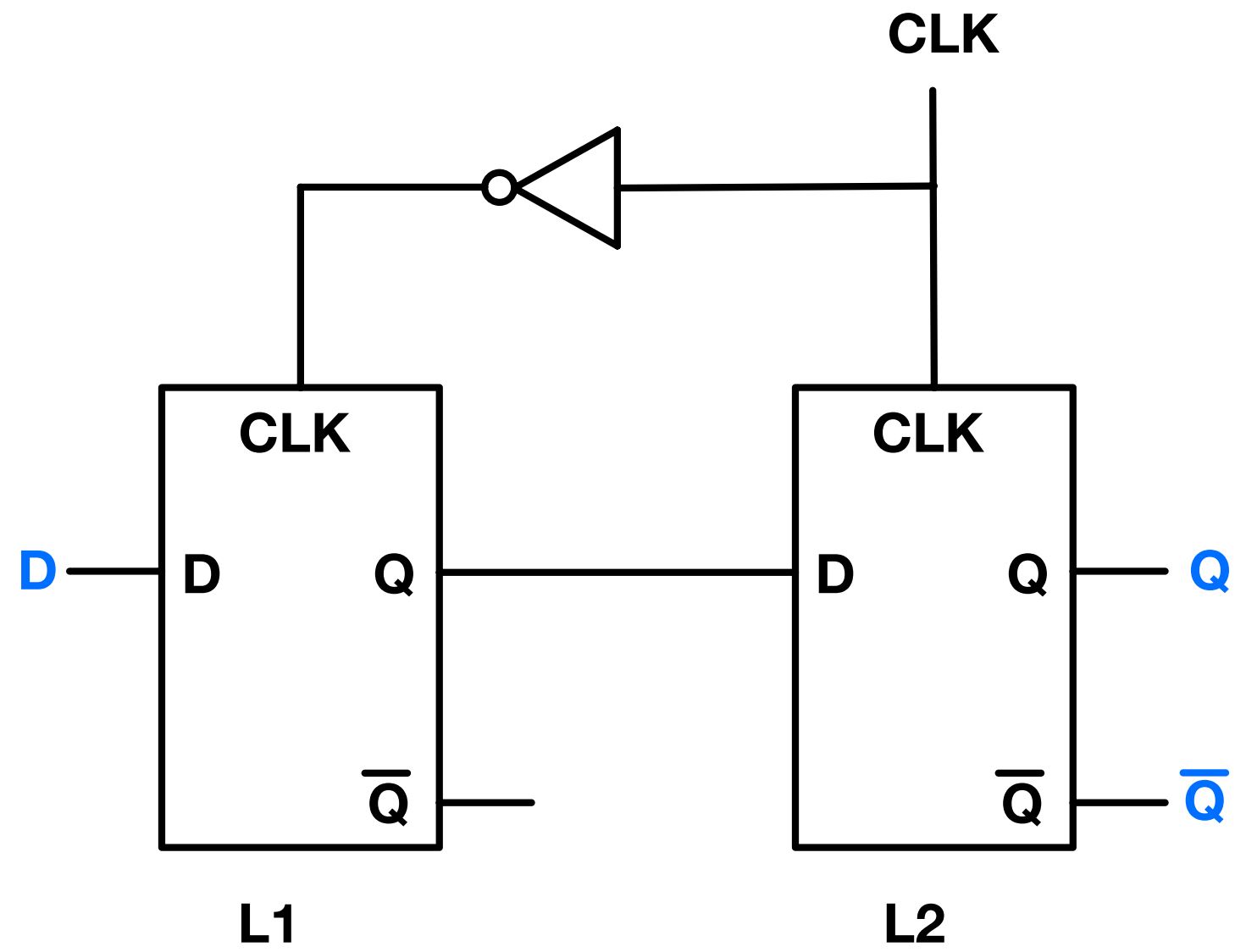
BREAK

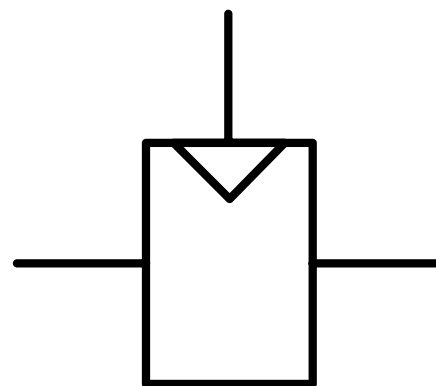
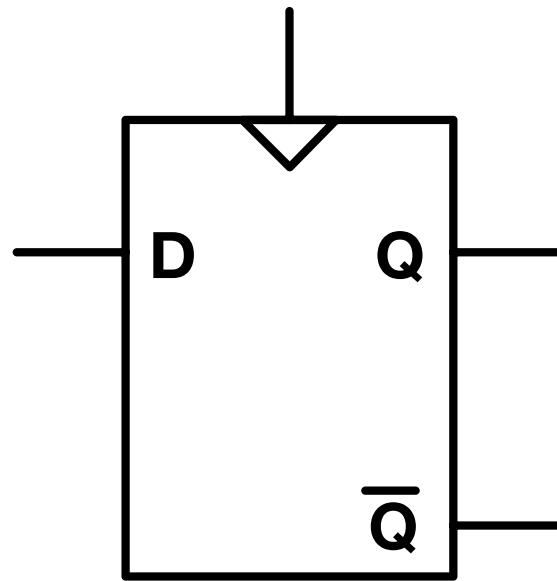


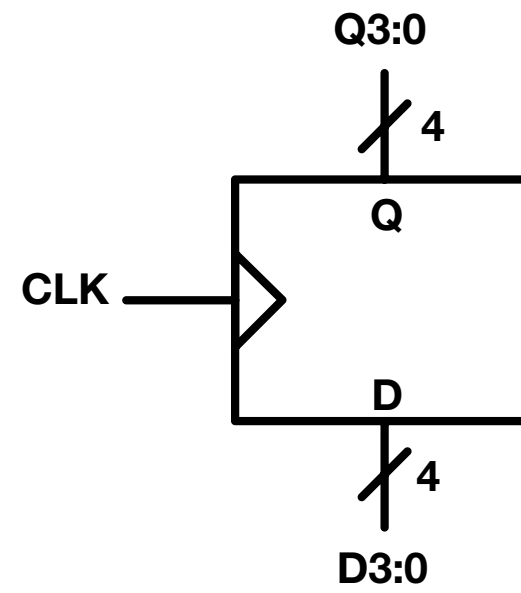
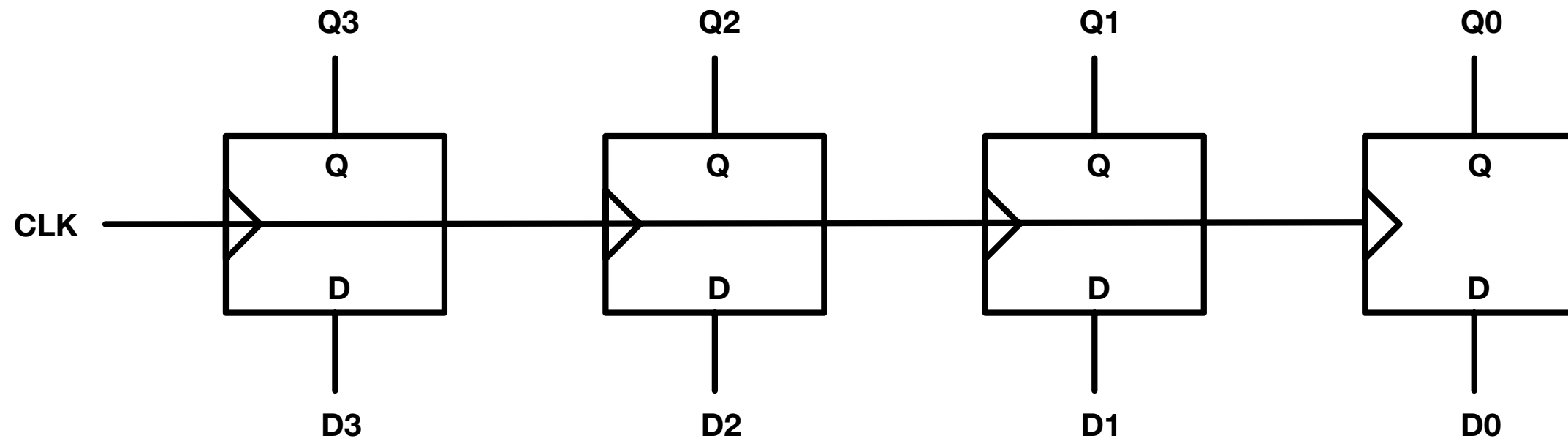












Driver

Receiver

