

وَمَا أُوتِيتُهُ مِنَ الْعِلْمِ إِلَّا قَلِيلًا

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## Analog IC Design

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### Lecture 05 MOSFET Small Signal Model

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Faculty of Engineering  
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## Analog IC Design

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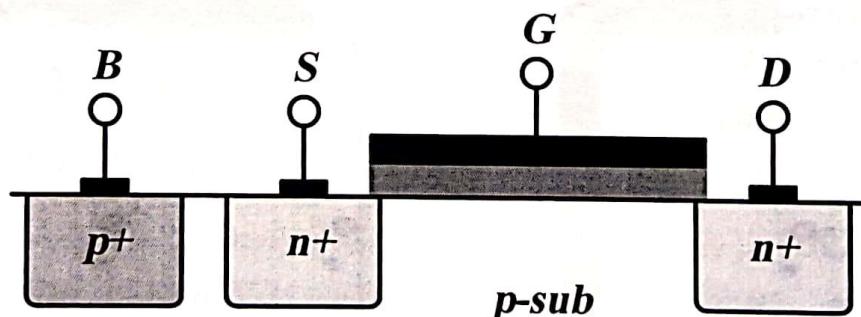
# Outline

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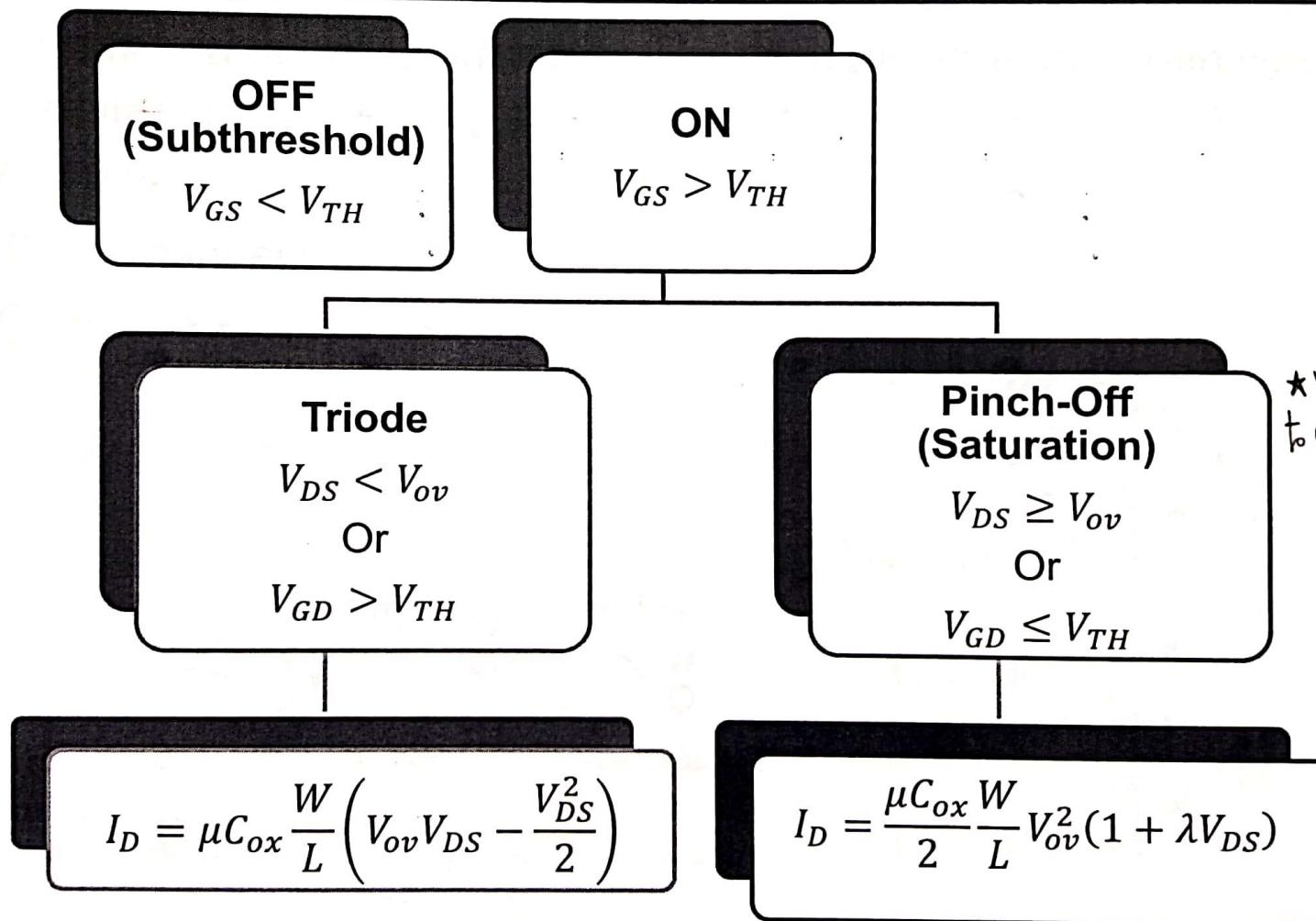
- Recapping previous key results
- The small signal approximation
- The transconductance ( $g_m$ )
- Body effect and body transconductance ( $g_{mb}$ )
- Channel length modulation and output resistance ( $r_o = \frac{1}{g_{ds}}$ )
- Small signal model
- Short channel effects

# N-Channel MOSFET Structure

- MOSFET: Metal-oxide-semiconductor field-effect transistor
- Three-terminal device: Gate (G), Source (S), and Drain (D)
- Substrate/Bulk/Body (S/B) can be treated as a fourth terminal



# Regions of Operation Summary



\* We want to bias the device to work @ Saturation region.

# MOSFET in Saturation

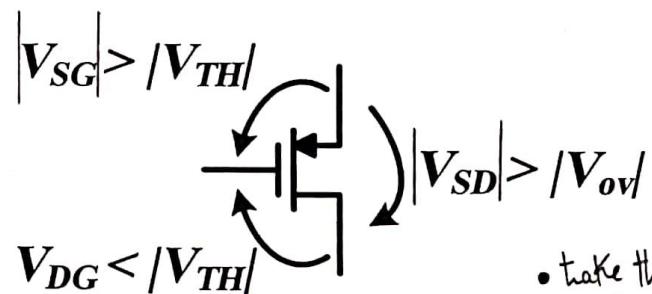
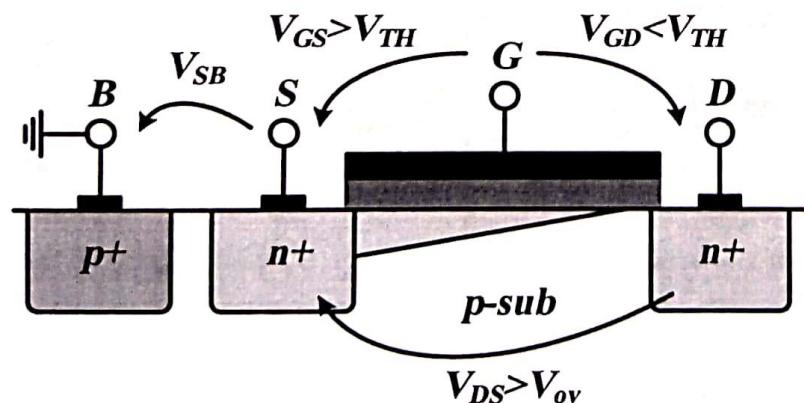
- The channel is pinched off if the difference between the gate and drain voltages is not sufficient to create an inversion layer

$$V_{GD} \leq V_{TH} \text{ OR } V_{DS} \geq V_{ov}$$

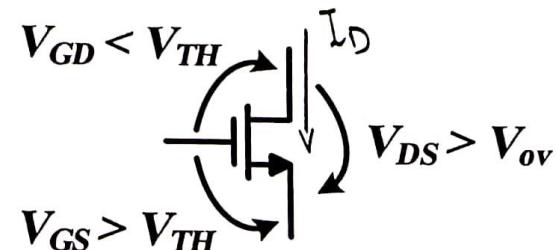
- Square-law (long channel MOS)

$$I_D = \frac{\mu_n C_{ox}}{2} \frac{W}{L} \cdot V_{ov}^2 (1 + \lambda V_{DS})$$

$$V_{SB} \uparrow \Rightarrow V_{TH} \uparrow \text{ (Body Effect)}$$



• take the voltages with the arrow (source).



• We want channel @ the Source  
So,  $V_{GS} > V_{TH}$

• We don't want channel @ the drain  
So,  $V_{GD} < V_{TH}$

# Large Signal Model

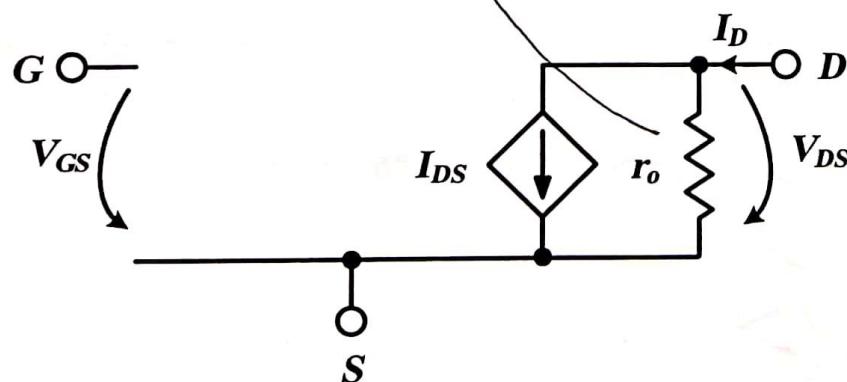
- The channel is pinched off if the difference between the gate and drain voltages is not sufficient to create an inversion layer

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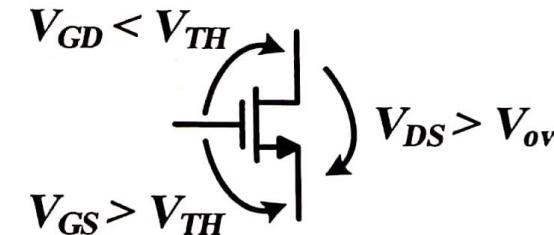
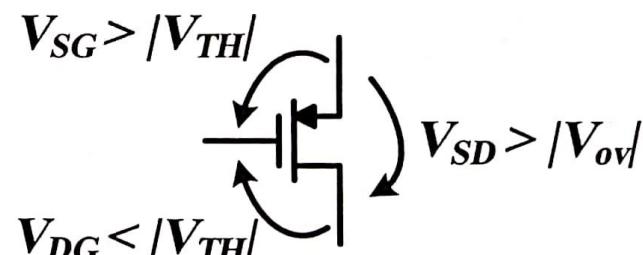
- Square-law (long channel MOS)

$$I_D = \frac{\mu_n C_{ox}}{2} \frac{W}{L} \cdot V_{ov}^2 (1 + \lambda V_{DS})$$

$$V_{SB} \uparrow \Rightarrow V_{TH} \uparrow$$



"non linear VCCS"



# Small Signal Approximation

- The transistor is a VCCS
- Transconductance: how well it converts the voltage to a current  
 $\xrightarrow{\text{Current/Voltage}}$   
 ↳ as if transfer the change from the input (control: volt) to the output (current)

1- in the long channel model ( $I_D - V_{GS}$ )  
 is quadratic characteristics.

2- In Designing amplifiers we have two main steps:

1- Bias Point (Biasing the amplifier)  
 @ Q (DC Current, DC Voltage)

2- We superimposed a small signal ( $v_{GS}$ ) on the DC component ( $V_{GS}$ ).

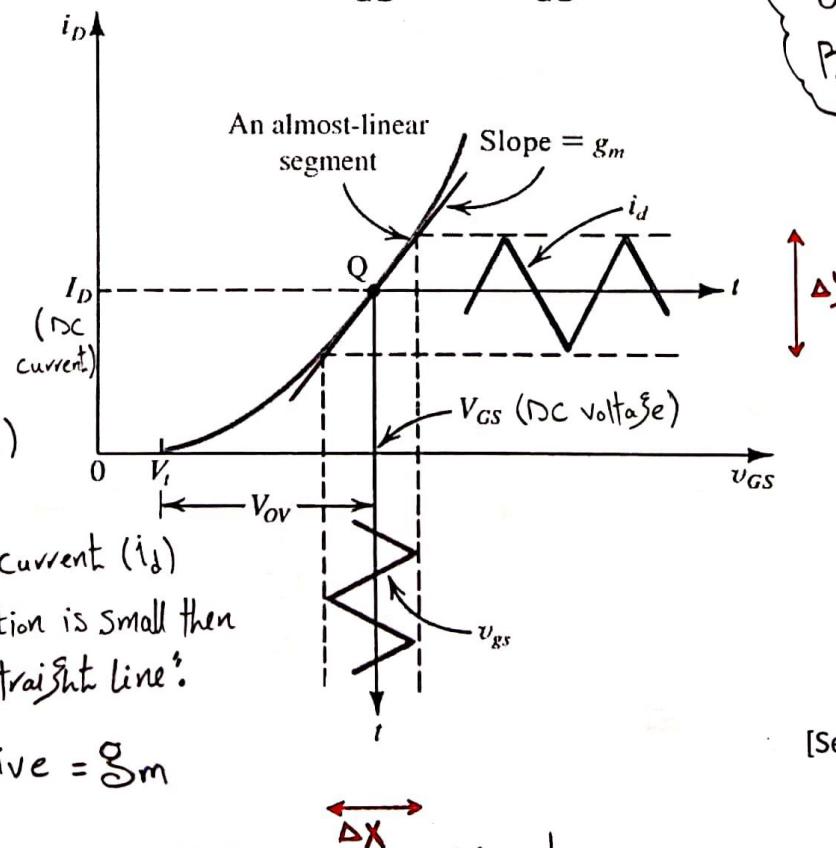
• The small signal or the perturbation ( $\Delta x$ ) will results a perturbation ( $\Delta y$ )

∴ Perturbation in Volt ( $v_{GS}$ ) → Perturbation in current ( $i_d$ )

★ Small Signal approximation: "If the Perturbation is small then you can approximate the non linear curve into straight line":

05: MOSFET AC

$$\therefore \frac{\Delta y}{\Delta x} = \text{Slope} = \text{derivative} = g_m$$



↳ Q: Quiescent operating point  
 Bias Point

★  $g_m$ : is the most important parameter in the transistor as it tells us: How can the transistor converts the input voltage on it into output current.

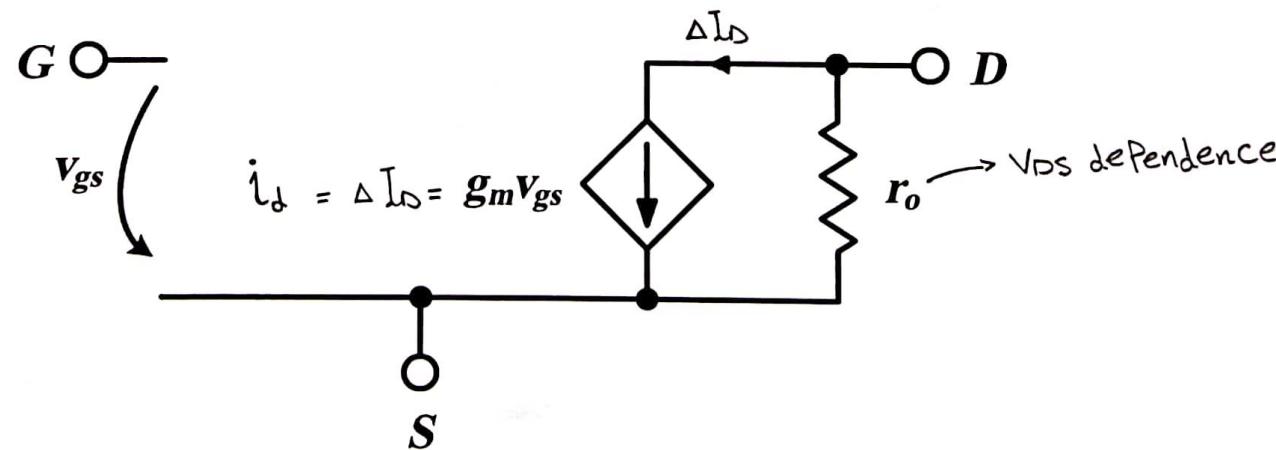
[Sedra/Smith, 2015]

• The region of the slope on the curve is so small but this graph is an exaggeration.

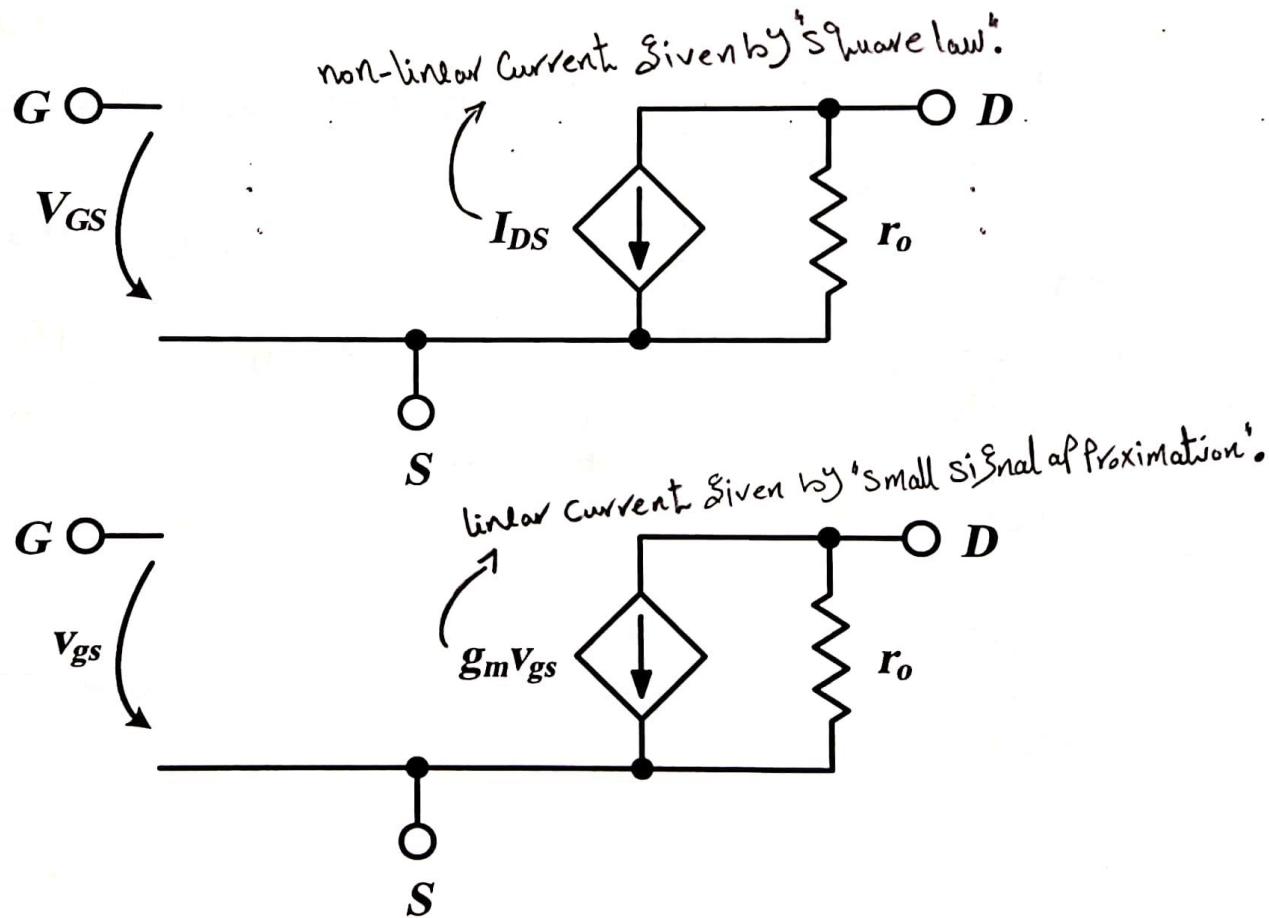
# Small Signal Model

$$g_m = \frac{\Delta I_D}{\Delta V_{GS}} = \frac{\partial I_D}{\partial V_{GS}}$$

$$r_o = \frac{\Delta V_{DS}}{\Delta I_D} = \frac{1}{\frac{\partial I_D}{\partial V_{DS}}}$$



# Large Signal vs Small Signal Model



\* Small Signal Approximation enables us to do the analysis for the amplifiers in an easy way.  
as we linearize the circuit so, it seems like we are solving linear circuit  
with only linear elements.

# Transconductance

- The transistor is a VCCS
- Transconductance: how well it converts the voltage to a current

$$I_D \approx \frac{\mu_n C_{ox} W}{2} \frac{1}{L} \cdot V_{ov}^2 \quad \rightarrow ①$$

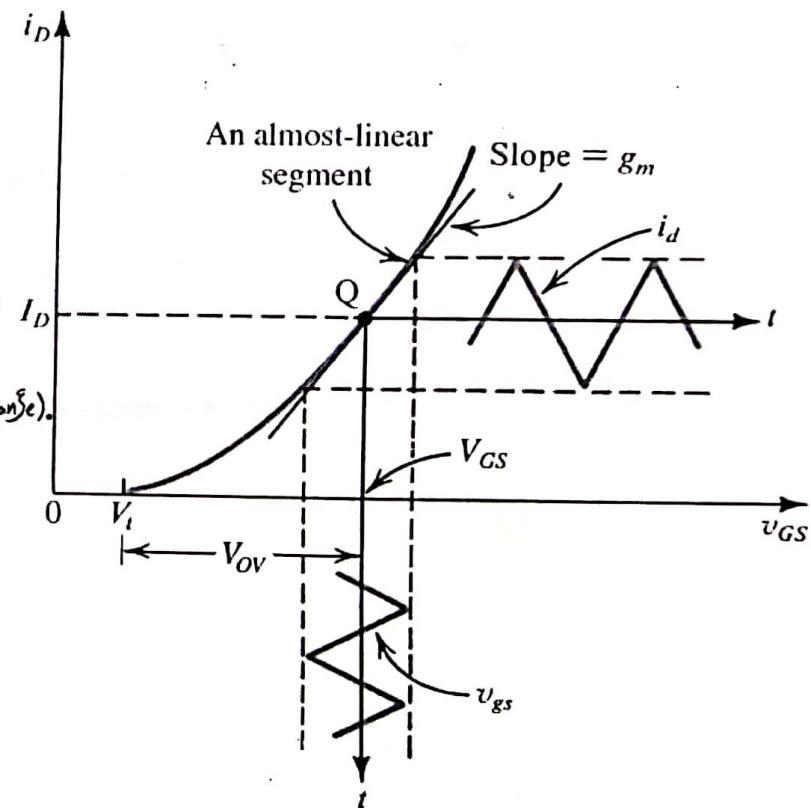
$$g_m = \frac{\Delta I_D}{\Delta V_{GS}} = \frac{\partial I_D}{\partial V_{GS}} = \frac{\partial I_D}{\partial V_{ov}}$$

to Derivate w.r.t.  $V_{GS}$   
equals to derivate w.r.t.  $V_{ov}$   
as the difference between  
them is  $V_{th}$  and we suppose  
that  $V_{th}$  is const. (won't change).

$$= \left( \mu C_{ox} \frac{W}{L} V_{ov} \right) \text{ Sub.b)} V_{ov} \text{ from } ①$$

$$= \sqrt{\mu C_{ox} \frac{W}{L} \cdot 2 I_D}$$

$$= \frac{2 I_D}{V_{ov}}$$



# Transconductance

$$I_D \approx \frac{\mu_n C_{ox}}{2} \frac{W}{L} \cdot V_{ov}^2$$
$$g_m = \frac{\partial I_D}{\partial V_{GS}} = \left( \mu C_{ox} \frac{W}{L} V_{ov} \right) = \sqrt{\mu C_{ox} \frac{W}{L} \cdot 2 I_D} = \frac{2 I_D}{V_{ov}}$$

measuring device in the lab

**$W/L$  constant**

$$g_m \propto V_{ov}$$

$$g_m \propto \sqrt{I_D}$$

Design with specific  $V_{ov}$

**$V_{ov}$  constant**

$$g_m \propto W/L$$

$$g_m \propto I_D$$

Design with specific  $I_D$

**$I_D$  constant**

$$g_m \propto \sqrt{W/L}$$

$$g_m \propto 1/V_{ov}$$

Limited with current budget

Measuring

Designing

# Body Effect

- $V_{SB}$  affects the charge required to invert the channel

- Increasing  $V_S$  or decreasing  $V_B$  increases  $V_{TH}$

$$V_{TH} = V_{TH0} + \gamma \left( \sqrt{2\Phi_F + V_{SB}} - \sqrt{|2\Phi_F|} \right)$$

- $\Phi_F$  = surface potential at threshold

- Depends on doping level and intrinsic carrier concentration  $n_i$

\* Note: Prof. Ali Hajimiri (lecture 116N)

MOSFET has a fourth terminal which is influencing the drain current ' $I_D$ ', But how?

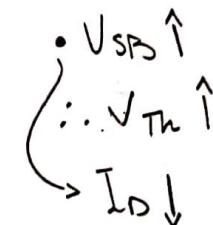
$$\star I_D = \frac{\mu_n C_{ox}}{2} \cdot \frac{W}{L} (V_{GS} - V_T)^2 (1 + \lambda V_{DS})$$

Here,  $\bullet V_T = V_{TH0} + \gamma (\sqrt{2\Phi_F + V_{SB}} - \sqrt{|2\Phi_F|})$

$$\bullet S_{mb} = \frac{\partial I_D}{\partial V_{BS}} = -\mu_n C_{ox} \frac{W}{L} (V_{GS} - V_T) \cdot \frac{\partial V_T}{\partial V_{BS}} = \chi S_m$$

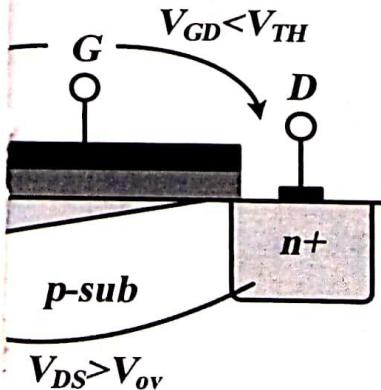
$$-\frac{\partial V_T}{\partial V_{BS}} = \frac{\chi}{2\sqrt{2\Phi_F + V_{SB}}} = \chi (\text{Chi}) = \frac{C_{js}}{C_{ox}} = \frac{S_{mb}}{S_m}$$

$\bullet \chi$ : is the rate of change of threshold voltage with body bias voltage. (Grey & Meyer)  
 \* It's more of a 'nuisance' most of the time, there are some topologies where ( $S_{mb}$ ) is useful but in more scenarios it is nuisance.



$\therefore V_{SB}$  as well as the Gate changes the current

$$\begin{aligned} \Delta V_{GS} &\xrightarrow{S_m} \Delta I_D \\ \Delta V_{BS} &\xrightarrow{S_{mb}} \Delta I_D \end{aligned}$$



$C_{js}$ : is the capacitance per unit area of the depletion region under the channel.

# Body Effect

- $V_{SB}$  affects the charge required to invert the channel

- Increasing  $V_S$  or decreasing  $V_B$  increases  $V_{TH}$

$$V_{TH} = V_{TH0} + \gamma \left( \sqrt{2\Phi_F + V_{SB}} - \sqrt{|2\Phi_F|} \right)$$

- $\Phi_F$  = surface potential at threshold

- Depends on doping level and intrinsic carrier concentration  $n_i$

- $\gamma$  = body effect coefficient

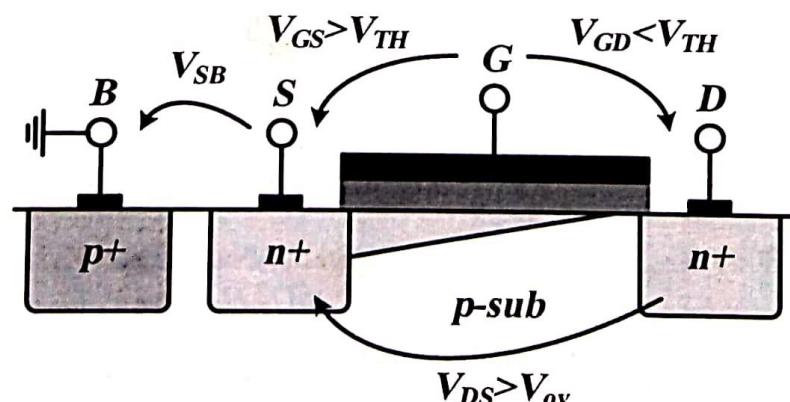
- Depends on  $C_{ox}$  and doping

•  $V_{SB} \uparrow$   
 $\therefore V_{Th} \uparrow$   
 $\rightarrow I_D \downarrow$

$\therefore V_{SB}$  as well as the Gate changes the current

$\Delta V_{GS} \xrightarrow{\xi_m} \Delta I_D$   
 $\Delta V_{BS} \xrightarrow{\xi_{m_b}} \Delta I_D$

• Like the top gate change  
 the current, the body can be  
 treated as the bottom gate which  
 also changes the current.



★ Note : Prof. Ali Hajimiri (lecture 116N)

- MOSFT has a fourth terminal which is influencing the drain current 'ID'. But how?

$$★ ID = \frac{\mu_n C_{ox}}{2} \cdot \frac{W}{L} \underbrace{(V_{GS} - V_T)^2}_{\text{---}} (1 + \lambda V_{DS})$$

Here,  $V_{TH} = V_{TH_0} + \gamma (\sqrt{2\phi_f + V_{SB}} - \sqrt{2\phi_f})$

$$• S_{mb} = \frac{\partial I_D}{\partial V_{BS}} = \overline{\mu_n C_{ox}} \frac{W}{L} (V_{GS} - V_T) \cdot \frac{\partial V_T}{\partial V_{BS}} = \chi S_m$$

$$- \frac{\partial V_T}{\partial V_{BS}} = \frac{\gamma}{2\sqrt{2\phi_f + V_{SB}}} = \chi (\text{chi}) = \frac{C_{js}}{C_{ox}} = \frac{S_{mb}}{S_m}$$

$C_{js}$  : is the capacitance per unit area of the depletion region under the channel.

- $\chi$  : is the rate of change of threshold voltage with body bias voltage. (Grey & Meyer)
- ★ It's more of a "nuisance" most of the time, there are some topologies where ( $S_{mb}$ ) is useful but in more scenarios it is nuisance.

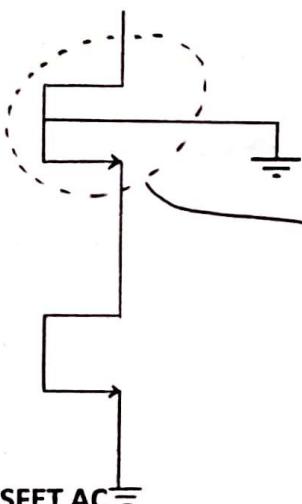
# Bulk Transconductance

- The bulk behaves as a second gate that changes the output current

$$\frac{\delta m}{\delta V_{GS}} = \frac{\Delta I_D}{\Delta V_{GS}}$$

$$\frac{\delta m_b}{\delta V_B} < \frac{\delta m}{\delta V_G}$$

20% - 25%

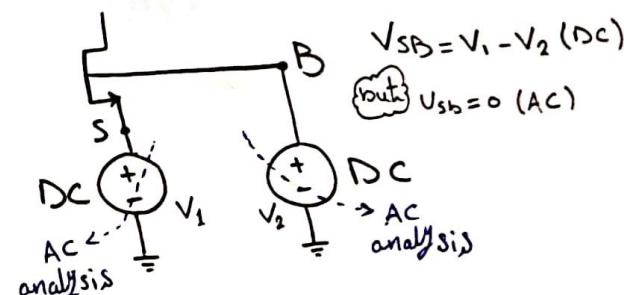


We should take into consideration the Body Transconductance while dealing with this MOSFET.  
as  $V_s$  &  $V_b$  have different values.

$$g_{mb} = \frac{\partial I_D}{\partial V_{BS}} = \eta g_m$$

$\eta$  is typically 0.1 → 0.25  
 $\eta$ : eta

- We only take into consideration the Body effect if:  $V_{BS}$  has a value.
- If Body & Source are "Ac short circuit" we don't care of  $\delta m_b$



$$\star \Delta I_D = \delta m_b \cdot \Delta V_{BS}$$

- If  $\delta m_b$  has a value (like in cadence) doesn't mean  $\Delta I_D$  have a value unless  $\Delta V_{BS} \neq 0$ .
- ↓  
★ Doesn't mean there is body effect.

# Channel Length Modulation (CLM)

- The VCCS is not ideal: There is some dependence on  $V_{DS}$

$$r_o = \frac{\Delta V_{DS}}{\Delta I_D} = \frac{1}{\partial I_D / \partial V_{DS}} = \frac{1}{g_{ds}} = \frac{V_A}{I_{DS}} = \frac{1}{\lambda I_{DS}}$$

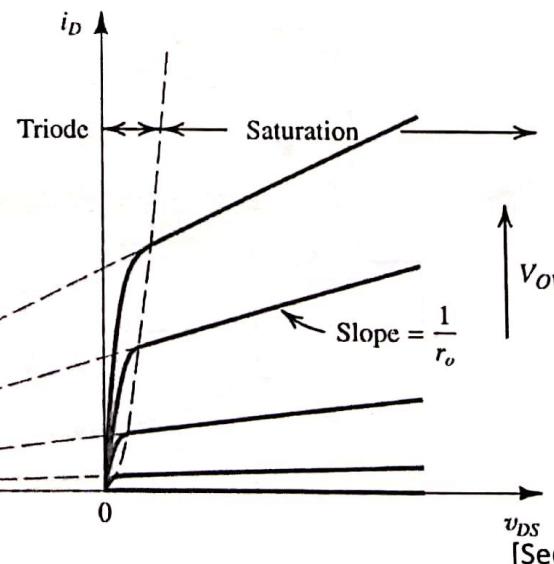
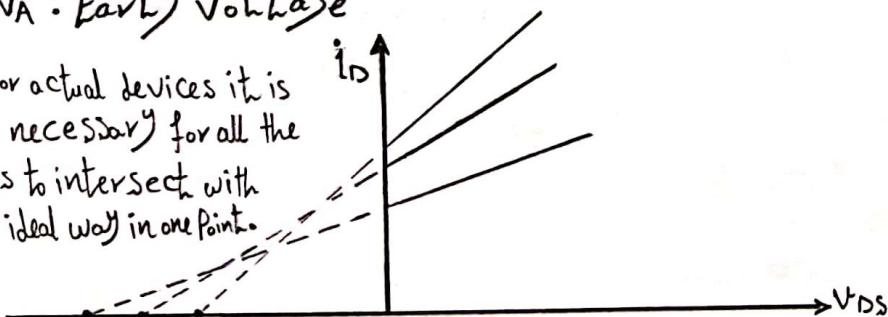
Ohm's Law

$V_A$ : Early voltage ( $V_A \propto L$ )  $\leftrightarrow \lambda$ : Channel length modulation coefficient ( $\lambda \propto 1/L$ )

$$I_D = I_{DS} + \frac{V_{DS}}{r_o} = I_{DS} \left( 1 + \frac{V_{DS}/I_{DS}}{r_o} \right) = \frac{\mu C_{ox} W}{2 L} V_{ov}^2 (1 + \lambda V_{DS})$$

\*  $V_A$ : Early voltage

- for actual devices it is not necessary for all the lines to intersect with this ideal way in one point.



[Sedra/Smith, 2015]

• Early Voltage can be a function of current.

•  $V_A$  is a Complex Parameter used to describe how the current depends on the

$$-V_A = -1/\lambda$$

05: MOSFET AC drain Voltage

• Due to many physical effect like: channel length modulation & DIBL

• We can also describe this effect with  $\lambda$  which equals  $1/V_A$

\* Remember that:  $V_A \propto L$  so, By increasing  $L \therefore V_A \uparrow$  &  $r_o \uparrow$

$\therefore$  You don't have to understand the physical reasons which explain the I-V char. in all cases as it will be hard in some cases and confusing and will consume time.

\* Briefly the contract between the device engineer & the circuit designer is the 'device Model'.  
designer the slope of ( $I_D - V_{DS}$ ) If you have accurate device model and you have I-V char. then you can proceed in the design process without understanding the physical reason.

$\therefore$  for you as a circuit designer the slope of ( $I_D - V_{DS}$ ) is  $\frac{1}{r_o}$ , also the slope of ( $I_D - V_{GS}$ ) is  $\lambda$ , it doesn't matter whether pinch-off, velocity sat., mobility degradation.

$\therefore$  Actually as an Analog IC designer you are concerned with the I-V characteristics of the device. If you have the I-V ch. then you can use it to design a specific circuit with specific specs.

# Channel Length Modulation (CLM)

- $L_{eff}$  decreases with  $V_{DS} \rightarrow$  Shorter L gives more current
- $V_A$ : Early voltage ( $V_A \propto L$ )
- $\lambda$ : Channel length modulation coefficient ( $\lambda \propto 1/L$ )

$$I_D = \frac{\mu C_{ox} W}{2} V_{ov}^2 (1 + \lambda V_{DS})$$

$$r_o = \frac{V_A}{I_{DS}} = \frac{1}{\lambda I_{DS}}$$

- $V_A$  increases with  $V_{DS}$ : higher  $r_o$  as we go deeper into saturation

$\therefore$  as we model ' $r_o$ ' by the parameter which is called the early voltage ' $V_A$ '  
 $\therefore V_A$  is  $f(V_{DS})$

$V_{DS} \uparrow \therefore V_A \uparrow \therefore r_o \uparrow$

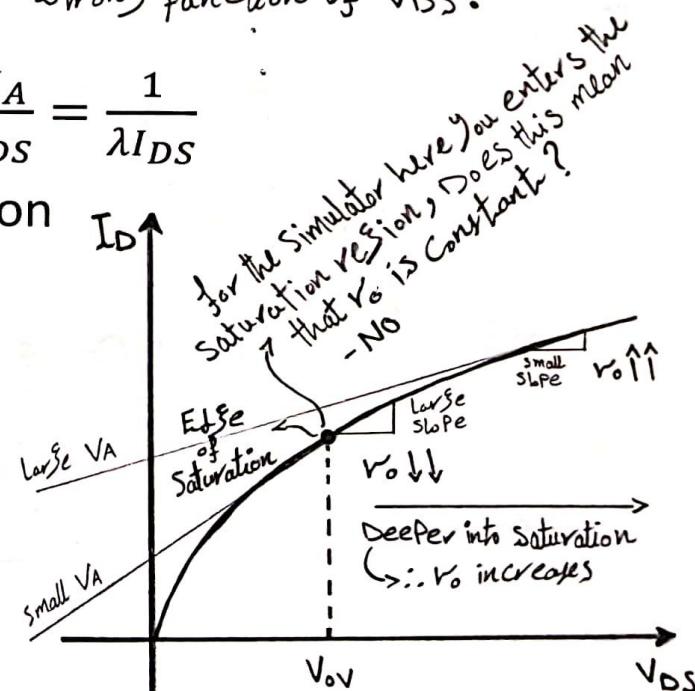
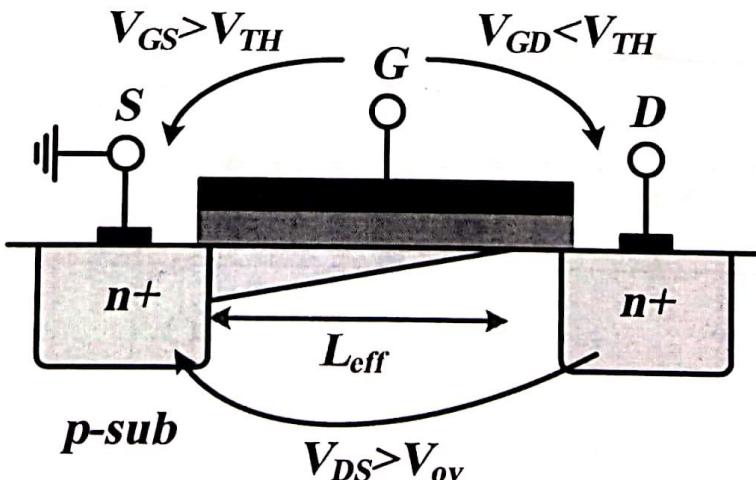
$\therefore$  Physicists explain this with:

- when Pinch-off occurs the tendency of movement of the Pinch-off Point

05: MOSFET AC (when we increase the drain voltage)

is more than after while

\* after the pinch-off has moved the rate of the movement decreases (i.e. the amount of its movement with the increase of  $V_{DS}$ ) most of  $V_{DS}$  lies on the high resistance depletion region. That means you weaken the CLM. (weaken CLM means the change in  $L_{eff}$  is less, means the change in  $I_D$  is less, means high  $r_o$ )



• increasing  $r_o$  is a good thing for analog design but increasing  $V_{DS}$  on this device costs me in signal swing as we will see

We will, when we study frequency response.

We didn't put into consideration the effect of the capacitors yet.

# Low-Frequency Small-Signal Model

- $\hat{g}_m (V_{GS} \rightarrow V_{BS})$

the two currents will be the same direction.

- But you say before that  $V_{SB}$  is +ve so,  $V_{BS}$  should be -ve?  
- Yes, but this is the large signal

• if  $V_{SB} = 0.5$  V

and a perturbation occurs with  $\pm 10$  mV

is the sign of the AC signal important? - No

(it is a perturbation on the large signal.)

$$g_m = \frac{\partial I_D}{\partial V_{GS}} = \mu C_{ox} \frac{W}{L} V_{ov} = \sqrt{\mu C_{ox} \frac{W}{L} \cdot 2I_D} = \frac{2I_D}{V_{ov}}$$

$$g_{mb} = \eta g_m$$

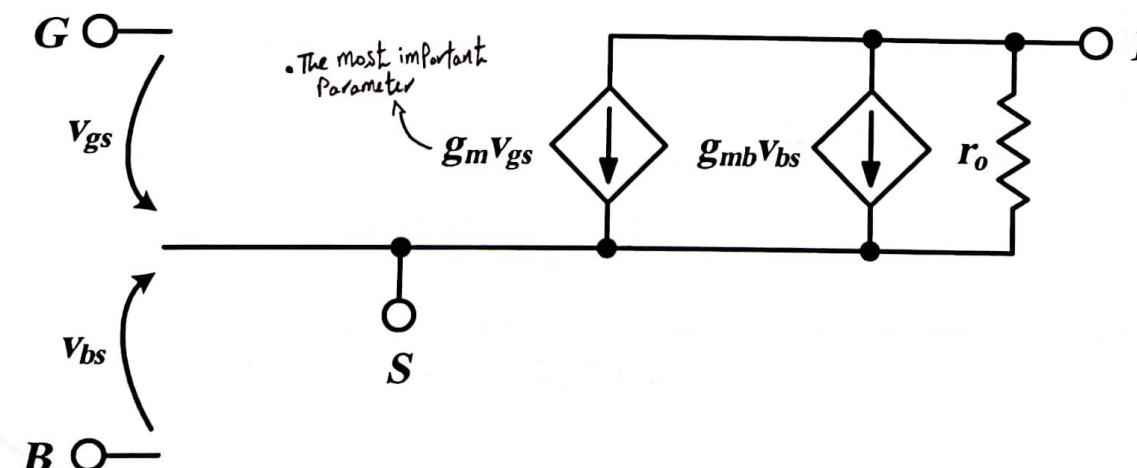
$$\eta \approx 0.1 - 0.25$$

$$r_o = \frac{1}{\partial I_D / \partial V_{DS}} = \frac{V_A}{I_D} = \frac{1}{\lambda I_D}$$

$$V_A \propto L \leftrightarrow \lambda \propto \frac{1}{L}$$

If you bias @ the edge of saturation  
you'll have small  $V_A$  : Small  $r_o$   
∴ it's not good to bias the device  
@ the edge of saturation.

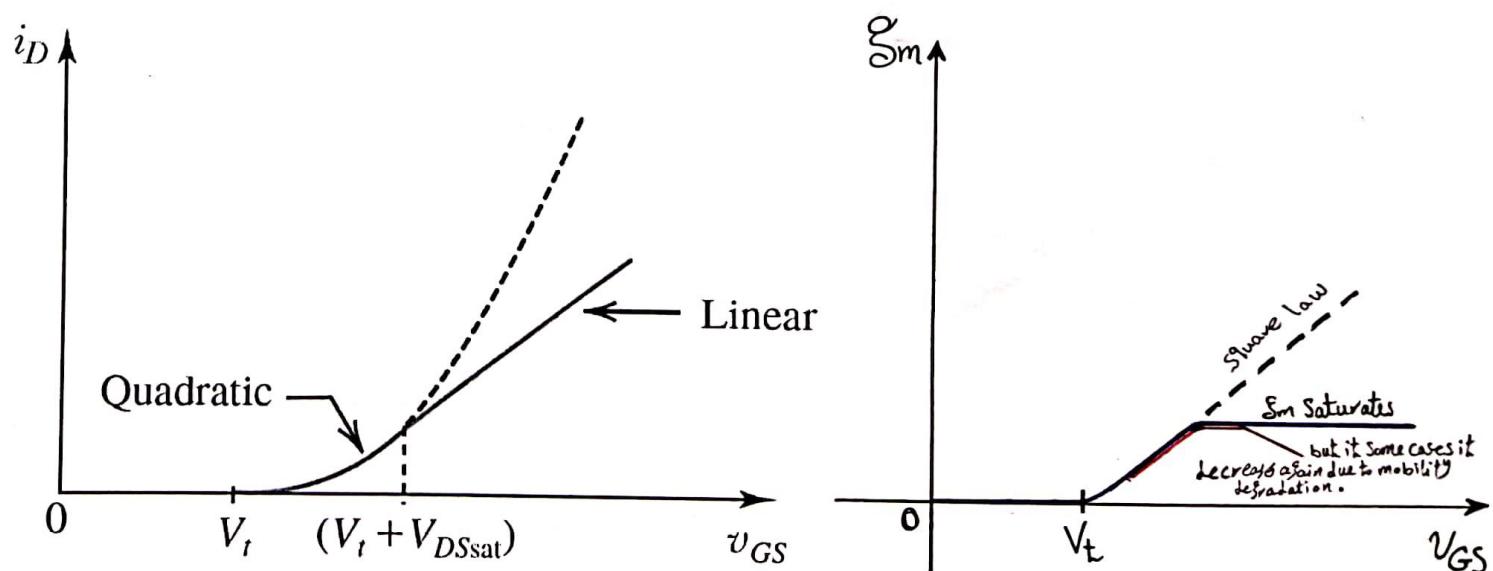
$$V_{DS} \uparrow V_A \uparrow$$



\*  $V_{GS} \uparrow - [I_D \uparrow]$   
• take Both in the same direction  
 $V_{SB} \uparrow - [I_D \downarrow]$   
 $V_{BS} \uparrow - [I_D \uparrow]$

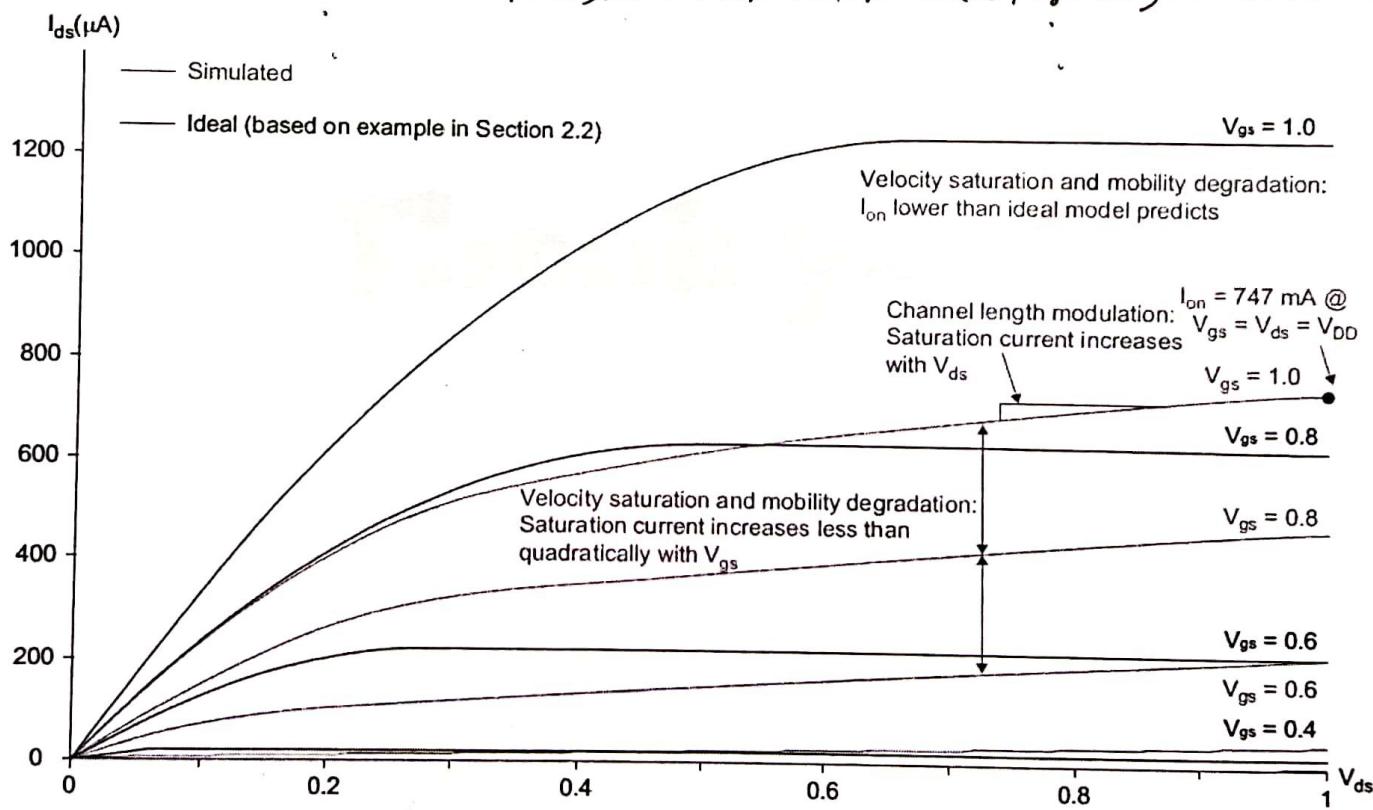
# Short Channel Effects: Velocity Saturation

- ID-VGS quadratic:  $g_m = \frac{\partial I_D}{\partial V_{GS}} = \text{linear} \rightarrow g_m \text{ increases with } V_{GS}$  (long channel Model)
- ID-VGS linear:  $g_m = \frac{\partial I_D}{\partial V_{GS}} = \text{constant} \rightarrow g_m \text{ saturates}$  (Velocity Saturation)



# Short Channel Effects: CLM and DIBL

- ID-VDS horizontal: no  $V_{DS}$  dependence  $\rightarrow$  ideal current source
- More slope  $\rightarrow$  more  $V_{DS}$  dependence  $\rightarrow$  smaller  $r_o$   $\rightarrow$  smaller gain (short channel devices)
  - for higher output resistance ( $r_o$ ) use long channel devices.



# **Thank you!**

Dr. Hesham Omran's Lectures  
Fady Sabry Negm's Notes

## References

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- A. Sedra and K. Smith, "Microelectronic Circuits," Oxford University Press, 7<sup>th</sup> ed., 2015.
- B. Razavi, "Fundamentals of Microelectronics," Wiley, 2<sup>nd</sup> ed., 2014.
- B. Razavi, "Design of Analog CMOS Integrated Circuits," McGraw-Hill, 2<sup>nd</sup> ed., 2017.
- N. Weste and D. Harris, "CMOS VLSI Design," Pearson, 4<sup>th</sup> ed., 2010.

وَمَا أُوتِيتُهُ مِنَ الْعِلْمِ إِلَّا قَلِيلًا

Allah almighty said in the Qur'an :  
“and you 'O humanity' have been given but little knowledge.”

لَوْأَنَ النَّاسَ كُلُّهُمْ سَتَّصِبُوا بِأَمْرٍ اتَرَكُوهُ مَا قَامَ لِلنَّاسِ وَنِيَّا وَلَوْعَنْ

The Muslim Caliph Umar ibn Abdulaziz, May Allah have mercy on him, Said:  
“If every time people found something difficult, they abandoned it, then  
neither worldly affairs nor religion would have ever been established for  
people.”

- All Credits for these lectures go to **Dr. Hesham Omran**, Associate Professor at Ain Shams University and CTO at Master Micro, May Allah bless Dr. Hesham for these Lectures.

<https://www.master-micro.com/professional-courses/analog-ic-design>



- These Notes were made by: **Fady Sabry Negm** and any success or guidance is from Allah, and any mistake or lapse is from me and Satan.